



# Expanding Capabilities of Traditional Cantilever Technology To Meet Advanced DRI Fine Pitch Probe Requirements

**Presenter: Bill Williams, Freescale Semiconductor  
FMTC Probe Mgr. and Sr. Member Technical Staff  
Final Manufacturing Technology Center**

**Co-Authors: Tony Angelo, S.S. Yan, Al Ferguson, Susan Downey, Alvin Youngblood**

**SWTW 2004  
June 8, 2004**



# Objectives / Outline:

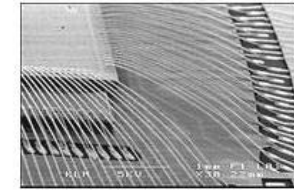
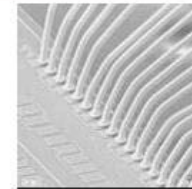
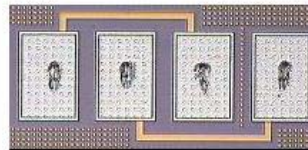
- **Freescale Semiconductor is a subsidiary of Motorola**
- **Recap 2003-SWTW paper on 44um Fine Pitch Probe**
  - High level Review of FMTC Assessment Process & Methods
  - Review POP technology (Probe Over Passivation)
- **Highlights 3 major new Probe Thrusts**
  - 37um Fine Pitch Developments – Standard Single Row Inline
  - What is Fine Pitch DRI? Why does it exist? What are the issues?
  - 57um Dual Row Inline (DRI) Developments
  - 47um Dual Row Inline (DRI) Developments (\*\* beyond design rules!)
  - Share Comparative Results from Development Thrusts
- **Conclusions, Future Work & Thanks!**

# Title Page - Recap of 2003 SWTW Paper

## A 44 $\mu$ m Probe Process Characterization and Factory Deployment Using Probe-Over-Passivation

**Presenter:** *Bill Williams, Motorola FMTC Probe Mgr. & Sr. MTS*

**Co-Authors:** *Tony Angelo, S.S. Yan, Tu-Anh Tran, Stephen Lee, Matt Ruston*



### Today's Objectives:

- 1.) Motorola's FMTC Probe Technology Development Methods
- 2.) 44 $\mu$ m Probe Characterization Process Used by FMTC
- 3.) Introduce POP (Probe-Over-Passivation): What is it? & Why needed?
- 4.) 44 $\mu$ m Technology Transfer to Factory Flow & Results Highlights
- 5.) "3 Key Lessons Learned": a.) Cleaning, b.) Prober set-up, and c.) Needle Alignment OD
6. Acknowledgements of Key 44 $\mu$ m Probe Project Participants

Are Your Seat Belts Fastened?  
This will be **FAST**

A 44 $\mu$ m Probe Process Cx and Factory Deployment using Probe-Over-Passivation  
SWTW-2003 Long Beach, Bill Williams Motorola FMTC  
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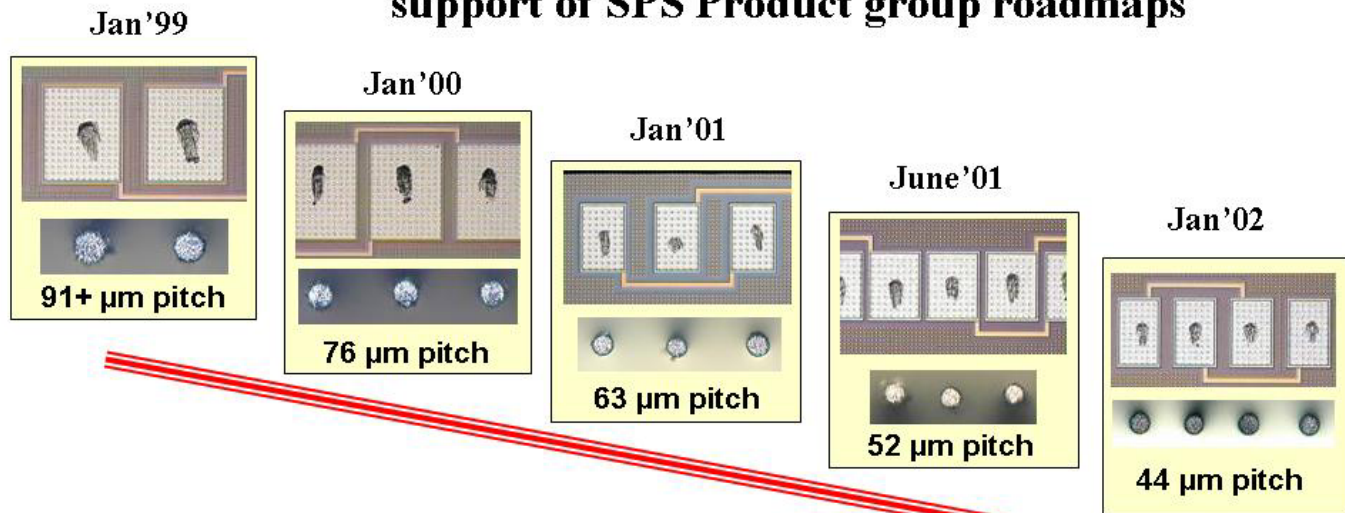


Recap From  
2003-SWTW

# Goals Page - Recap of 2003 SWTW Paper

Recap From  
2003-SWTW

**Primary FMTC Team Goal: Drive advancements in probe technology to enable silicon shrinks in support of SPS Product group roadmaps**



**Secondary Team Goal:**

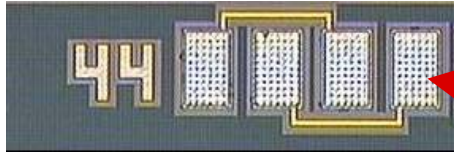
**Accelerate improvements in Motorola's wafer probe position to move from "tied for dead-last" in late '98 to a position of one of the Industry Leaders by '2002 for Fine Pitch Probe!**

This Was My  
Team's True  
Motivation

A 44μm Probe Process Cx and Factory Deployment using Probe-Over-Passivation  
SWTW-2003 Long Beach, Bill Williams Motorola FMTC  
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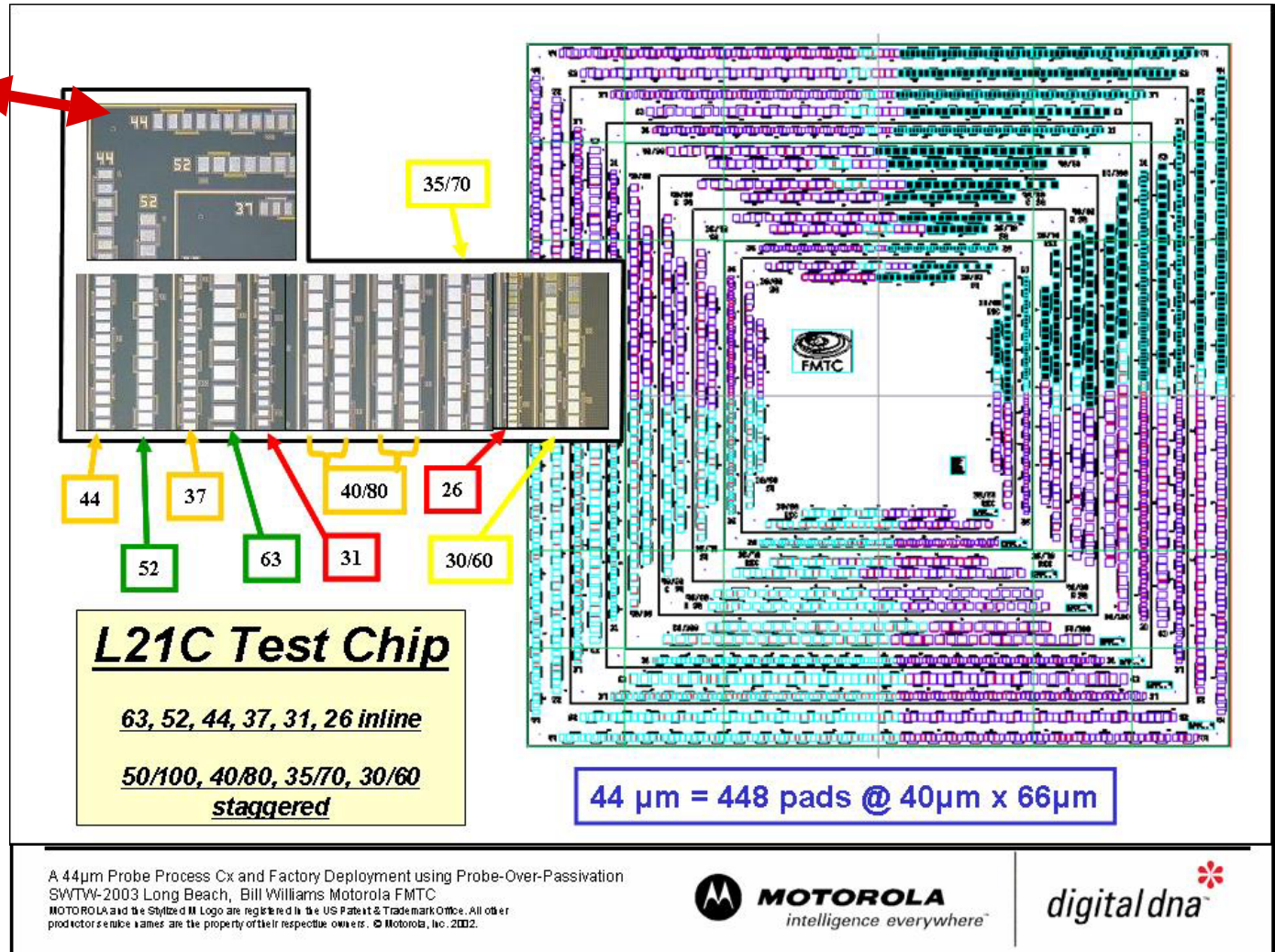


# L21C Test Chip - Recap of 2003 SWTW Paper



Note the pads are “daisy chain”

Recap From  
2003-SWTW



# FMTC Fine Pitch Probe Process Development:

## Fine Pitch, Alum Capped Copper and Direct Probe on Copper *Recap info*

44 μm L21C Probe Card Docs

**Recap From 2003-SWTW**

PROJEC#	44 μm L21C Probe Card Docs
PROJ DATE	12/17/2003
PROJ NAME	44 μm L21C Probe Card Docs
PROJ MGR	BILL WILLIAMS
PROJ STATUS	COMPLETED
PROJ DESCRIPTION	44 μm L21C Probe Card Docs
PROJ OBJECTIVES	
PROJ SCOPE	
PROJ RISK	
PROJ CHALLENGES	
PROJ DELIVERABLES	
PROJ BUDGET	
PROJ TEAM	
PROJ CONTACTS	
PROJ HISTORY	
PROJ STATUS HISTORY	
PROJ CHANGES	
PROJ VERSIONS	
PROJ APPROVALS	
PROJ COMMENTS	
PROJ FILES	
PROJ DOCUMENTS	
PROJ IMAGES	
PROJ LINKS	
PROJ TOOLS	
PROJ HELP	

2.) Prep DOE and Probe Wafers

**L21C**

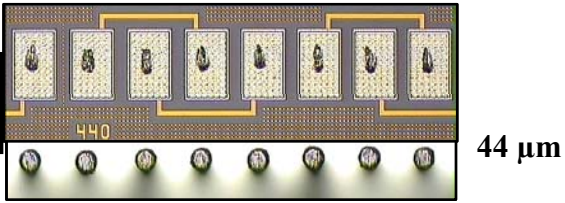
Wfr	MRET	MRET	MRET	MRET	MRET
Wfr	MRET	MRET	MRET	MRET	MRET

**MRET "Marks-Resistance Edge Test"**

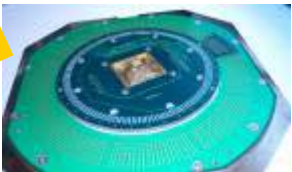
44 μm L21C Probe Card Docs

3.) Photo doc Scrubs & Probes

~ 1600 Photos/5Wfr-DOE

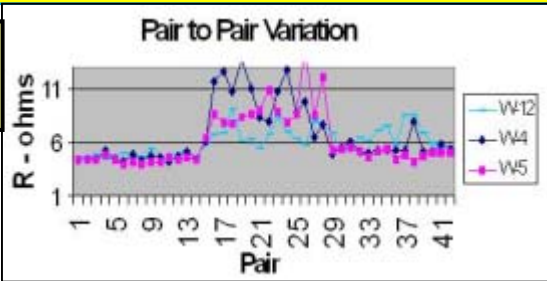


Step 1.) Define & Obtain Probe Hardware



Step 4.) Electrical Test & Contact Resistance

~125K resistance data points / L21C wafer

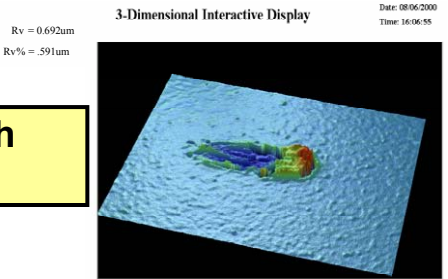


5.) Scrub Size & X-Y Cpk



Scrub Length Process Capability	Cp	Cpk	Mn Pad L	Delta Pad
25 Deg-C-S	1.33	1.17	120	+24
25 Deg-C+S	1.84	1.58	92	-4
135 Deg-C-S	1.06	0.67	171	+75
135 Deg-C+S	1.80	1.55	94	-2

6.) 3-D Depth Analysis

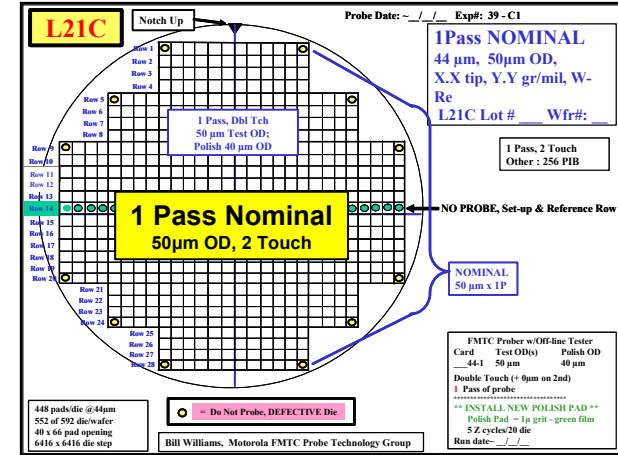
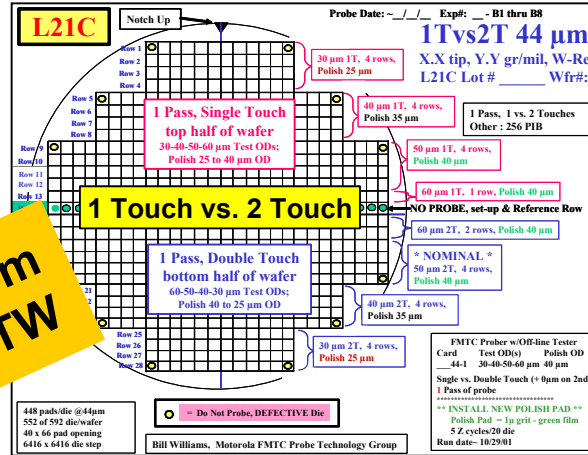
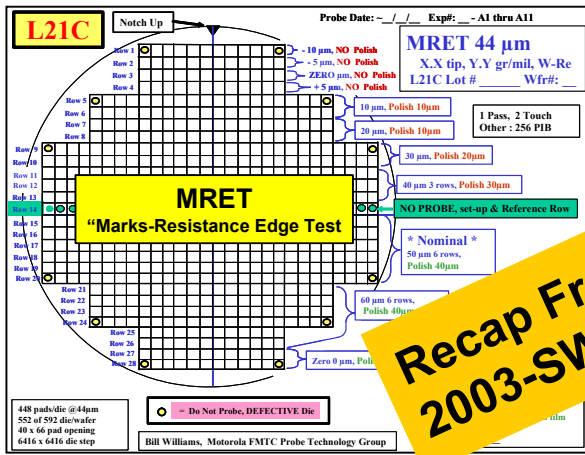


7.) Acid Dip Testing

7.) Transfer Recipe, Process & Methods to Production Site

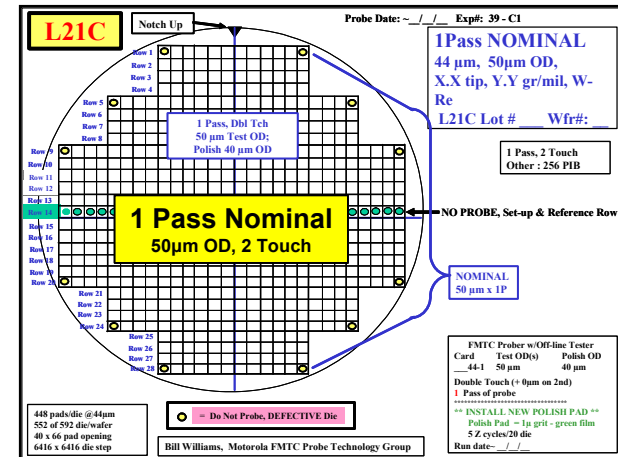
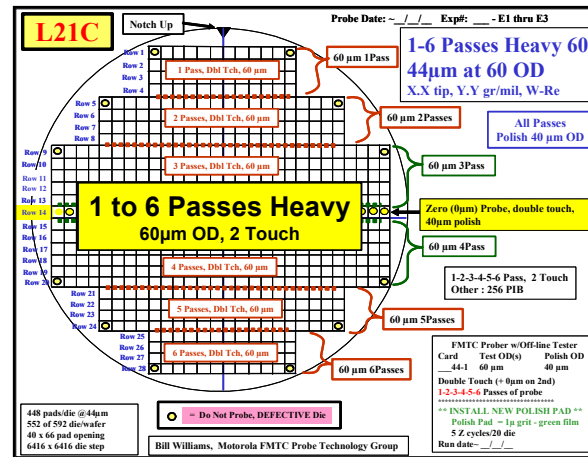
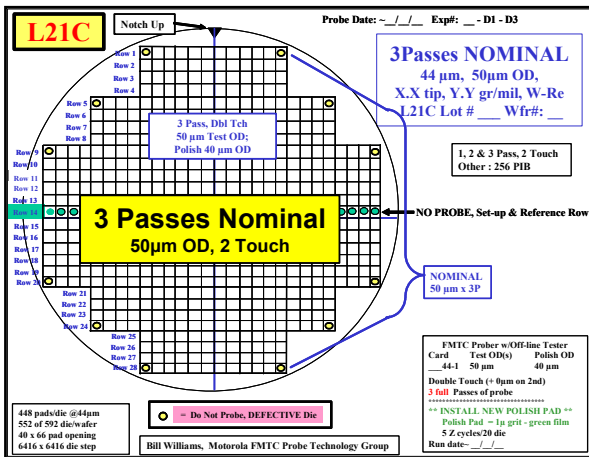
# 6 Wafer Maps: - Recap of 2003 SWTW Paper

## MRET, 1T vs. 2T, 1Pass Nominal, 3 Pass Nominal, 1-6Pass Heavy60, 1Pass Nominal



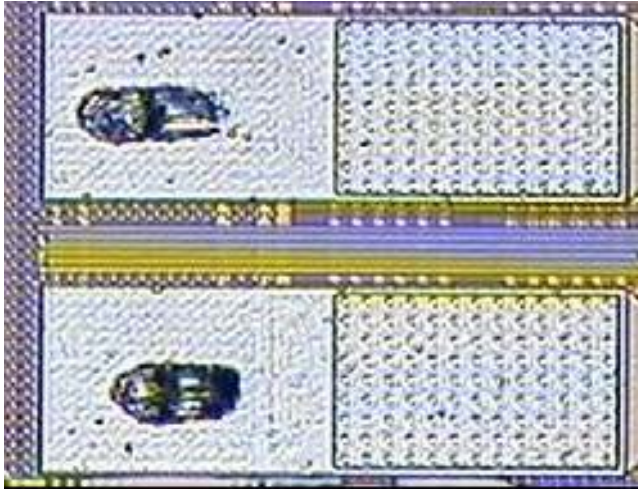
## MRET, 1T vs. 2T, 1Pass Nominal, 3 Pass Nominal, 1-6Pass Heavy60, 1Pass Nominal

**\*\* Then when successful = MANY Assembly Wafers of 1Pass & 3Pass**

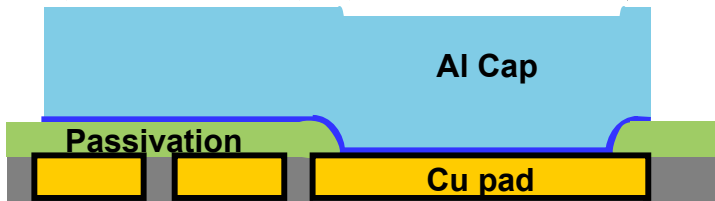




# What is POP? & Why Is It Used? – Recap of 2003



Probe Region      Wire Bond Region



**Probe Over Passivation Bond Pad**

**POP is “Probe-Over-Passivation”**

**We define it as a “Novel Method of Separating Probe and Wire Bond Regions Without Increasing Die Size”.**

**POP is easily implemented on Alum Capped Copper pad wafer designs and may be adapted to others.**

**POP saves die size!**

## **Benefits:**

- Eliminate Cu exposure due to heavy probe marks
- Eliminate probe and wire bond interference
- Create longer bond pad but DO NOT increase die size
- Requires 1 mask change: Al Cap
- Low cost solution
- Ease of implementation on existing and new products

## **Challenges:**

- Develop Probe Over Passivation (POP)
- Monitor passivation cracking risk

For a detailed paper on POP (Probe-Over-Passivation) see an ECTC-2003 paper titled “Novel Method of Separating Probe and Wire Bond Regions Without Increasing Die Size” by Tu Anh Tran, Lois Yong, Stephen Lee, Bill Williams and Jody Ross

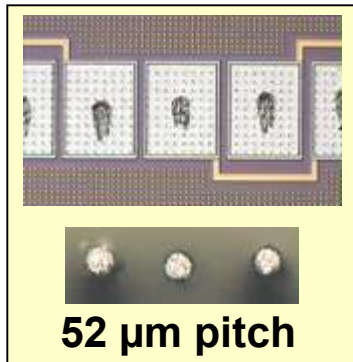


## 37um Fine Pitch Development Updates

# 37um Fine Pitch Thrust Development

**Primary FMTC Team Goal: Drive advancements in probe to enable silicon shrinks in support of Freescale's Product group roadmaps**

June'01

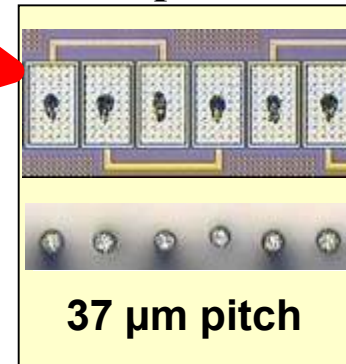


Jan'02



**Roadmap timeframe limited by Assembly Wirebond Capability???**

Sept'03



31um Probe

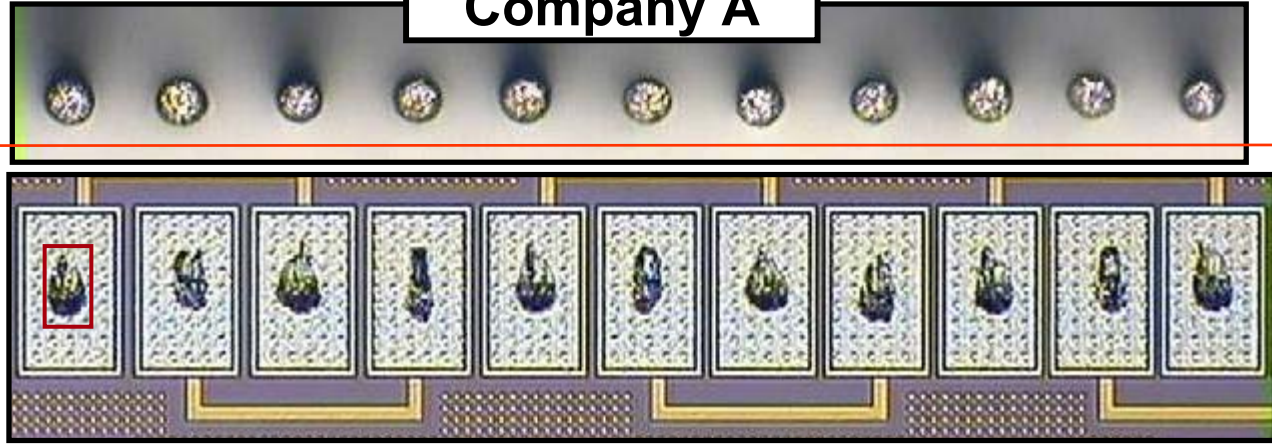
?when?,  
not if!


## Secondary Team Goal:

**Continue improvements in Freescale's wafer probe position to remain at, or near, the Industry Leadership for Fine Pitch Probe!**

# 37um Comparisons: Probe Pin “free state” and Scrub Alignment

**Company A**

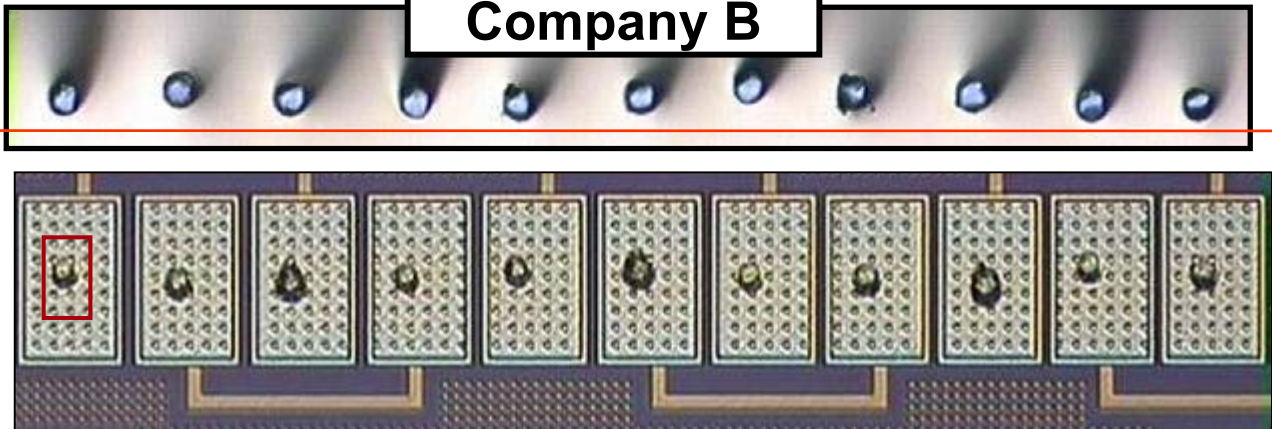


 = 1/4 of pad  
Scrub Limit

## Key Responses:

- Probe Alignment X-Y-Z
- Tip Diameter & Shape
- Scrub Placement
- Scrub Uniformity
- Scrub Size <25%

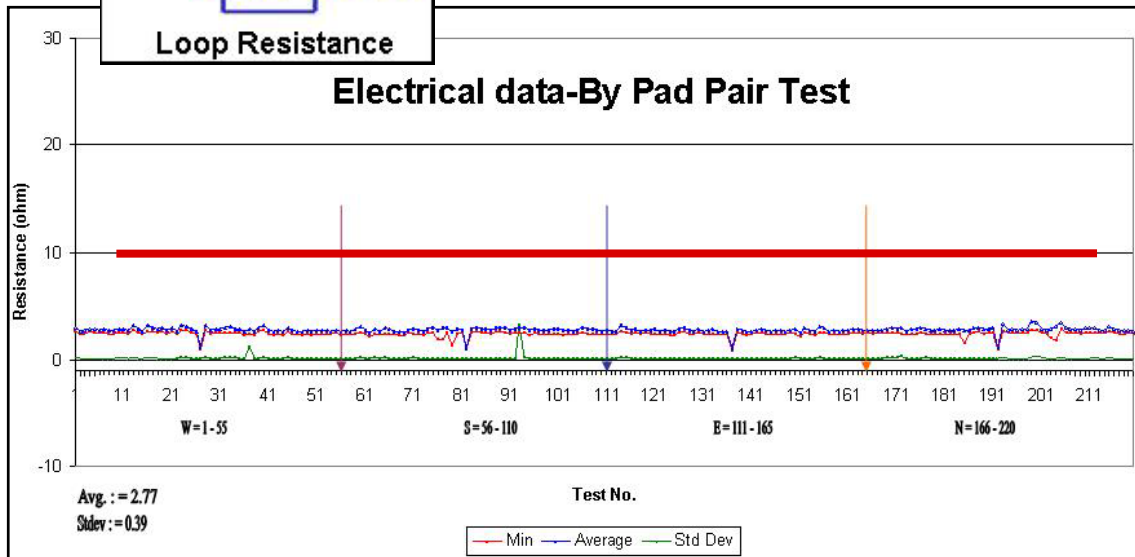
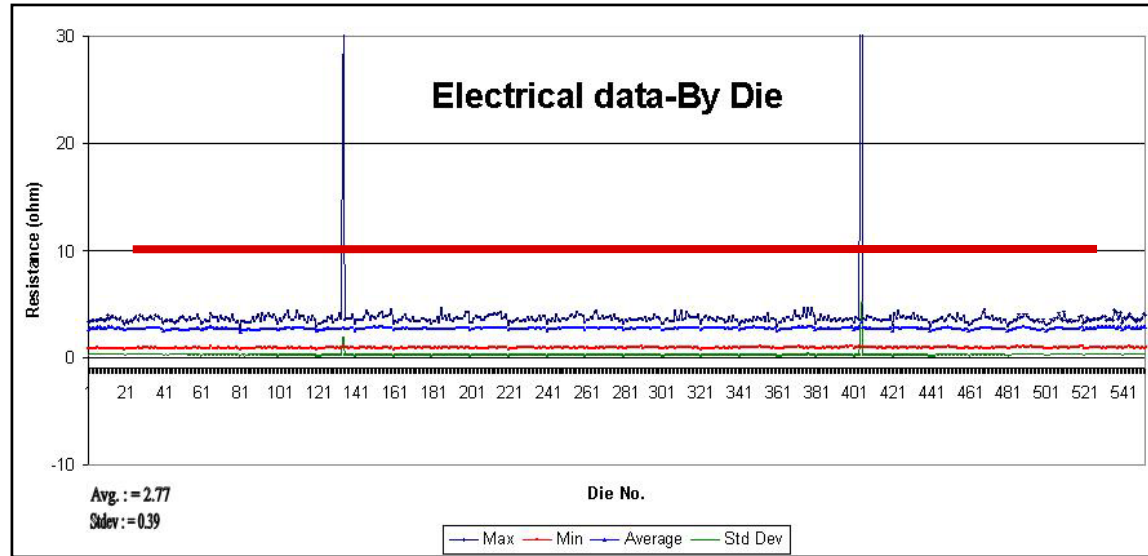
**Company B**



# 37um Comparisons: Electrical by Pad Pair and by Die

## Electrical Responses:

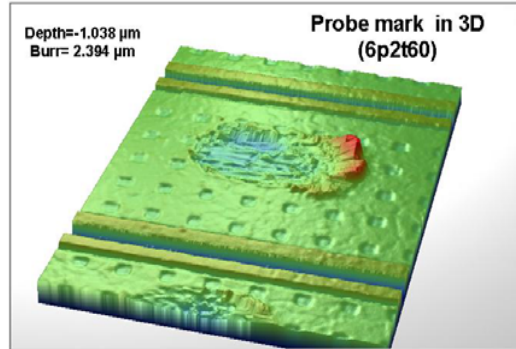
- **Loop Res < 10ohm = Good**
- Pad Pair Res & Std Dev.**
- Die Res Max >10 = Fail die**
- Die Res Min, Ave, Std Dev.**



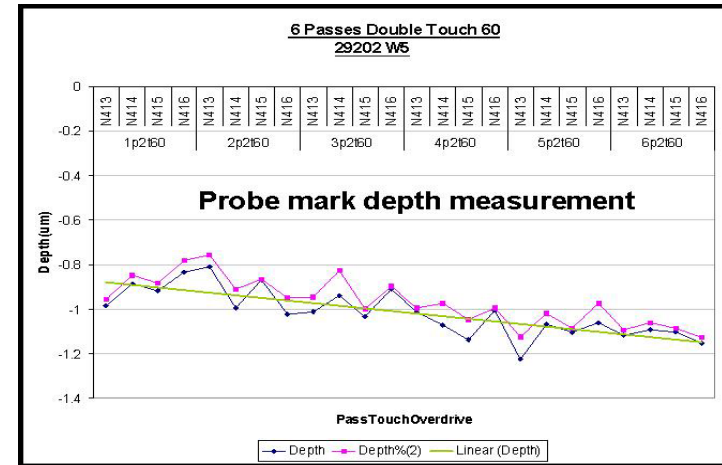
**“Best In Class”  
Electrical Results  
~ Company B**

# 37um Scrub Cpk and Scrub Depth Comparisons

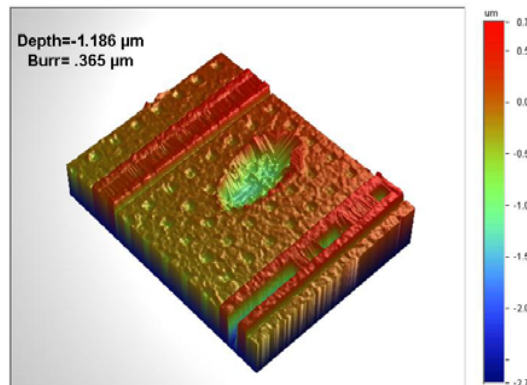
1 Pass Nominal  
 Y-Cpk = 1.80  
 X-Cpk = .78



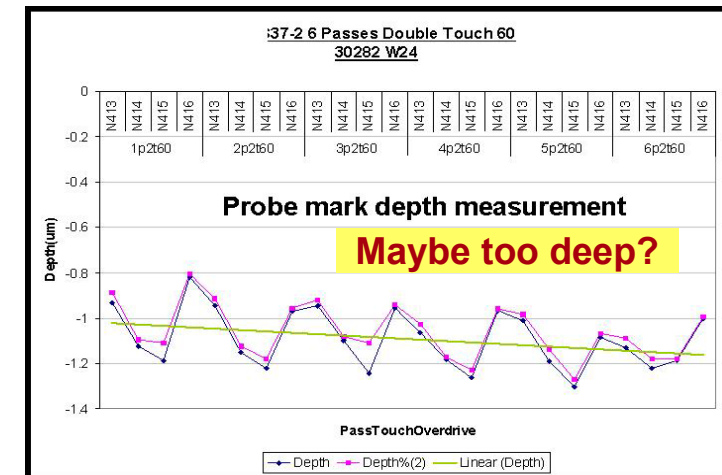
**Company A**



1 Pass Nominal  
 Y-Cpk = 1.92  
 X-Cpk = .90



**Company B**

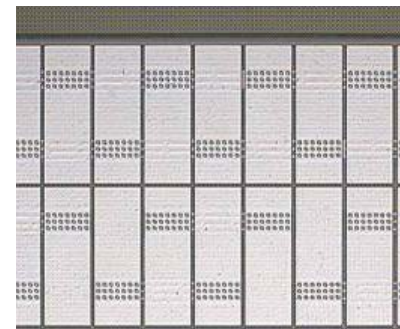
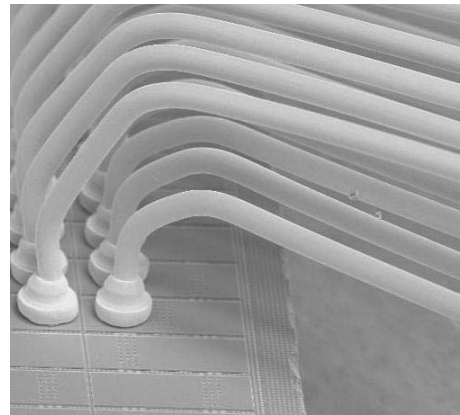
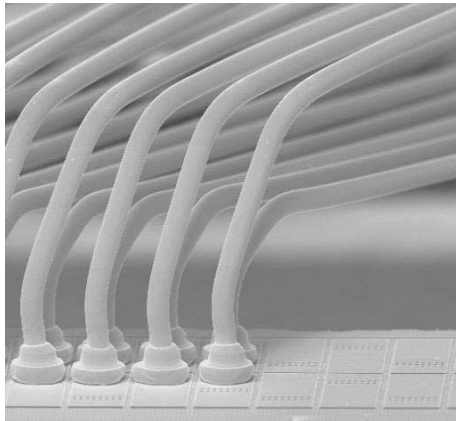
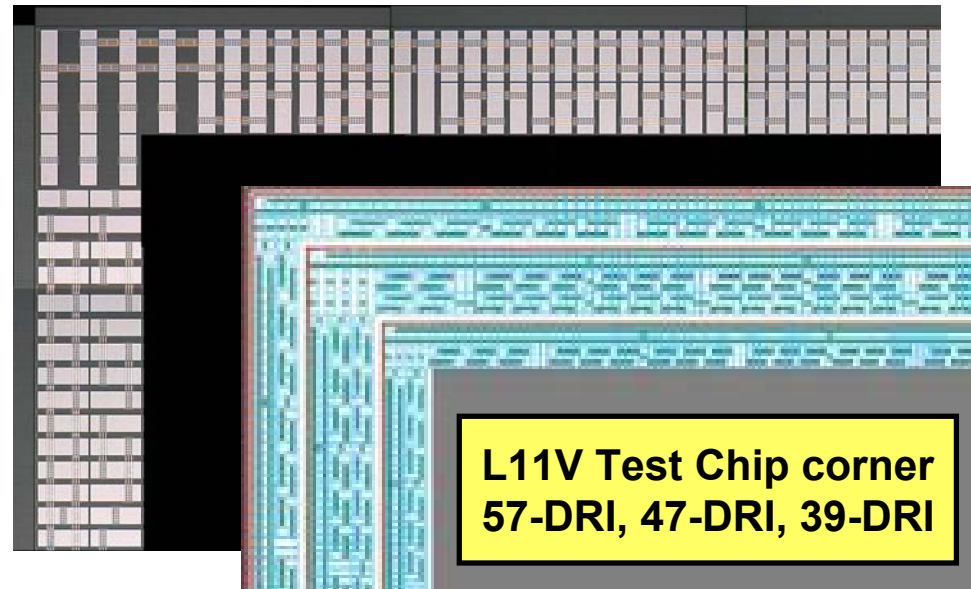
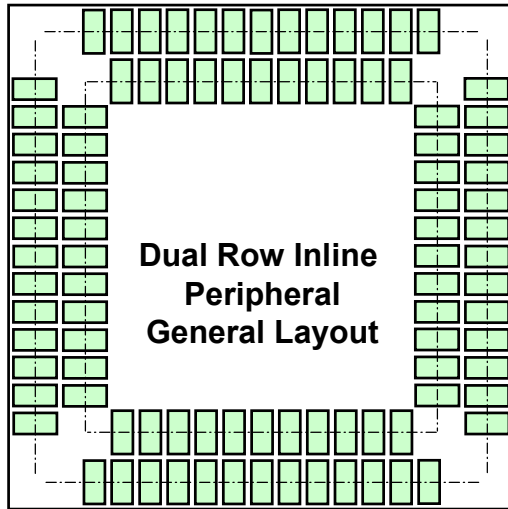




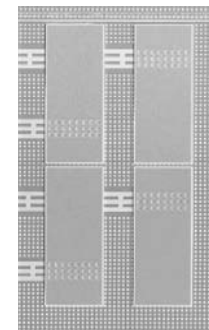
**What is Fine Pitch DRI? Why does it exist? What are the issues?**  
**57um Dual Row Inline (DRI) Developments**  
**47um Dual Row Inline (DRI) Developments (\*\* beyond design rules!)**  
**Share Comparative Results from Development Thrusts**

## **57um & 47um Dual Row Inline (DRI) Probe Development**

# What is Fine Pitch Dual Row Inline? & Why?



47um Dual Row Inline  
Fine Pitch Pads  
44um x 130um



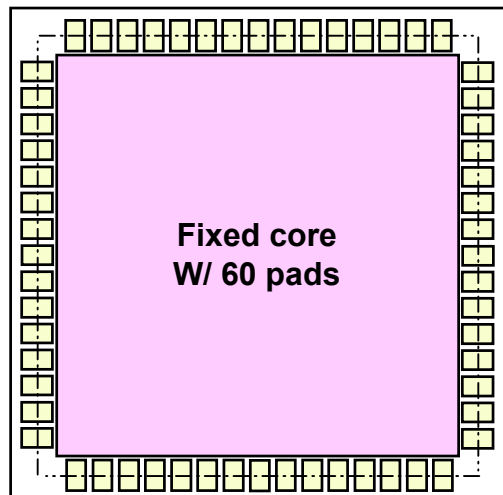


# Design Comparisons

## What is Fine Pitch Dual Row Inline? & Why?

### Ultra Fine Pitch

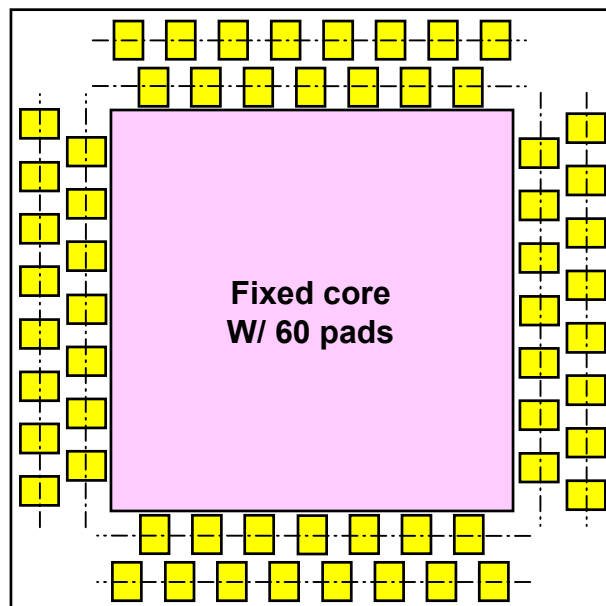
- **Beyond Wirebond Capability**
- **Difficult Probe Card Design**
  - Small Dia wire & Probe tips
  - Beyond metrology equip.



**“Ultra Fine Pitch”**  
Single Row  
Peripheral Inline  
i.e. 44um or 37um

### Non-POP 2 Row Staggered

- Good Wirebond Capability
- Good Probe Card Design
  - Large Dia wire & Probe tips
- **Increases die size !!!!!!!**

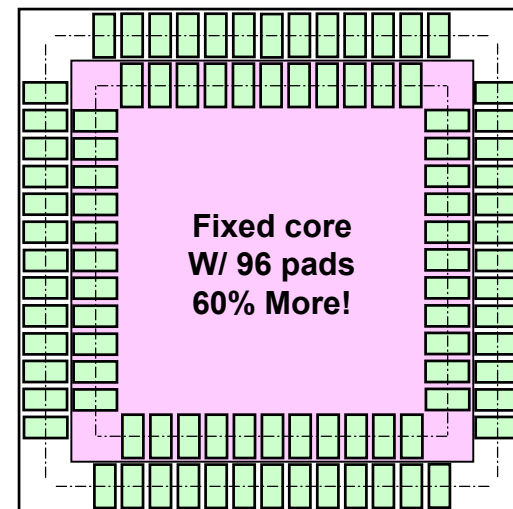


**“Moderate Fine Pitch”**  
2 Row w/ Offset  
Dual Row Staggered  
Peripheral

i.e. 45/90 or 40/80 or 35/70

### POP 2 Row Inline (DRI)

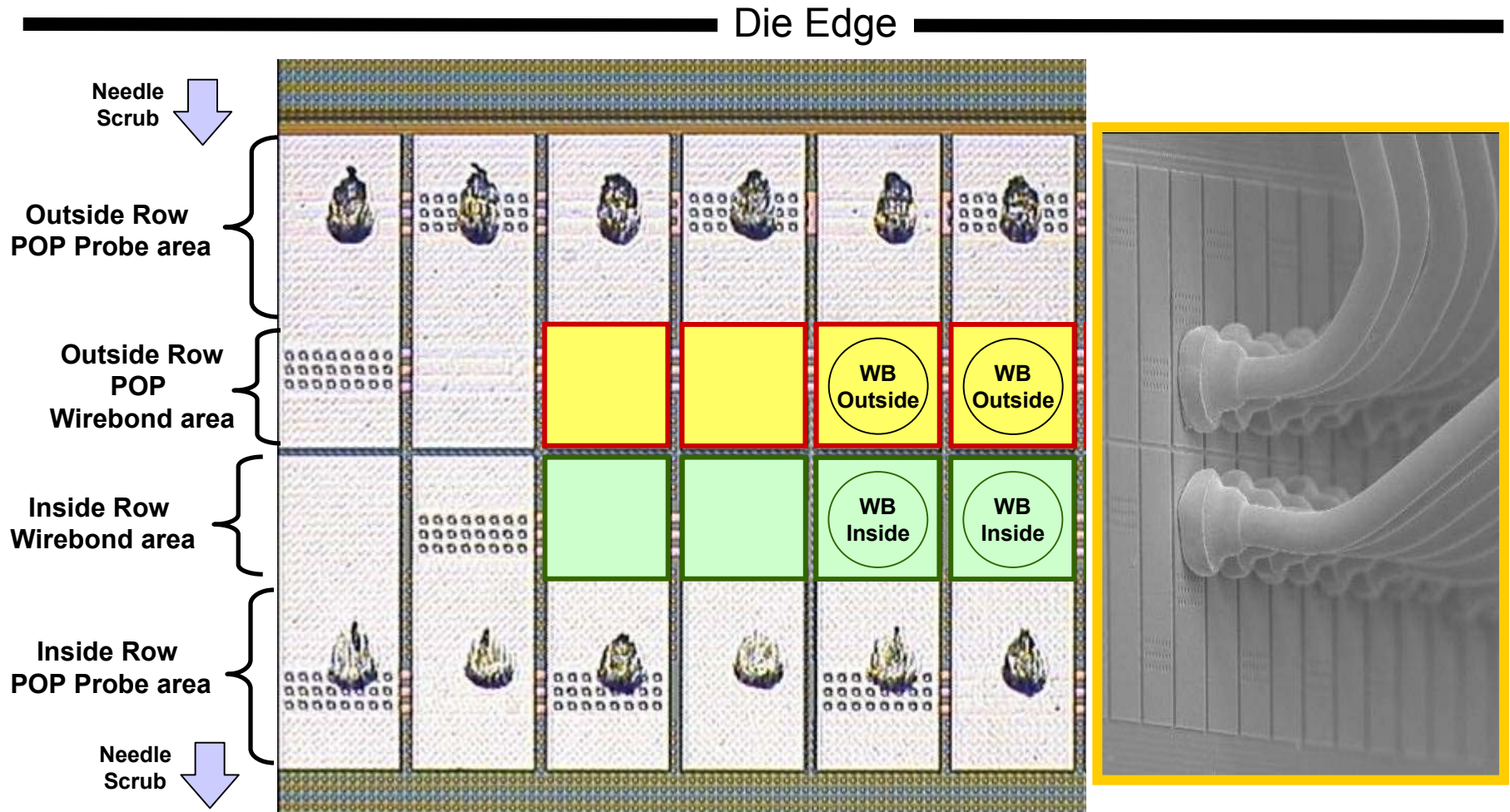
- Good Wirebond Capability
- Fair Probe Card Design
  - Medium Dia wire & Probe tips
- **Slight die size increase!**
- Staggered is optional



**“Very Fine Pitch”**  
2 Row Zero Offset  
Dual Row Inline  
Peripheral

i.e. 57umDRI or 47umDRI

# Dual Row Inline With Probe Over Passivation (POP) POP Designated Wirebond areas vs. Probing areas



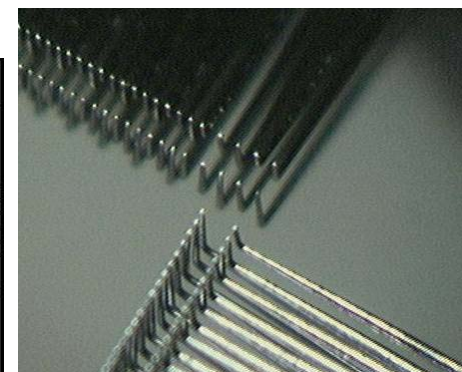
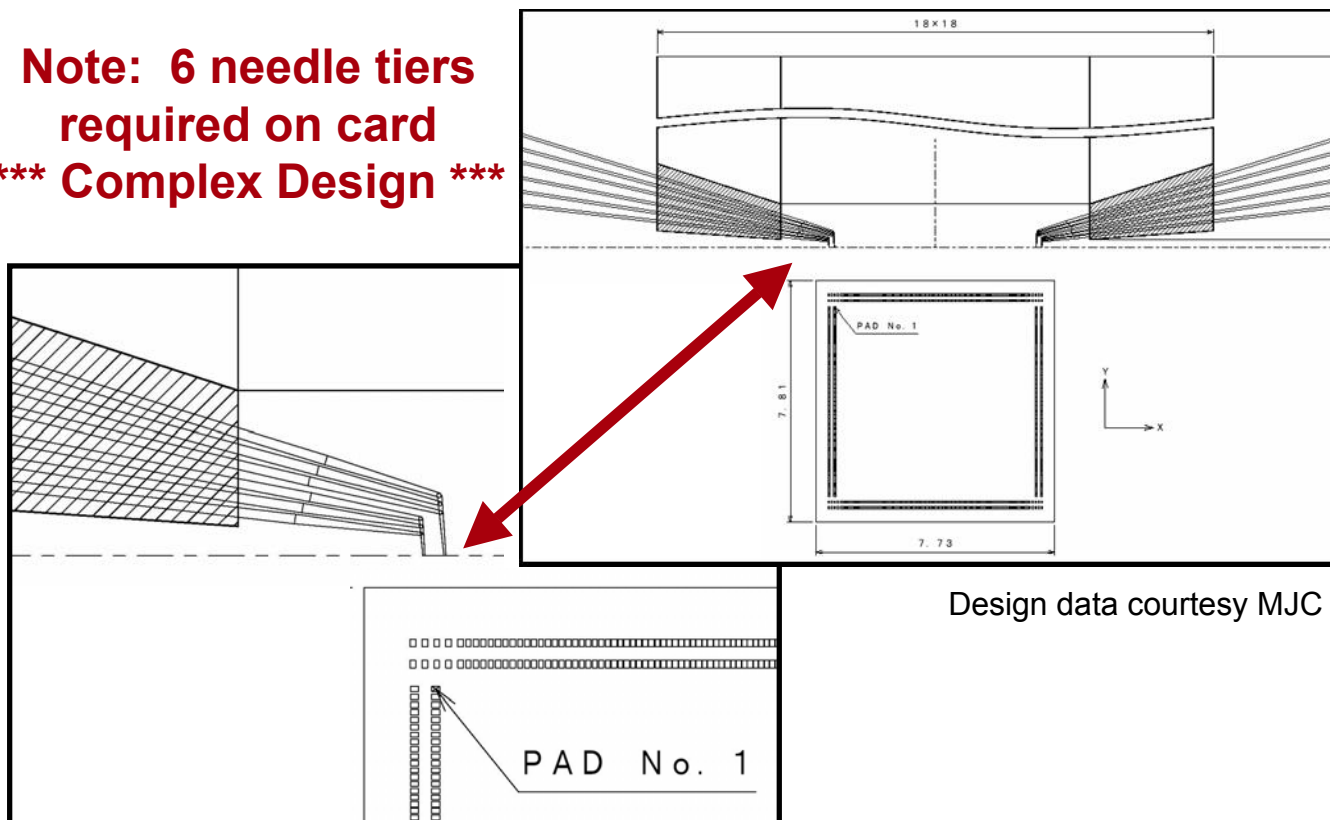
**Note: Scrubs are from 6-Passes of Heavy Double Touch probe on 57um DRI**

# Design Issue: Complexity of Dual Row Inline (DRI)

57um DRI = 880 pads  
47um DRI = 968 pads

Serious example of  
10# of Stuff in a 5# Bag!

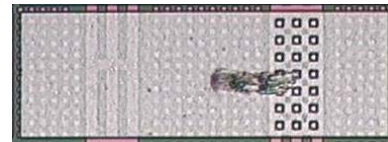
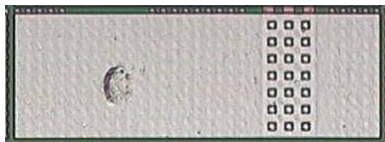
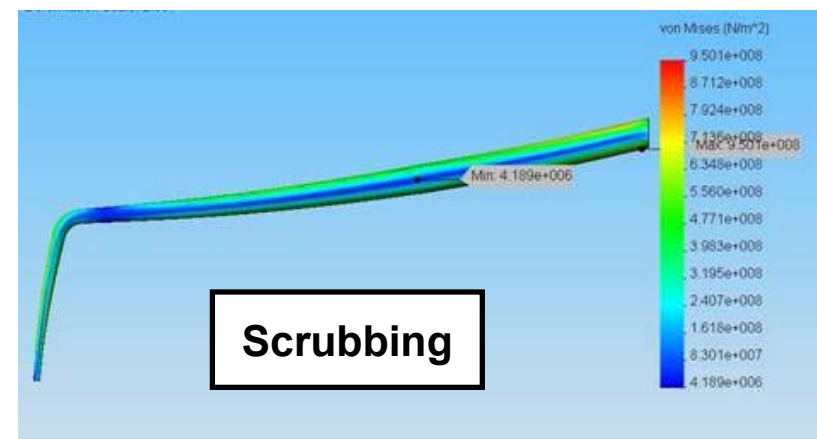
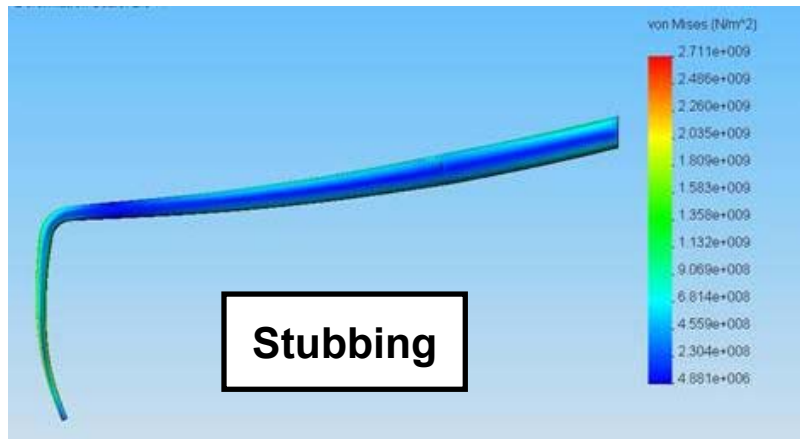
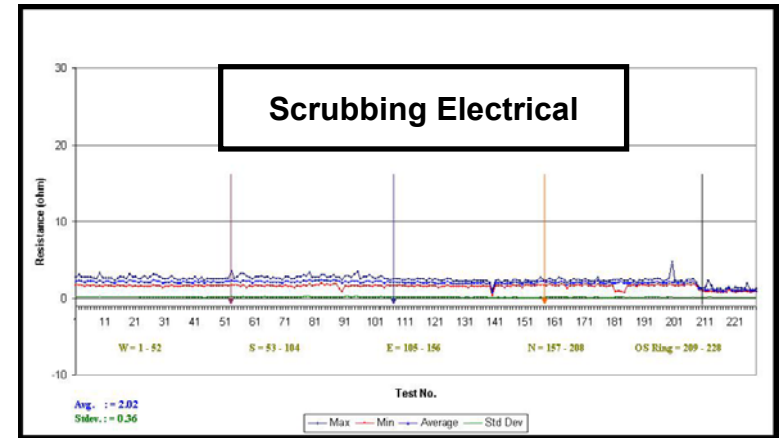
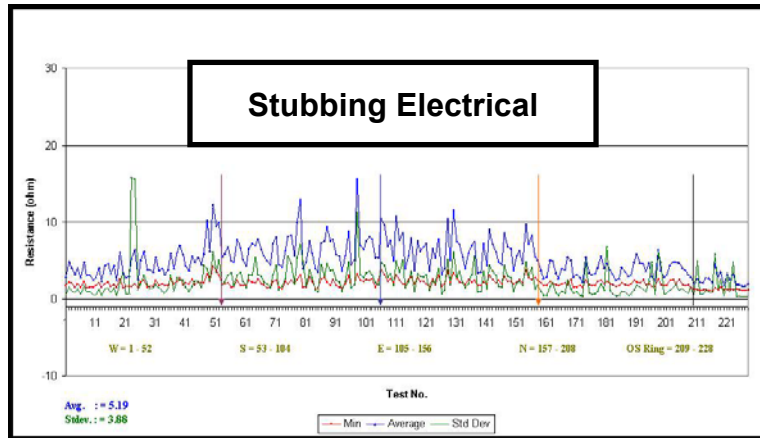
Note: 6 needle tiers  
required on card  
\*\*\* Complex Design \*\*\*



Design data courtesy MJC

# Design Issue: Stubbing vs. Scrubbing Probes

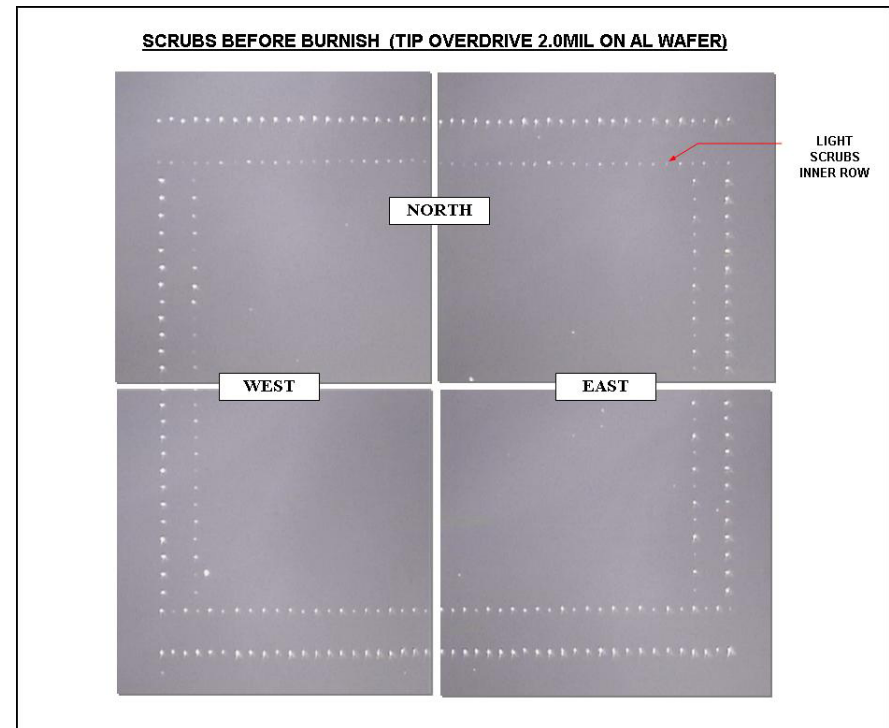
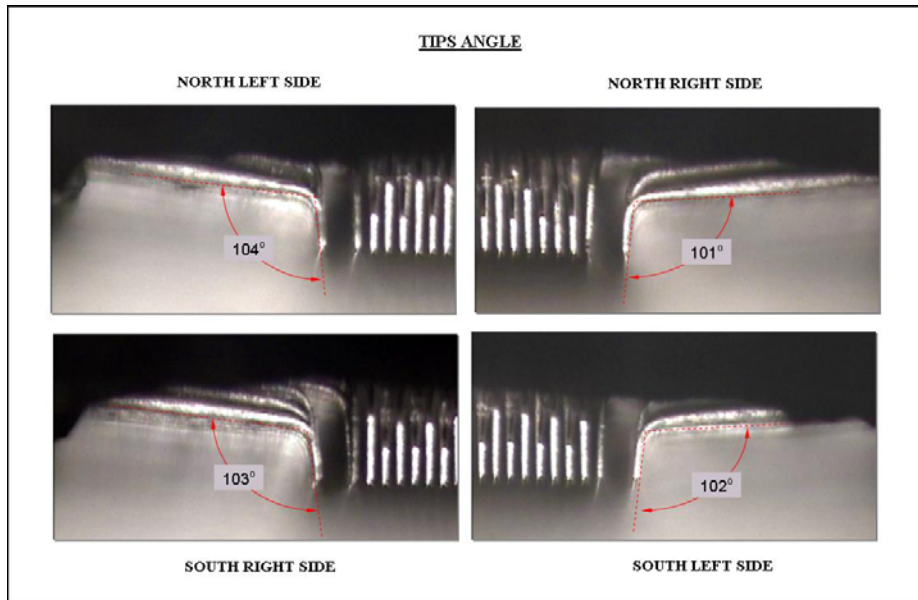
## High Tier count increases tip length = risk of stubbing too much!



FEA courtesy of Probe Logic

# Dual Row Inline (DRI) Design Issues: Balanced Contact Force

Numerous Needle Layers combined with necessary long tip lengths and various approach angles causes serious issues controlling Balanced Contact Force (BCF).

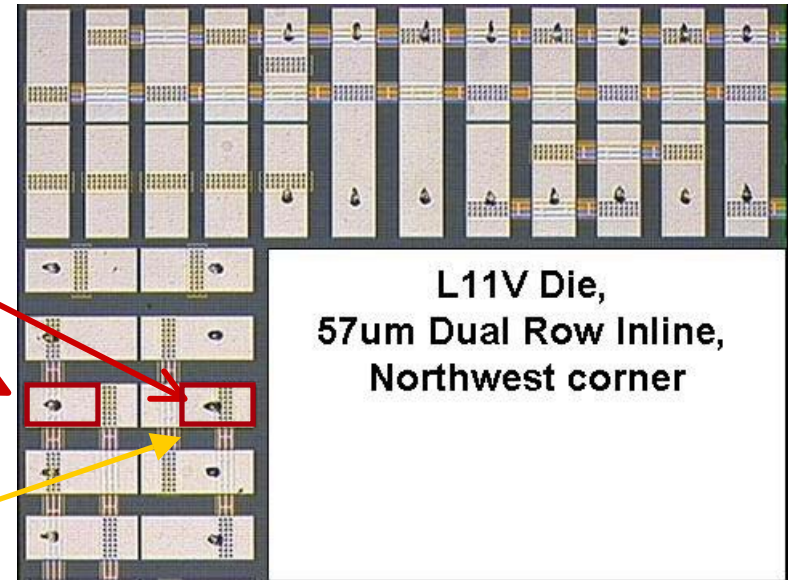
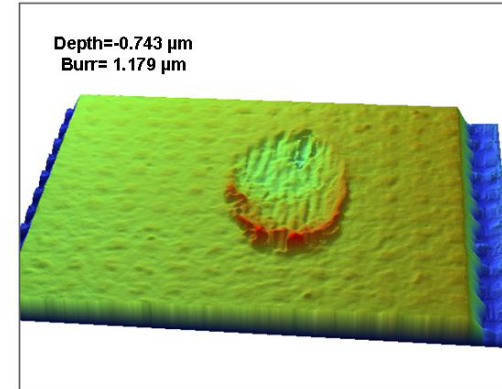
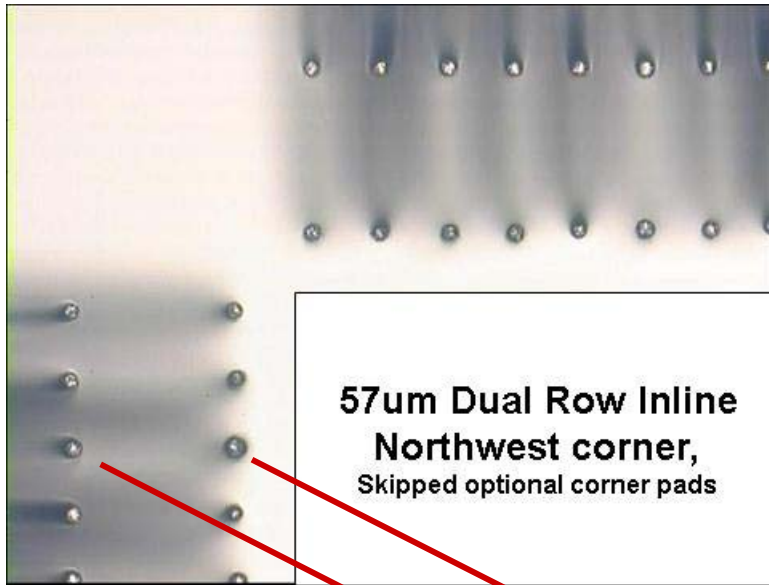


Pin Angle analysis vs. Scrub courtesy of K&S

# PROBE RESULTS

# L11V Test Vehicle

**Dual Row Inline-57P Probed Pads** 1pass 50 OD Nominal Probe card

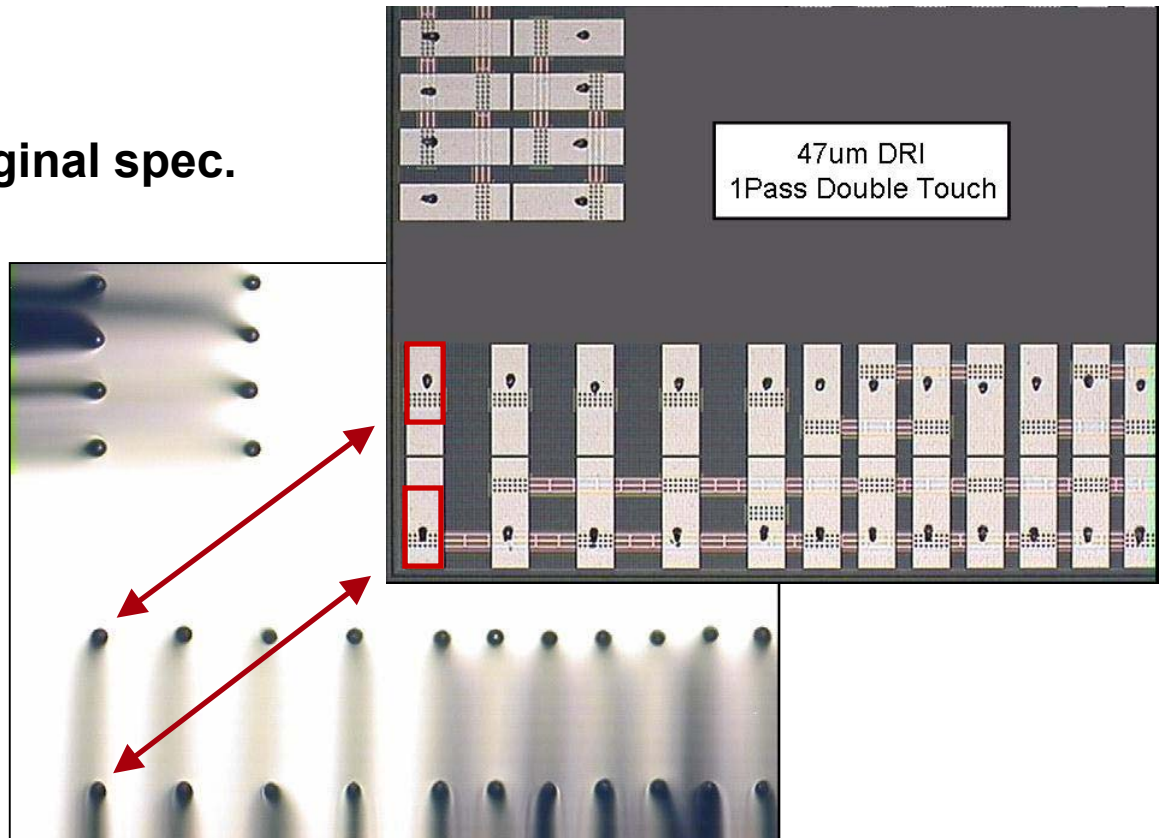


Y-Capability	
<b>Outer Row</b>	
Y Capability I57DRI.1 65952 w10 1-Pass 50 Outer Row 10/01/03	
PL=Passivation Length	78.00
SL=Average Scrub Width (PR ScrubY Size)	16.05
TE=Average Scrub Target Error (Top-Bot) / 2	3.63
SD <sub>SL</sub> =Std Dev of Scrub Length	5.10
SD <sub>TE</sub> =Std Dev of Target Error	3.29
$Y C_{PK} = (PL/2 - SL/2 - TE) / (3 * \sqrt{SD_{SL}^2 + SD_{TE}^2})$	1.50
$Y C_P = (PL - SL) / (6 * \sqrt{SD_{SL}^2 + SD_{TE}^2})$	1.70
<b>Inner Row</b>	
Y Capability I57DRI.1 65952 w10 1-Pass 50 Inner Row 10/01/03	
PL=Passivation Length	78.00
SL=Average Scrub Width (PR ScrubY Size)	19.32
TE=Average Scrub Target Error (Top-Bot) / 2	7.99
SD <sub>SL</sub> =Std Dev of Scrub Length	5.10
SD <sub>TE</sub> =Std Dev of Target Error	2.86
$Y C_{PK} = (PL/2 - SL/2 - TE) / (3 * \sqrt{SD_{SL}^2 + SD_{TE}^2})$	1.22
$Y C_P = (PL - SL) / (6 * \sqrt{SD_{SL}^2 + SD_{TE}^2})$	1.67

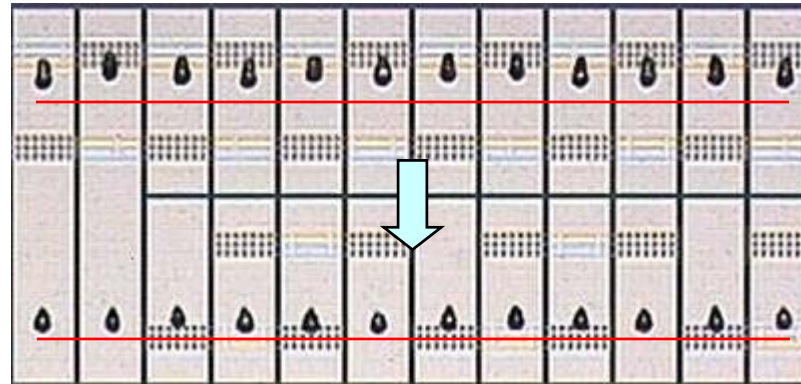
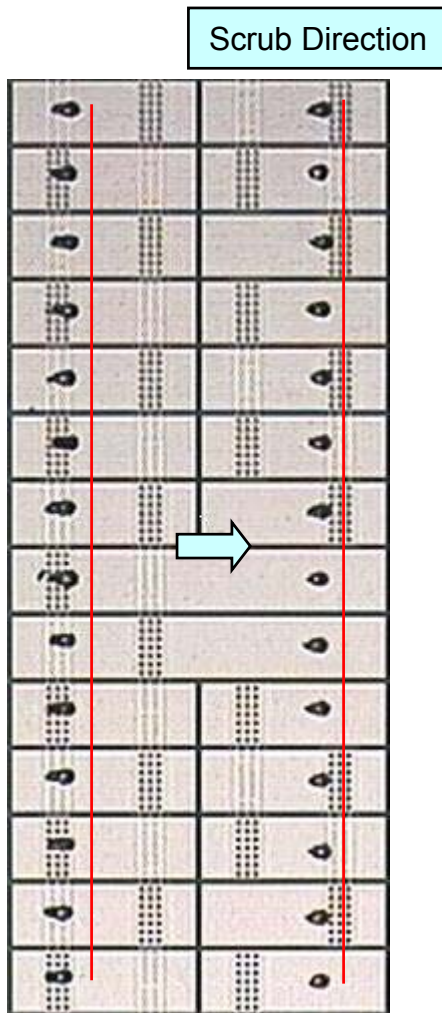
# Results: 47um Dual Row Inline ISSUES! & ISSUES! & ISSUES!

- The Dual Row “row to row spacing” combined with Fine Pitch caused design rule violations at suppliers
  - – so, we shared failure risk!
- Wire diameters reduced
- Pin Taper rates changed
- Tip Diameters reduced
- Resultant Force below original spec.
- Corner Keep-outs defined

- Outcome Results have been **FANTASTIC!**
- @ Multiple Suppliers!
- Limited data – much more to come!

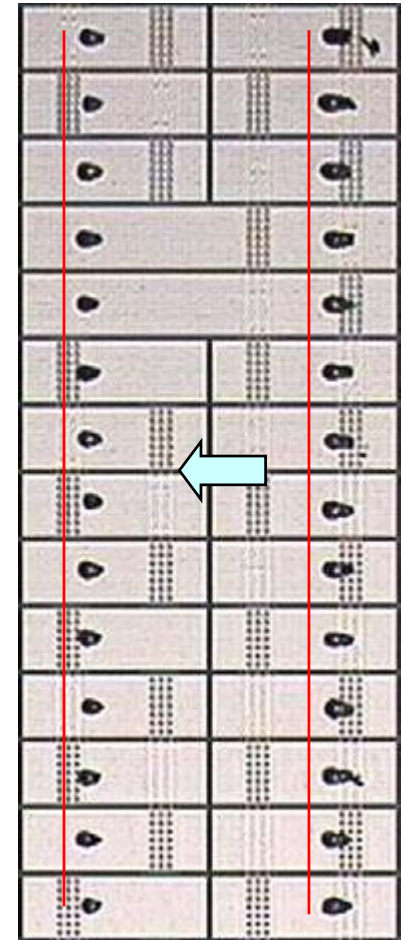
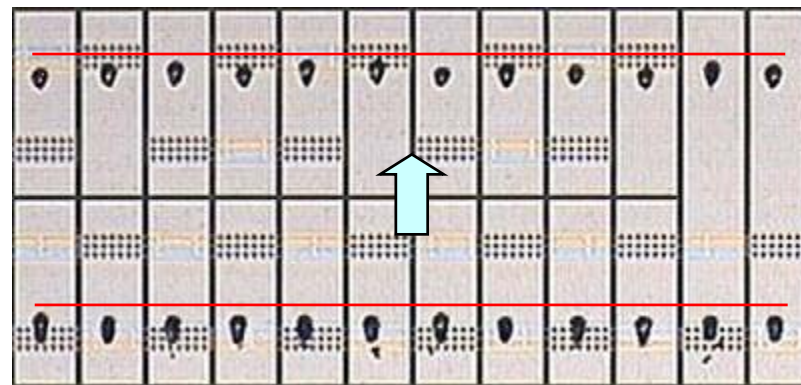


# Results: 47um Dual Row Inline – Scrubs



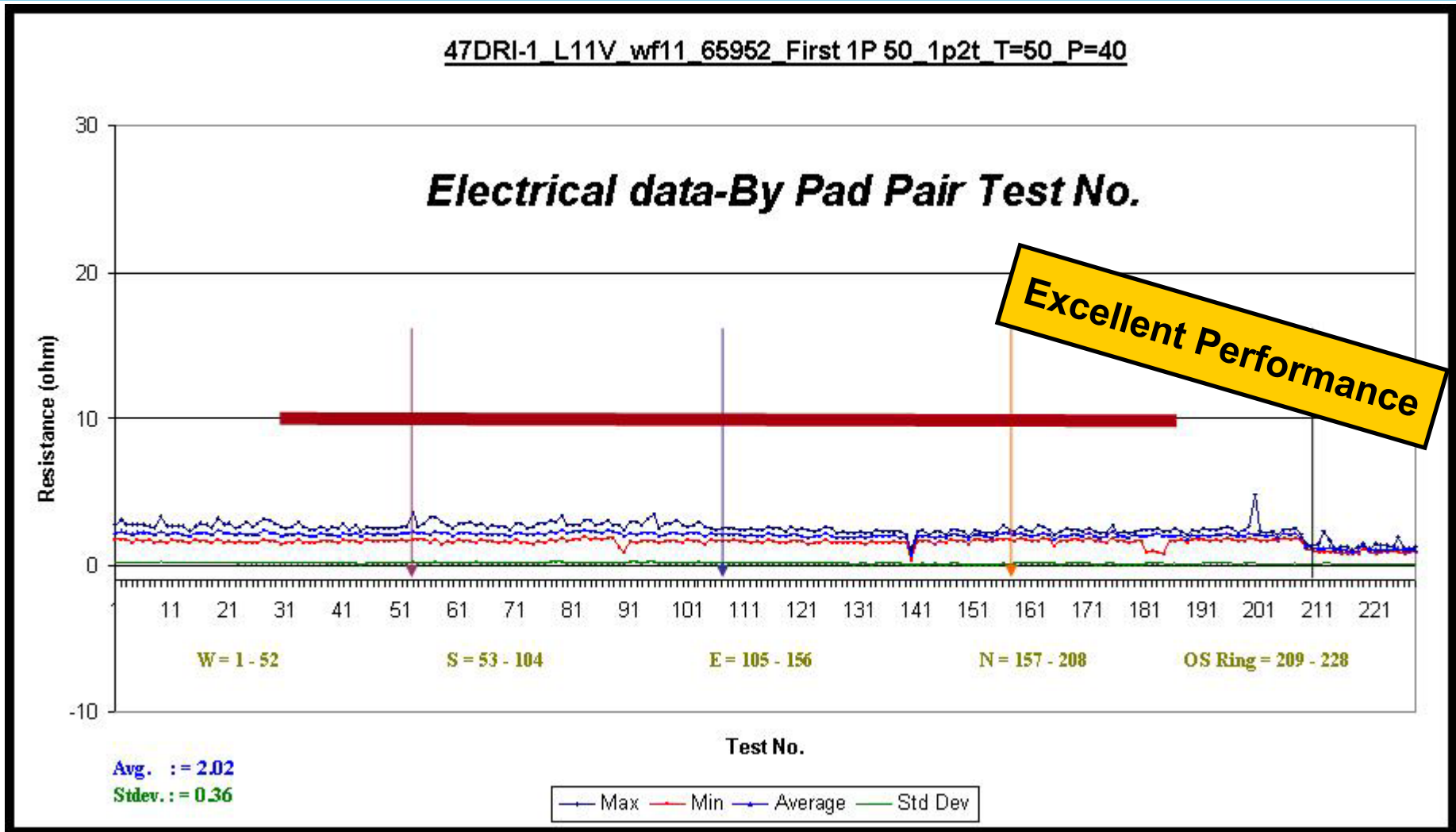
**Probe mark photo 1 Pass Nominal**  
**\*\*\* EXCELLENT PLACEMENT! \*\*\***

*(Remember there are 968 pins! @47um)*





# Results: 47um Dual Row Inline – Electrical by Pair

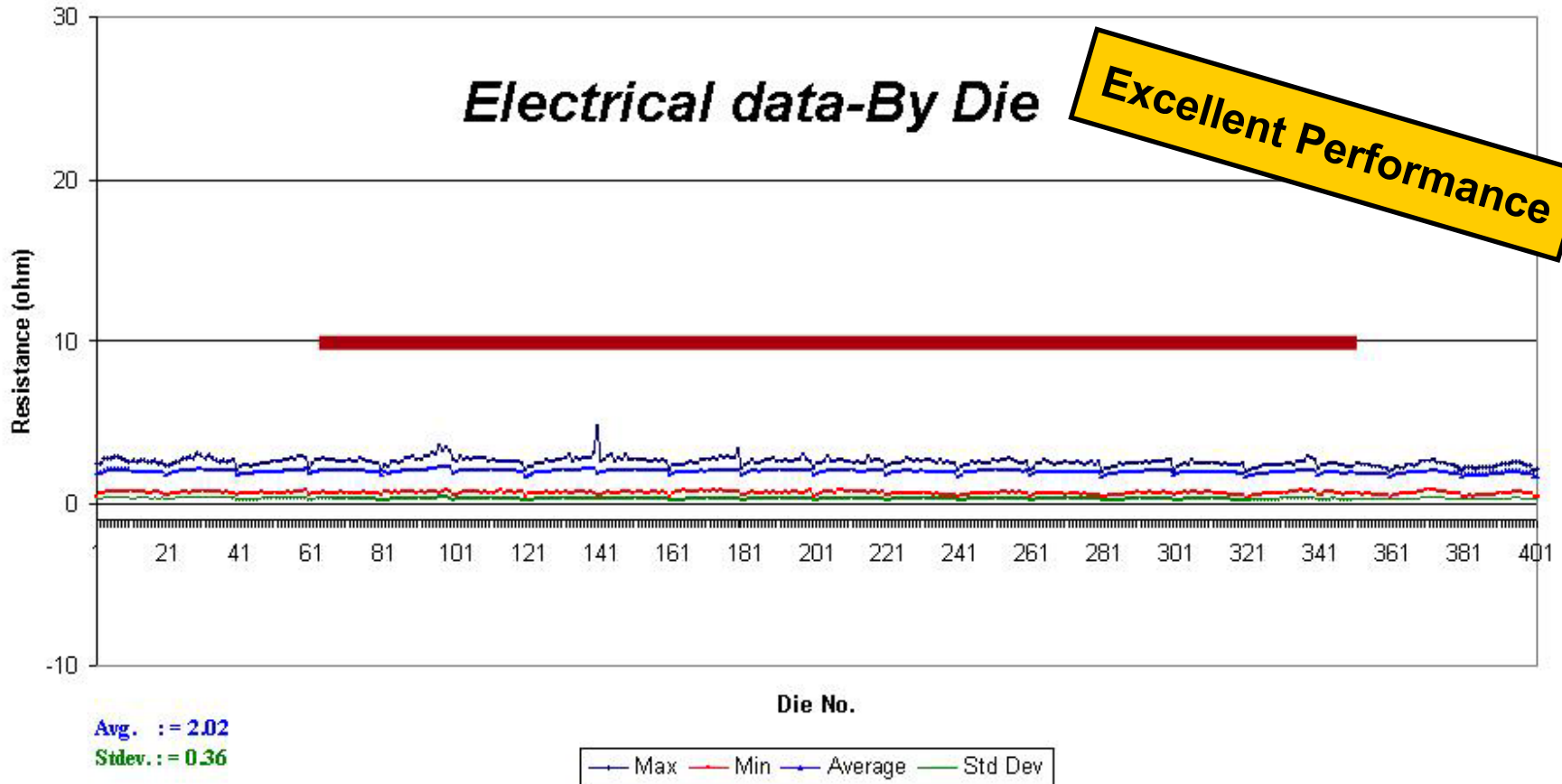


# Results: 47um Dual Row Inline – Electrical by Die

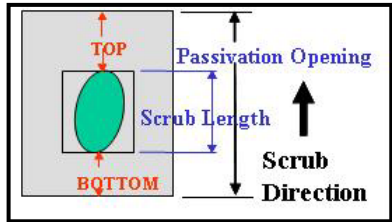
47DRI-1 L11V wf11\_65952 First 1P 50 1p2t T=50 P=40

*Electrical data-By Die*

**Excellent Performance**



# Results: 47um Dual Row Inline – Cpk Placement



## Mechanical Capability Data 47um Dual Row In-Line Design First 1-Pass

**Excellent Performance**

### Y-Cpk for Various Pad Lengths

PL=Passivation Length
SL=Average Scrub Length (PR ScrubY Size)
TE=Average Scrub Target Error  Top-Bot  / 2
SD <sub>SL</sub> =Std Dev of Scrub Length
SD <sub>TE</sub> =Std Dev of Target Error
$Y C_{PK} = (PL/2 - SL/2 - TE) / (3 * (SQRT(SD_{SL}^2 + SD_{TE}^2)))$
$Y C_P = (PL - SL) / (6 * (SQRT(SD_{SL}^2 + SD_{TE}^2)))$

Outer			
66.00	76.00	84.00	86.00
16.92	16.92	16.92	16.92
2.95	2.95	2.96	2.96
4.14	4.14	4.14	4.14
2.31	2.35	2.39	2.40
1.52	1.86	2.13	2.20
1.73	2.07	2.34	2.41

Inner			
66.00	76.00	84.00	86.00
16.92	16.92	16.92	16.92
2.48	2.47	2.48	2.48
4.14	4.14	4.14	4.14
1.93	1.95	1.98	1.98
1.61	1.97	2.26	2.33
1.79	2.15	2.44	2.51

### X-Cpk for Various Pad Widths

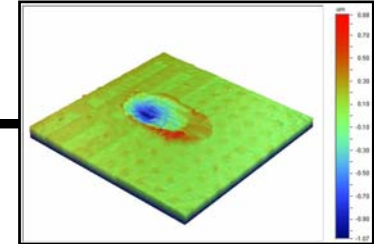
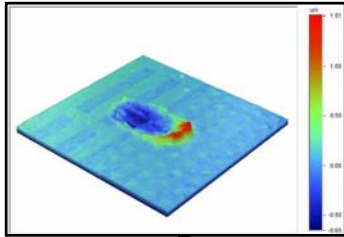
PW=Passivation Width
SW=Average Scrub Width (PR Scrub X Size)
TE=Average Scrub Target Error  Left-Right  / 2
SD <sub>SW</sub> =Std Dev of Scrub Width
SD <sub>TE</sub> =Std Dev of Target Error
$X C_{PK} = (PW/2 - SW/2 - TE) / (3 * (SQRT(SD_{SW}^2 + SD_{TE}^2)))$
$X C_P = (PW - SW) / (6 * (SQRT(SD_{SW}^2 + SD_{TE}^2)))$

40.00	44.00
11.15	11.15
2.27	2.28
1.65	1.65
1.86	1.88
1.63	1.89
1.93	2.19

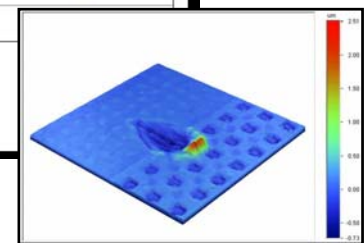
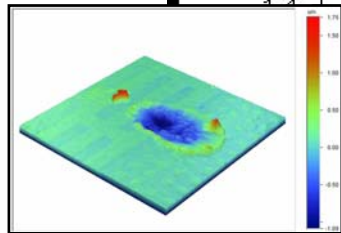
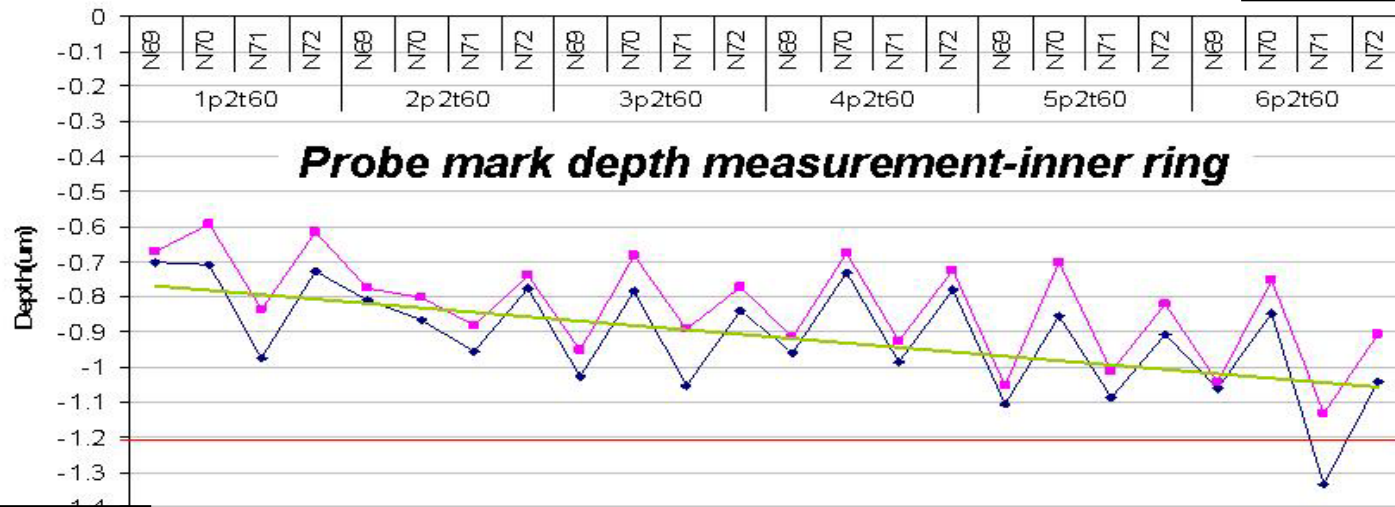
40.00	44.00
11.15	11.15
1.94	1.95
1.65	1.65
1.44	1.44
1.90	2.20
2.20	2.50



# Results: 47um Dual Row Inline – Scrub Depth



**DRI47-1 6 Passes Double Touch 60**  
**65952 W15-Inner Ring**



**Very Good Performance**

# Conclusion Observations:

Based on the FMTC evaluations, only the best cantilever probe card suppliers are able to meet our requirements and support expansion into “Fine Pitch – Dual Row – High Pad Count” and the “Ultra Fine Pitch” probe requirements.

Traditional Cantilever Probe has finally moved Probe Capability back to being ahead of Wirebond Capability .... but only slightly!,  
.... so don't relax yet!

Bill Williams, Freescale FMTC Probe Mgr  
Freescale Semiconductor

# Acknowledgements



## Future Work Planned

Deploy 37um FP to factories  
Develop 31um on 448 pads  
Deploy 47um DRI to factories  
Triple Row 47um High Pin Count  
Probe Polish Material Optimization

*There are many groups supporting the Fine Pitch Probe effort, my sincere apologies to any of those I have neglected to highlight !*

FMTC Assembly Development  
Fine Pitch Wire bond Team

FMTC Fine Pitch Probe  
Development Group

FMTC Asia Assembly  
Support

WBSG Product Team

TSPG Product Team

NPI Austin Test OHT

NCSG Product Team

## Key Probe Supplier Support Team Participants:

*You know who you are! & I say to you .....*

*Thanks! Domo Arigato! Great Work!*

*You are the keys to these successes and future development!*

# Thanks for your Attention!

## ?? Questions ??



*You can contact me at:*

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