

IEEE SW Test Workshop Semiconductor Wafer Test Workshop

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Probe Card Cost Drivers from Architecture to Zero Defects

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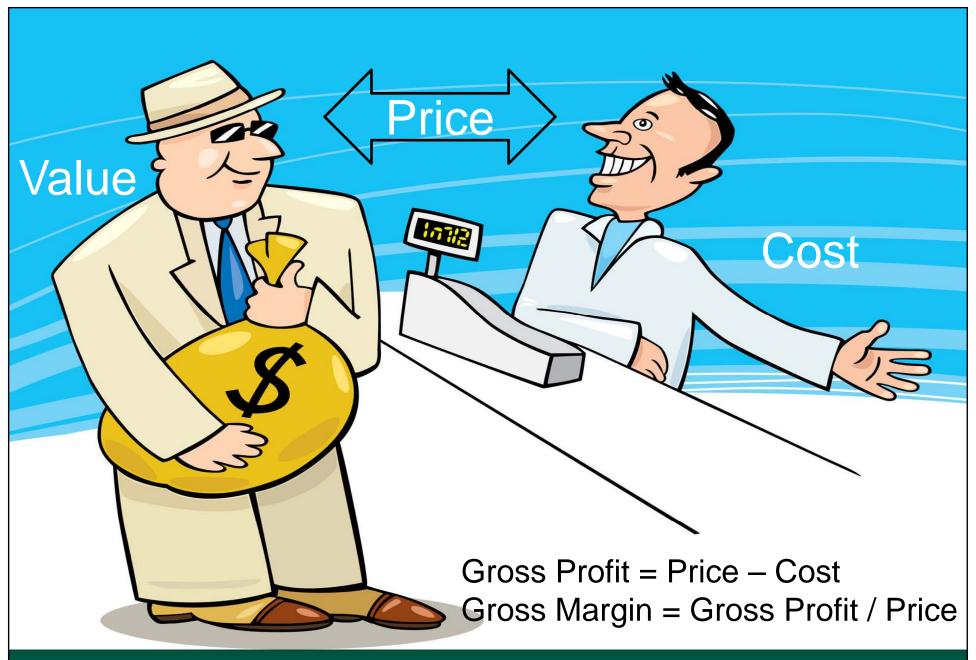
Feldman Engineering Corp.

Overview

- Cost, Price, & Cost Drivers
- Serial Processing Drilling Example
- NRE
- Advanced Process Technology
- Profitless Prosperity
- Cost Savings
- Summary

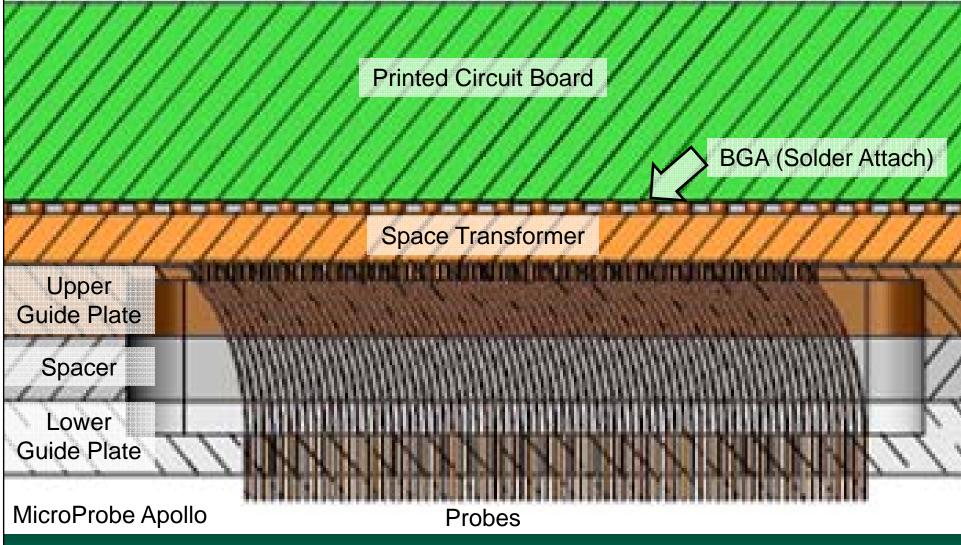
Note: price/cost examples are approximate and from multiple vendors not necessarily those identified or shown.







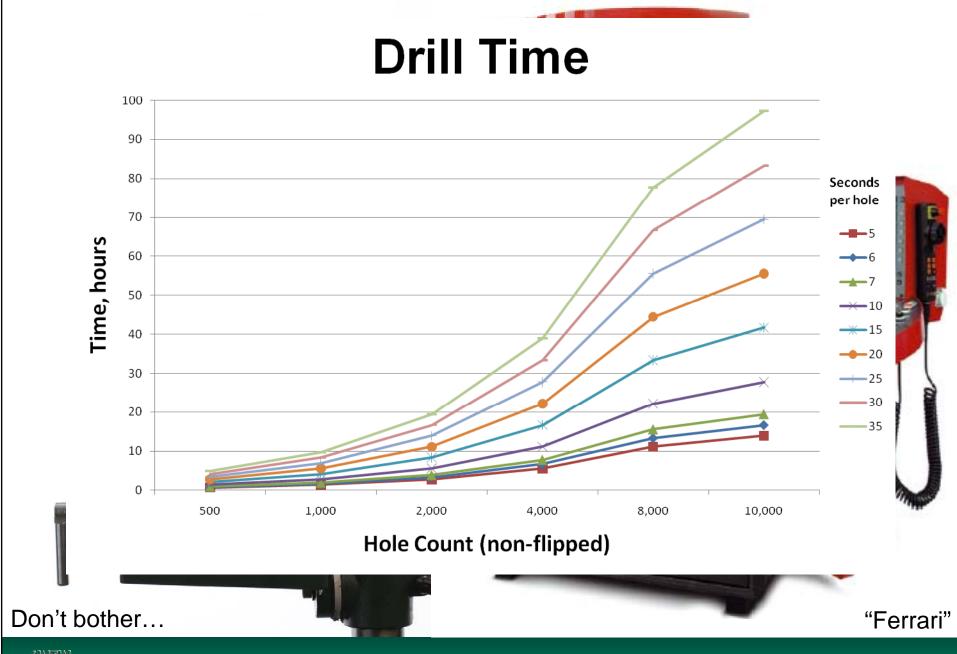
Vertical Probe Head





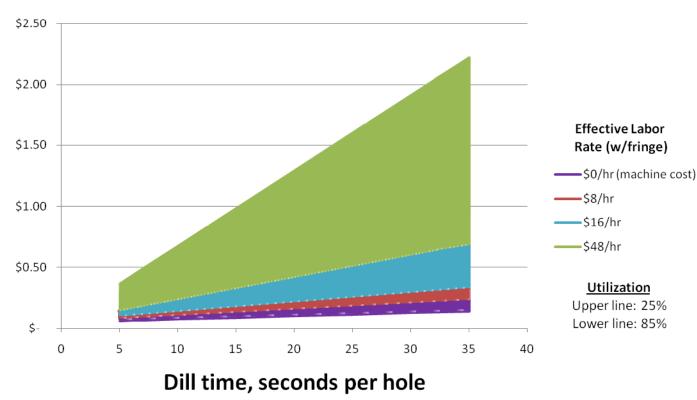




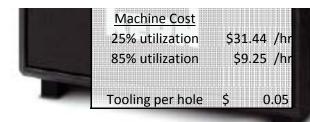




Cost Per Drill Hole







"Ferrari"







Non Recurring Engineering Expense

Architecture	Design	Tester	Customer
R&D	NRE	NRE	NRE
Design Input	X		X
Probes		10/A 0/	?
Guide Plates			The state of the s
Space Transformer			And the Contract of the Contra
Interposer	?		$\mathcal{M}\mathcal{A}\mathcal{A}$
PCB Design	X (External?)	X	?
PCB Fab	External	? /	?
Mechanical H/W	?	X	?
Electronics	?	3	?
Metrology	X	X	2
Packaging	1230054362705543400	X The state of the	?







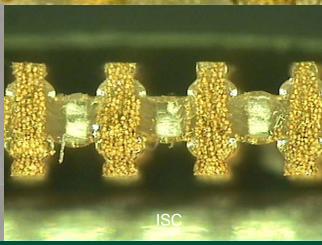


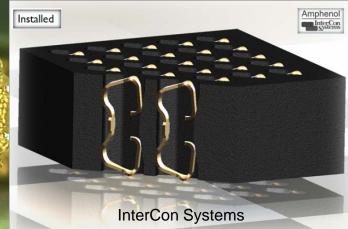


Interposers

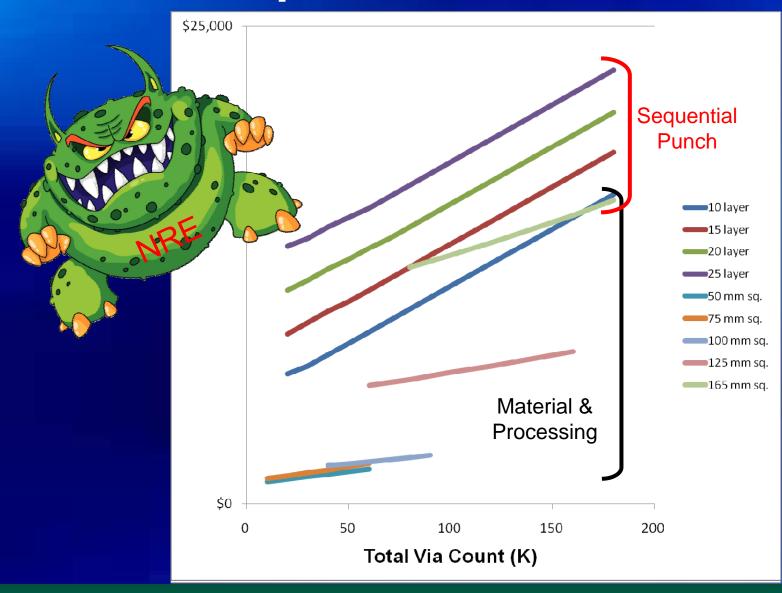
	(A) (A)	Spring Pin	Elastomeric	Molded Frame
Small Area	NRE	\$.5 - 1 K	\$2 - 3 K	\$20 - 30 K
	\$ / contact	\$1 - 10	\$.5060	< \$.2040
Large Area (1/4 wafer +)	NRE		\$10 - 15 K	\$100 - 150 K
	\$ / contact		\$.40 50	< \$.1020







Space Transformers





Advanced Process Technology

Cost Drivers

Process Steps

Masks

Substrates

Material

Active Area

Yield

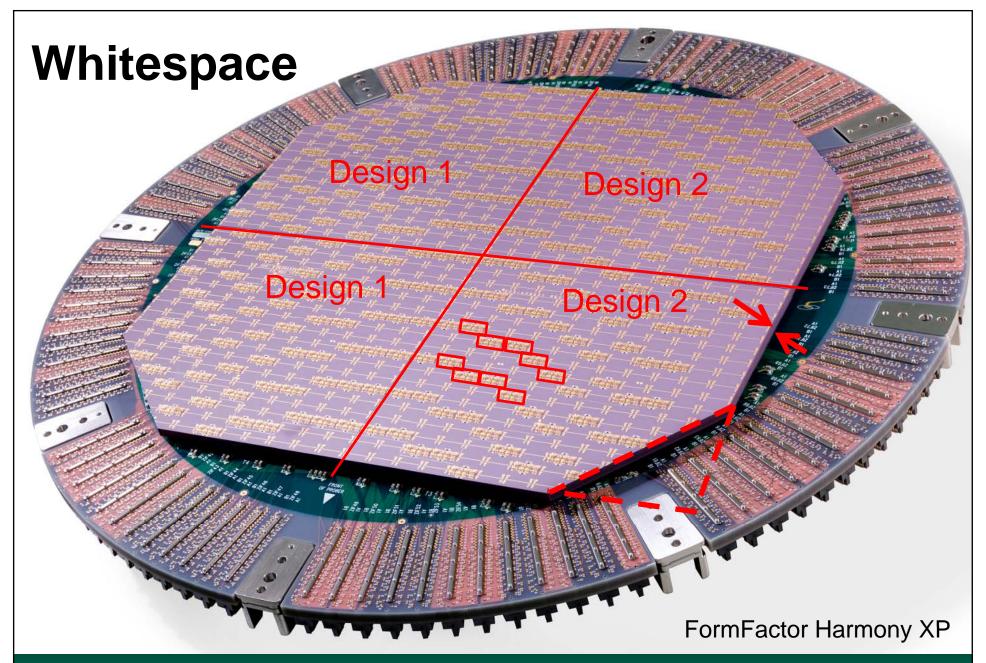
Defect Density

Layers

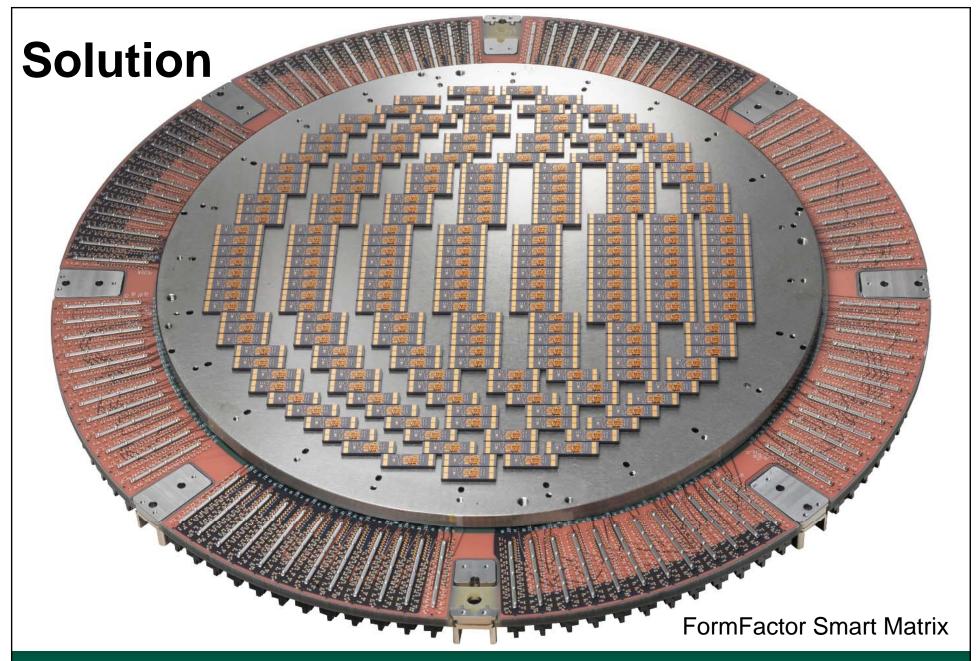
Equipment

Rework / Repair

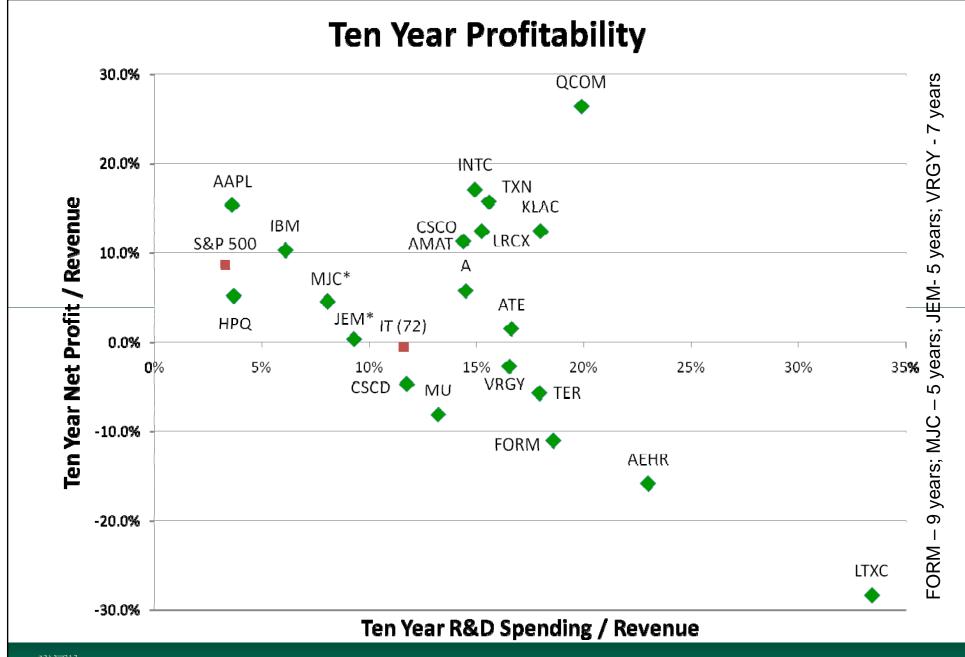


















Summary

- Understand true cost of architectures
 - Beware of NRE
 - New architectures needed for cost reductions
- Maintain sufficient Gross Margin
 - Company health
 - Funding for R&D
- Honest supplier customer partnerships



Acknowledgments

- Amphenol InterCon Systems
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- FormFactor
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- Kern
- Robin McAdams
- MicroProbe
- Sergio Perez
- SV Probe
- Frank Swiatowiec



Thank You!

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Visit my blog www.hightechbizdev.com for my summary of SWTW

