

# High Density and Low Cost Approach for the PCB of semiconductor tester



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**IEEE SW Test Workshop**  
**Semiconductor Wafer Test Workshop**

June 10 - 13, 2012 | San Diego, California

# Contents

1. Technical trend for PCB
2. MWB technology
3. Hybrid MWB for advanced PCB
4. Summary

# Technical trend for testing

- High Speed ;

Speed of electric device is getting higher

- High Density ;

More signal is required

- increasing in Pin count

- Pair drive for High speed signal

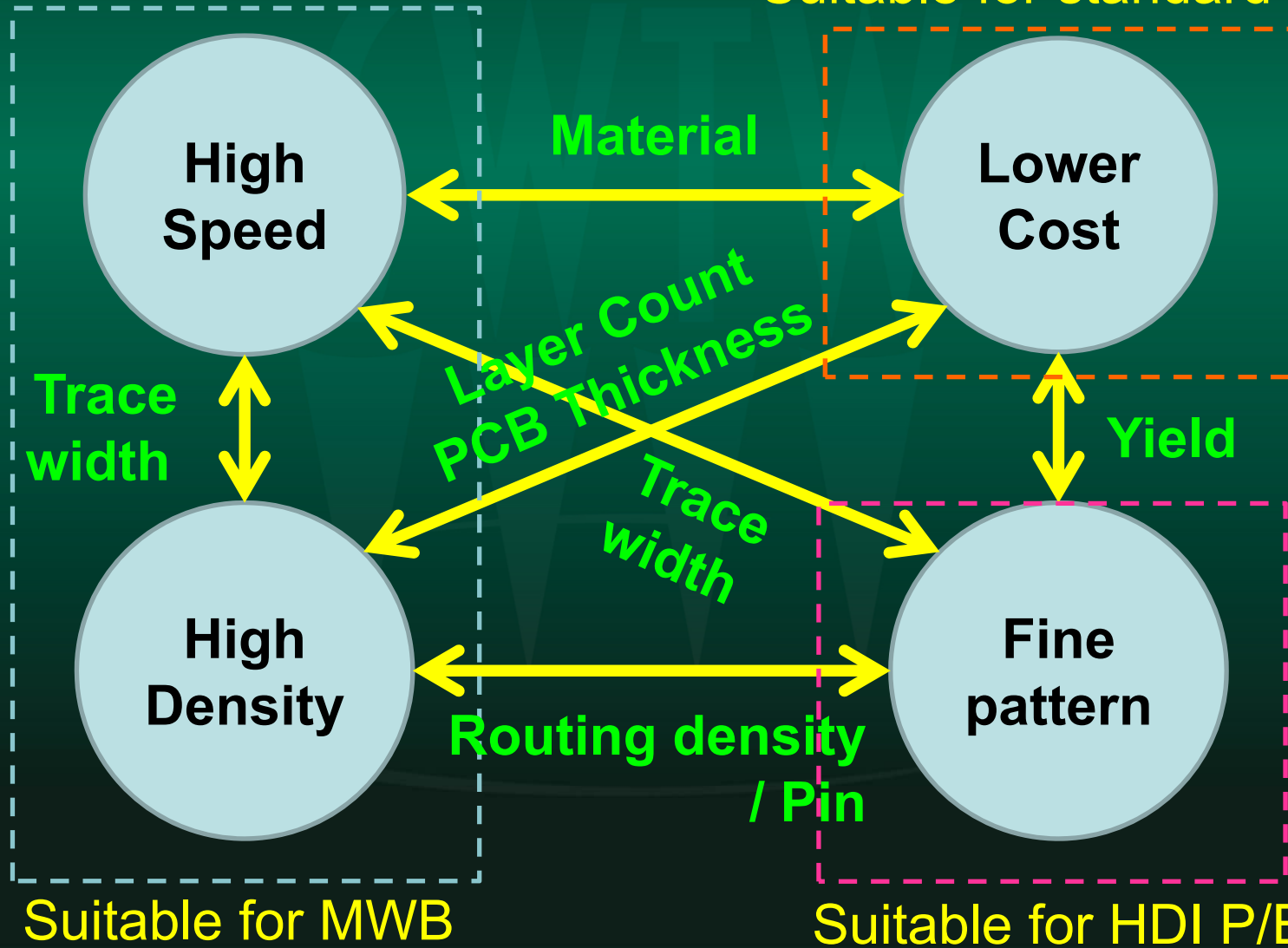
- Fine Pattern ;

Testing system becomes complicated and various components to be required

- pin count of comps. increased and pin pitch become narrow due to comps. downsizing

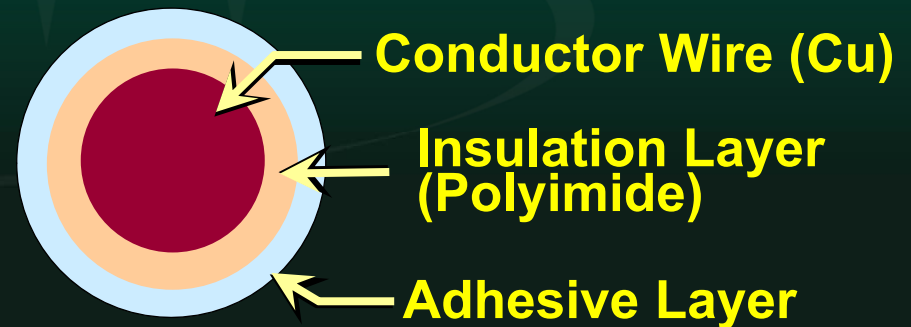
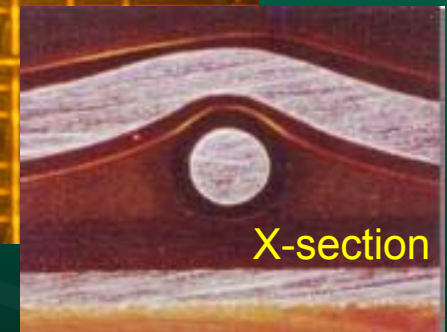
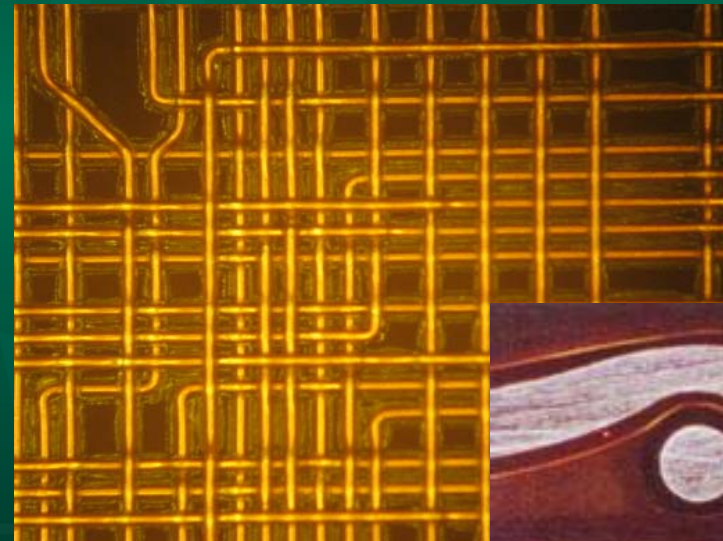
# Conflicting PCB Requirements

Suitable for standard P/E

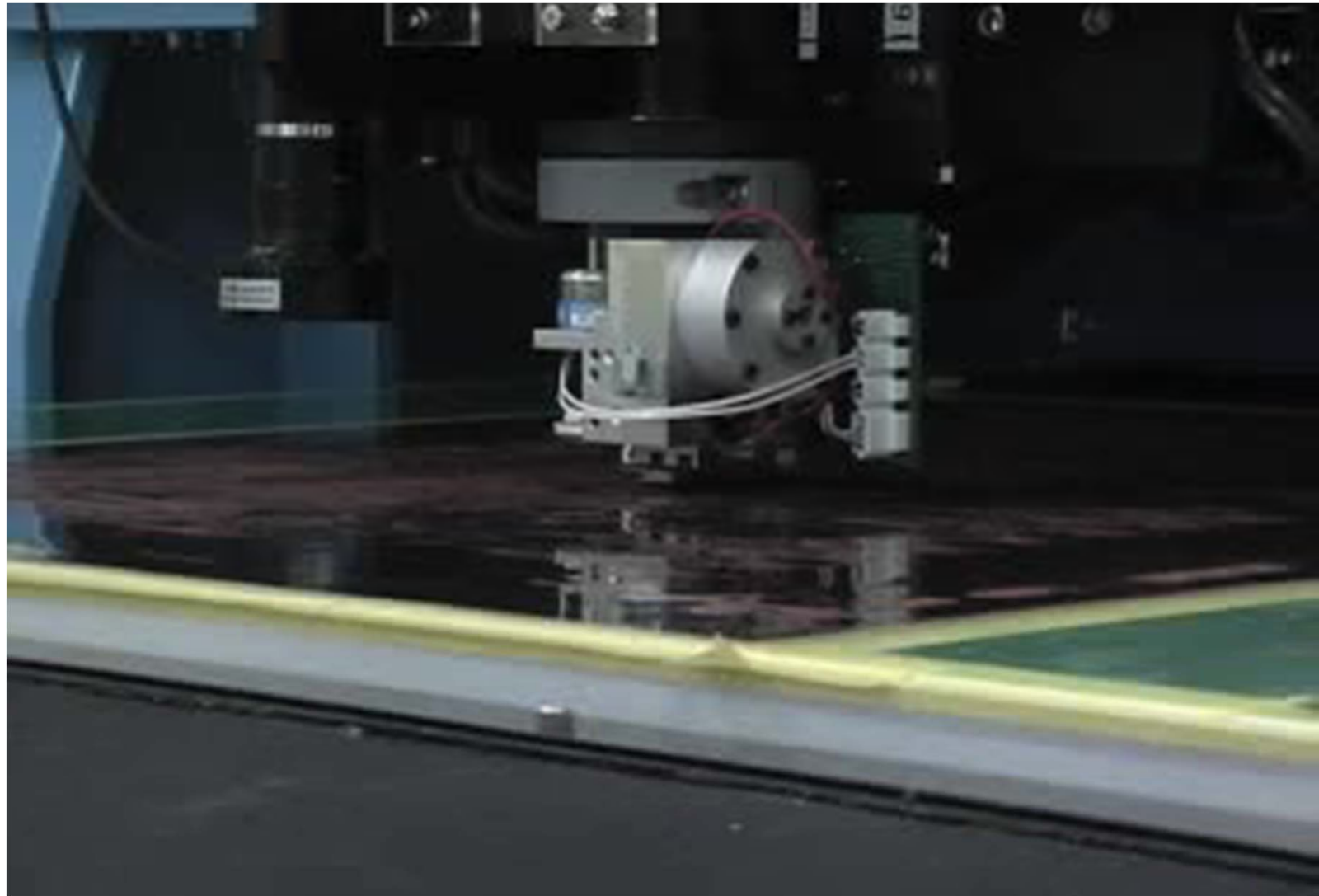


# MWB technology

- \*MWB is a PCB which replaces etched signal traces with copper wires
- \*Signal lines are able to cross over each other because copper wire is insulated with resin coating.



# Actual wiring operation



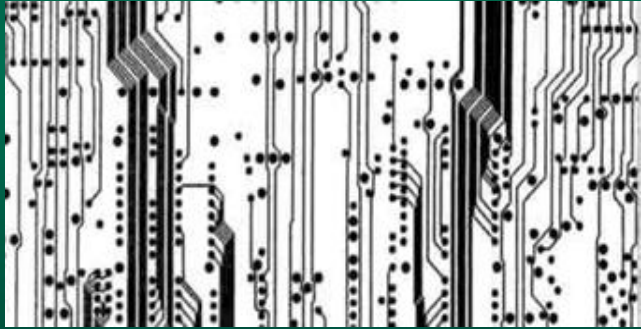
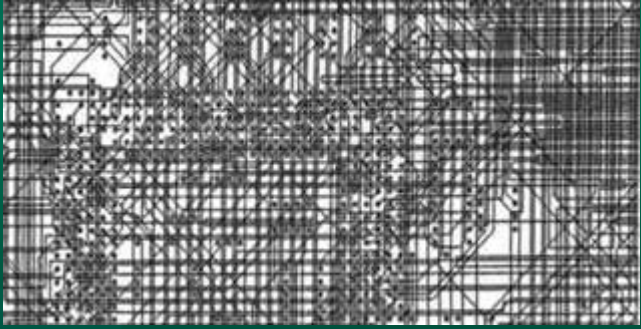
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6

# High Density Wiring

	Print and Etch (P/E)	MWB
Signal Layer CAM Image		
Index # of signals per layer	1	2.5 * Average based on achievements (convert fm P/E to MW)

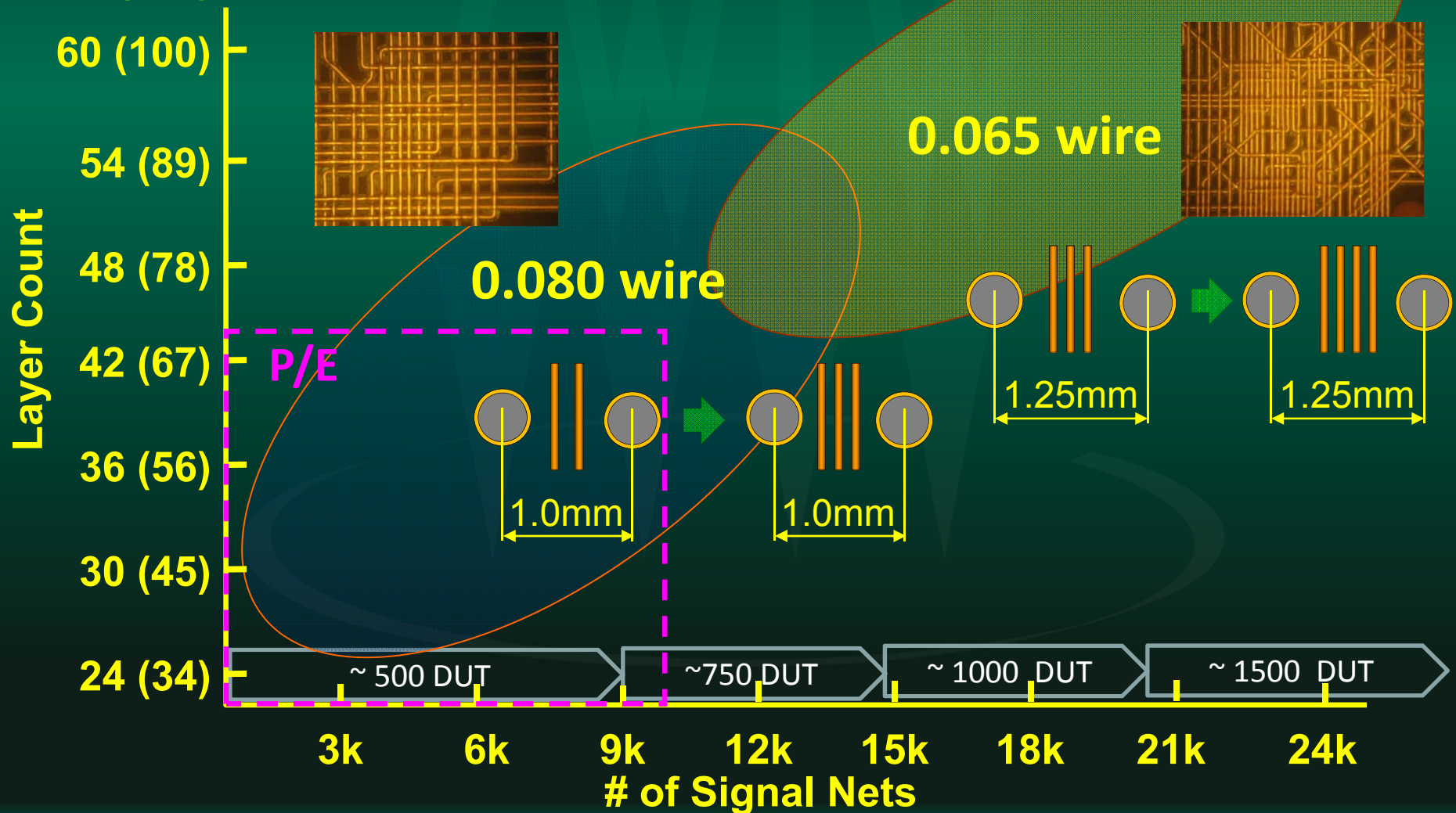
**Multi-Wire has abilities to,**

- Route 2.5 times of signals than P/E PCB into one layer.
- Increase total # of routed signals in PCB.

# MWB - High signal capacity

Based on Achievements for Probe Card PCB

MWB (P/E)



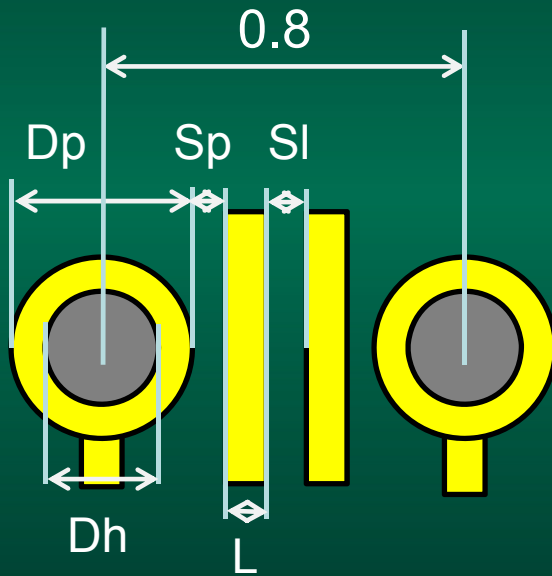
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# Design study for pair drive (1)



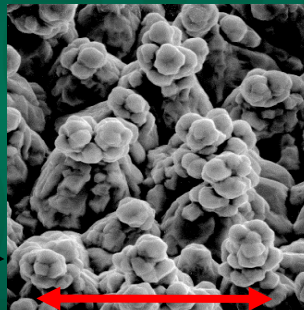
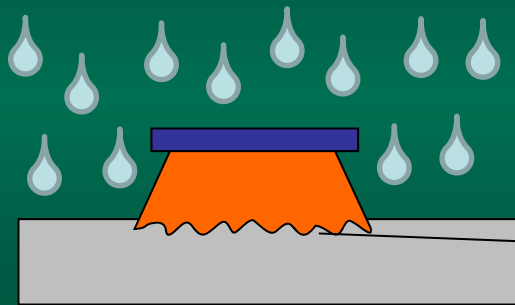
Item	Unit	P/E 0.5oz HVLP	MWB
Material	-	FX-2(s) Dk3.6	I-671 Dk3.6*
PCB Thickness	mm	6.2	
Drill Dia. (Dh)	mm	0.25	
Pad Dia. (Dp)	mm	0.40	-
Line width (L)	mm	0.080	0.065
Space L-L (SL)	mm	0.080	0.113
Space P-L (SP)	mm	0.080	-
Resistance	$\Omega/m$	11.9	5.3
Crosstalk	%	9.8	2.5
Attenuation @3GHz	dB	-4.9	-4.6

\* Nominal value for MWB structure

# Comparison of Signal Shape

## Print and Etch (P/E)

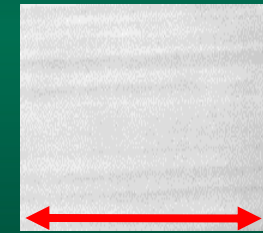
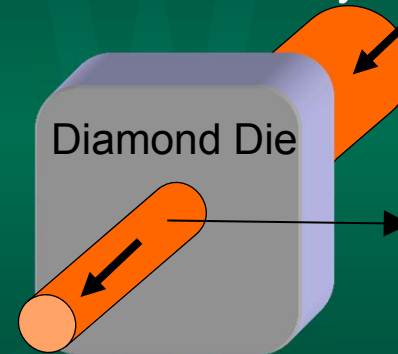
Formed by Etching Process



10um

## Multi-Wire (MW)

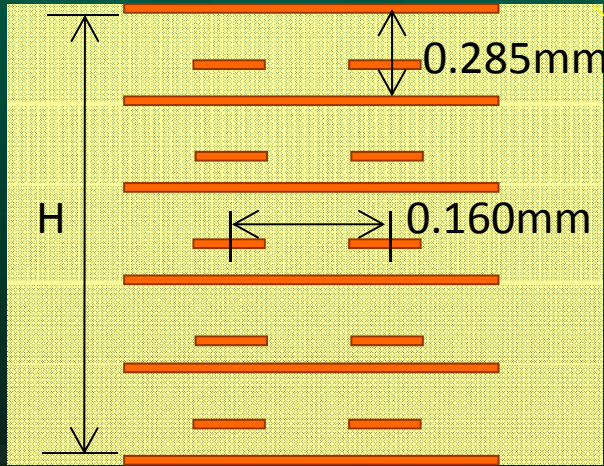
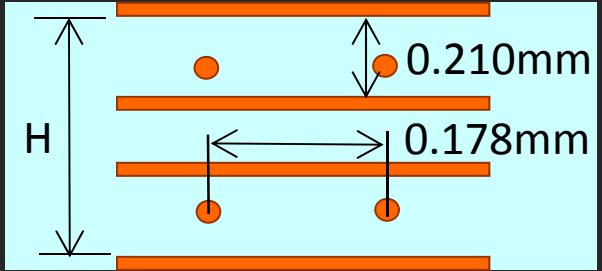
Formed by Die Drawing



10um

P/E 0.08mm, 0.5oz	Compared item	MW 0.065mm
1	X-section size index <u>Low resistance (Low attenuation)</u>	2.3
+/- 0.010mm	Tolerance of Conductor Width <u>Easy impedance control</u>	+/- 0.003mm
~ 10um	Conductor Surface Roughness <u>Low skin effect , Low attenuation in high frequency</u>	~ 0.5um

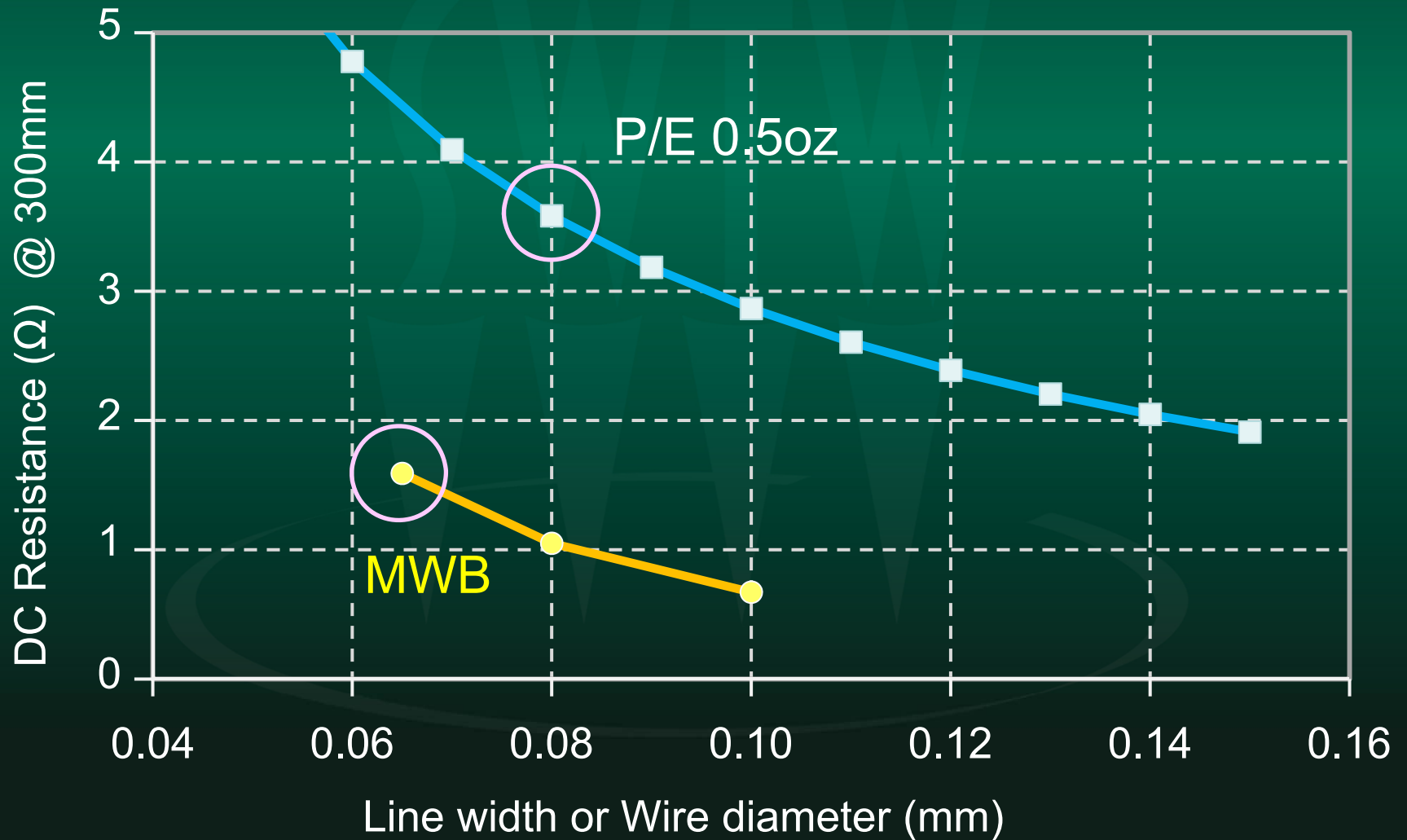
# Design study for pair drive (2)

Item	P/E	MWB
Characteristic impedance ( $Z_0$ )	Differential 100 $\Omega$	<b>Differential 100<math>\Omega</math></b>
Base material	FX-2(s) (Dk:3.6)	<b>I-671 (Dk:3.6*)</b>
Line width	0.080mm	<b>0.065mm</b>
Line thickness	0.018mm	
Structure		
H	1.485mm	<b>0.550mm (63% reduced)</b>

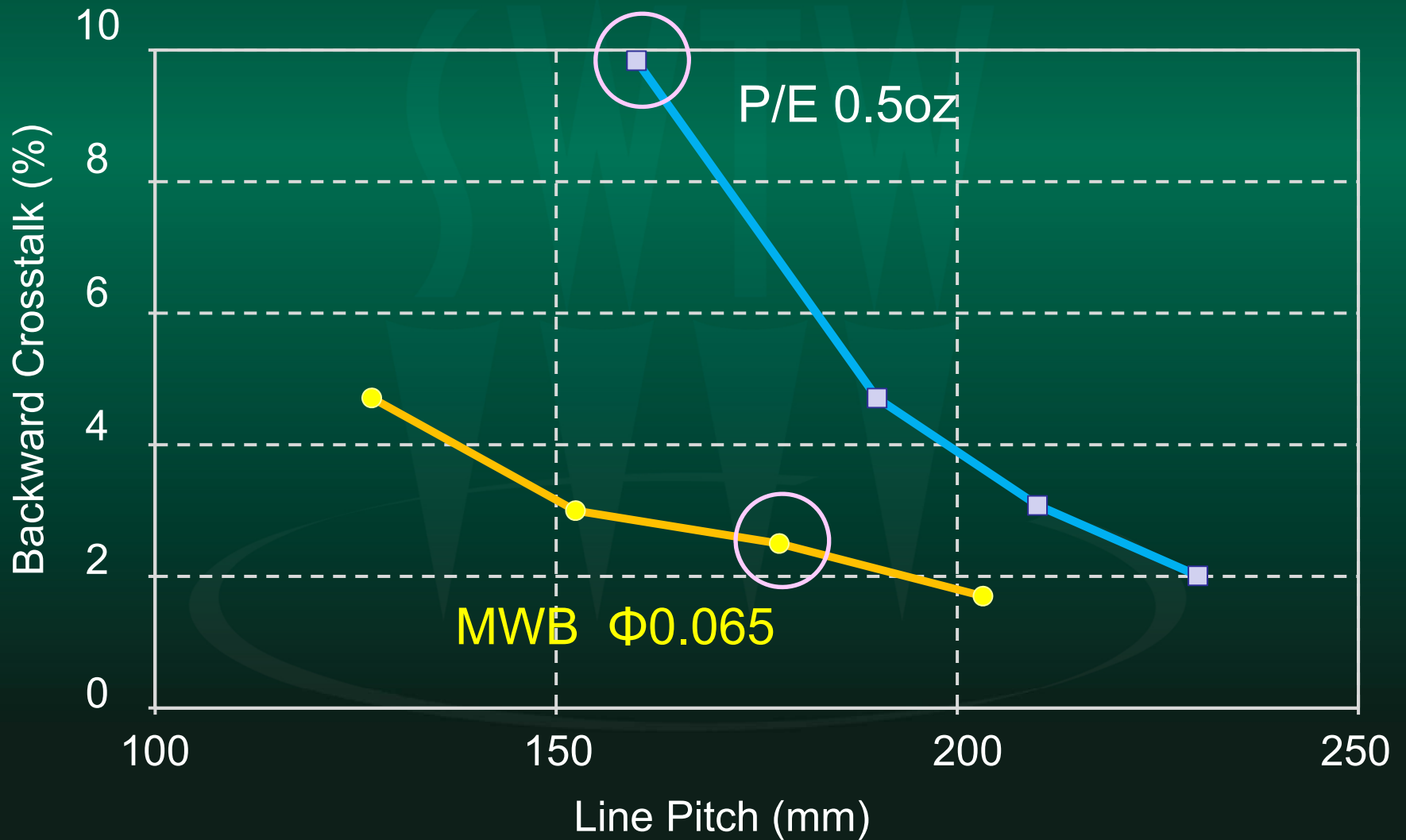
\* Nominal value for MWB structure

**MWB can reduce total thickness with keeping high routing density**

# Signal Line Resistance comparison



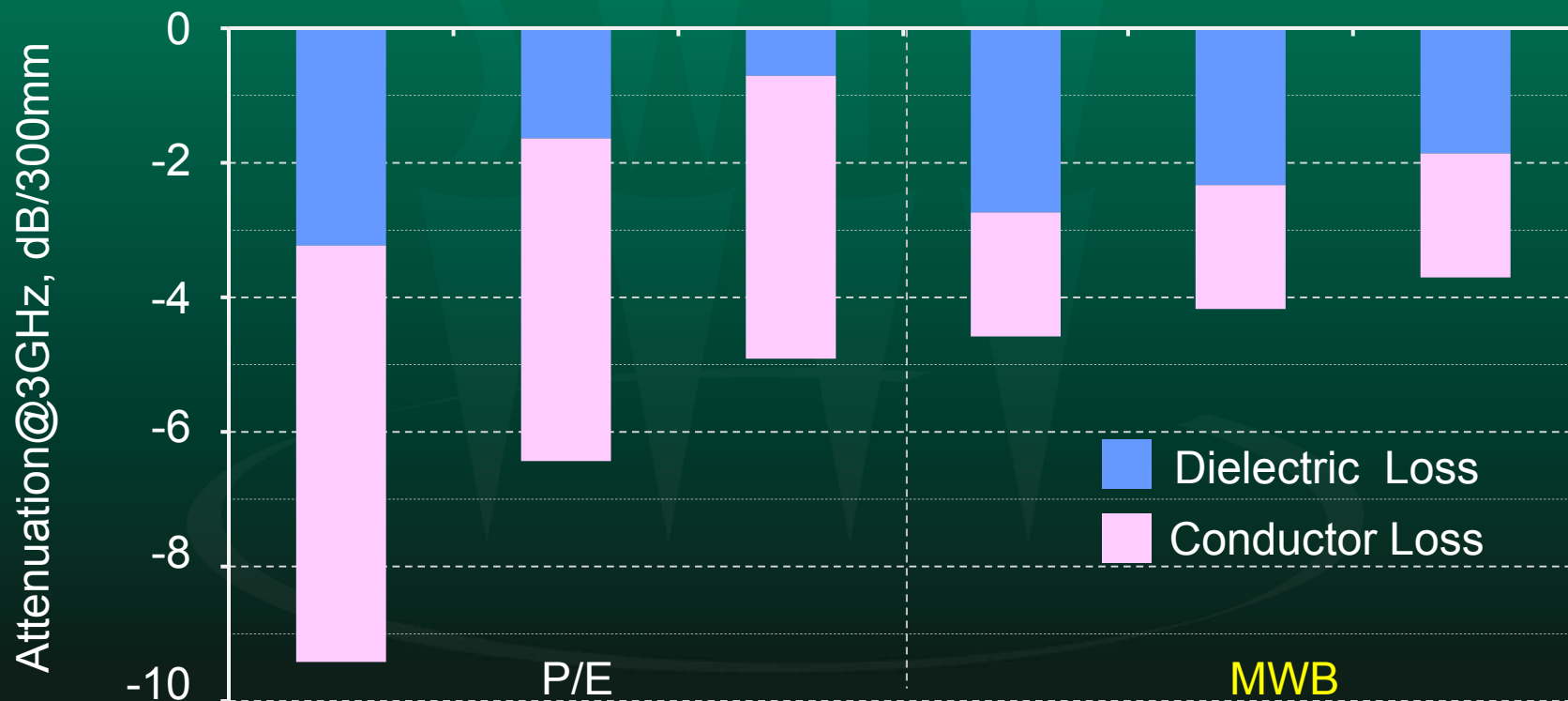
# Cross-talk (Backward) Comparison



(Condition) L : 300mm,  $Z_0$  : 50 $\Omega$ , Rise Time : 35ps

# 3GHz Signal attenuation

Material	FR4	Mid-Range	High-end	Standard	Mid-Range	High-end
	E-679	HE-679G	FX-2(s)	I-671	HE-679G	FX-2(s)
Cu Foil type	Standard	VLP	HVLP	MWB	MWB	MWB
Trace	Line width = 0.08mm, 0.5oz Foil			Wire diameter = 0.065mm		



**Attenuation = Dielectric Loss (Dk, Df) + Conductor Loss (Re)**

# Summary of MWB technology

- **Signal Density per layer of MWB is 2.5 times higher than P/E PCB by using cross over wires.**
- **Low resistance, Low crosstalk characteristics of MWB bring better signal integrity for high speed application.**
- **Compared with P/E PCB, MWB can reduce a insulation thickness with keeping high density routing and high electrical performance.**

# Hybrid MWB for advanced PCB

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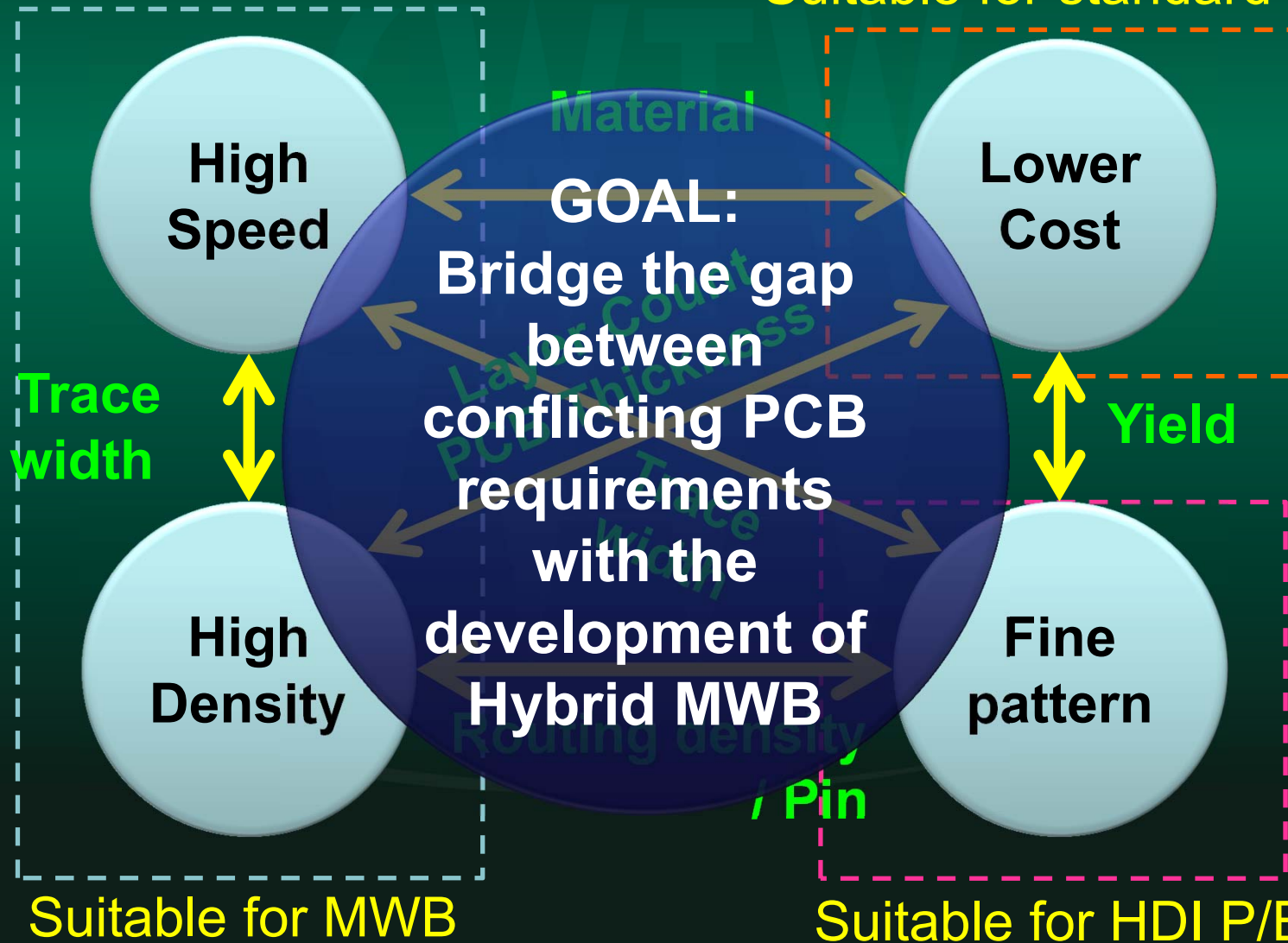
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16

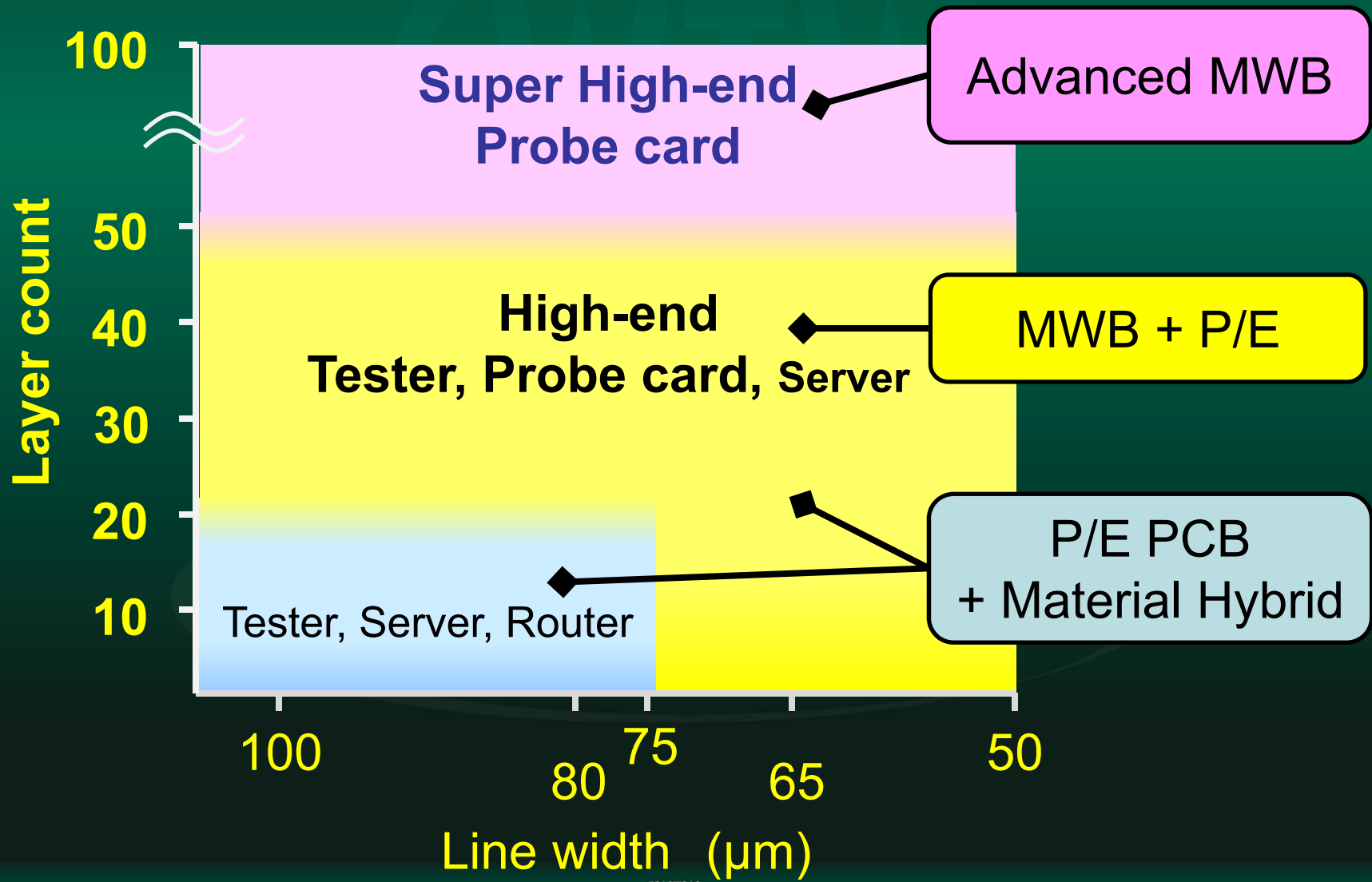


# Target for Hybrid MWB

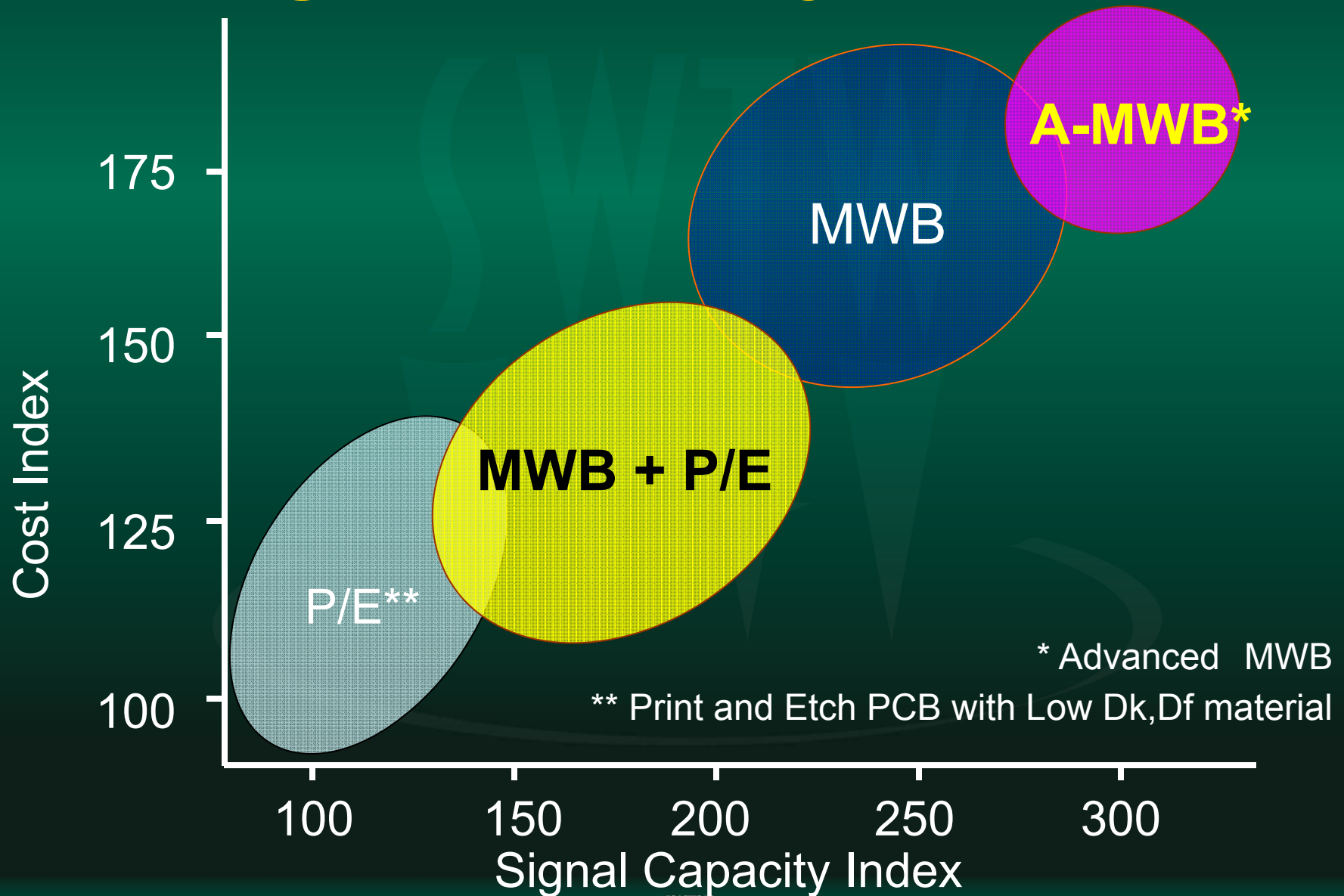
Suitable for standard P/E



# Application field for Hybrid MWB



# Target area for Hybrid MWB



\* Advanced MWB

\*\* Print and Etch PCB with Low Dk, Df material

# Material Hybrid

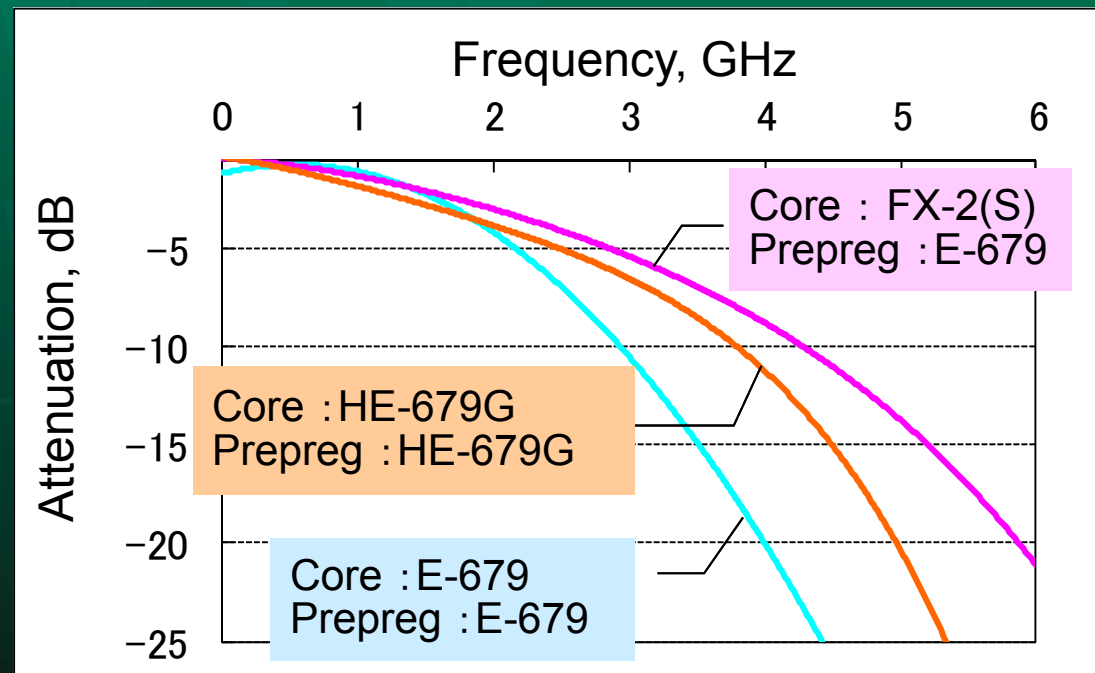
- Lower Dk, Df material for high speed signal layers
- Conventional FR-4 (high Dk, Df) for Power / GND layers



Power/GND  
Core : E-679  
Prepreg : E-679

Signal  
Core : FX-2(S)  
Prepreg : E-679

Power/GND  
Core : E-679  
Prepreg : E-679



# MWB + P/E Hybrid (e.g. 1)

- MW for critical signals (high speed, shortest, equal length, etc.)
- P/E for other signal layers (DC signal, etc.)
- P/E for Power / GND layers



	Layer Type	MWB	<b>MWB + P/E Hybrid e.g. 1</b>
P/E	Power /GND	I-671	<b>E-679</b>
	Critical Signal	2L - MW I-671	<b>2L - MW</b> I-671 or <b>HE-679G</b> or <b>FX-2(s)</b>
P/E	Other Signal	4L- MW I-671	<b>10L- P/E</b> <b>E-679</b>

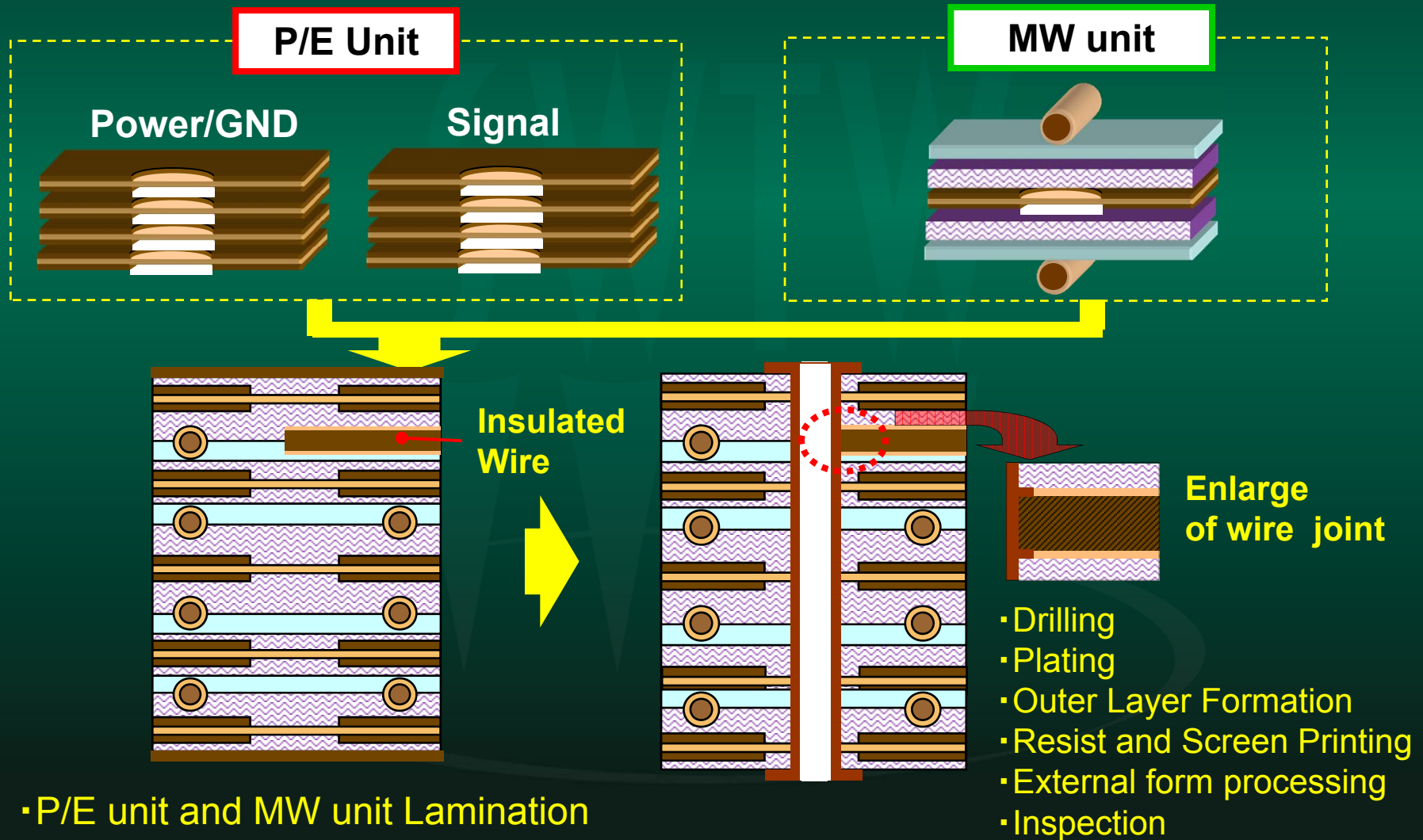
# MWB + P/E Hybrid (e.g. 2)

- P/E with lower Dk, Df material for critical signals
  - Wider trace (lower density routing) is also applicable to improve electrical performance.
- > Routing density decreases, However, MWB can cover it.



	Layer Type	MWB	<b>MWB + P/E Hybrid e.g. 2</b>
P/E	Power /GND	I-671	I-671
	Critical Signal	2L - MW I-671 2 wires / pin	<b>6L - P/E FX-2(S) 1 trace / pin</b>
P/E	Other Signal	4L - MW I-671 2 to 5 wires/ pin	4L - MW I-671 2 to 5 wires/pin

# Hybrid MWB process flow



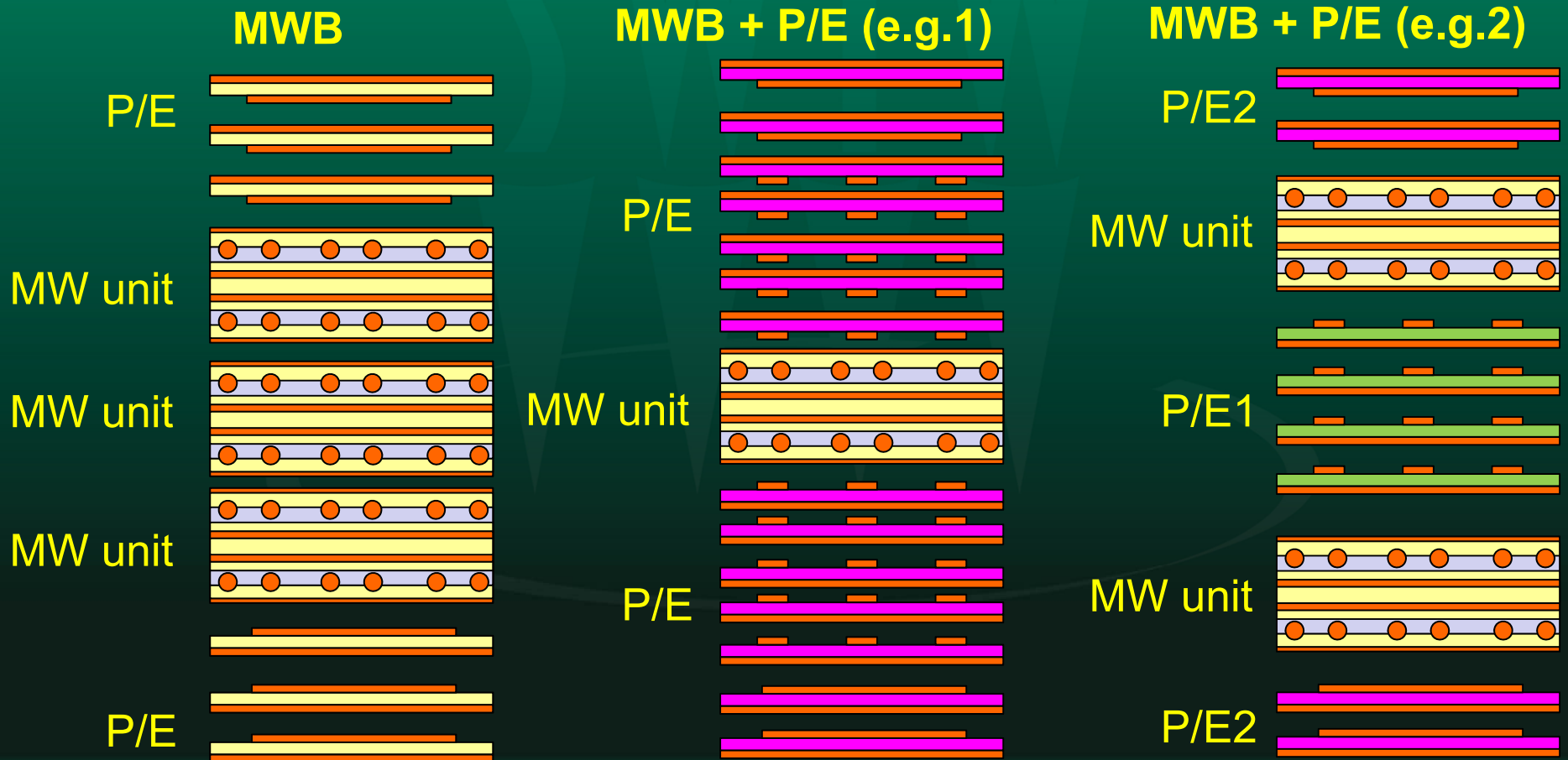
• P/E unit and MW unit Lamination

Each unit have different CTE (Coefficient of Thermal Expansion) .  
Key technologies which control CTE-related problem are required.

# Key tech. (1) - Optimized stack up -

Concern : Bow / Twist

Key tech.: Optimized (Symmetrical) stack up



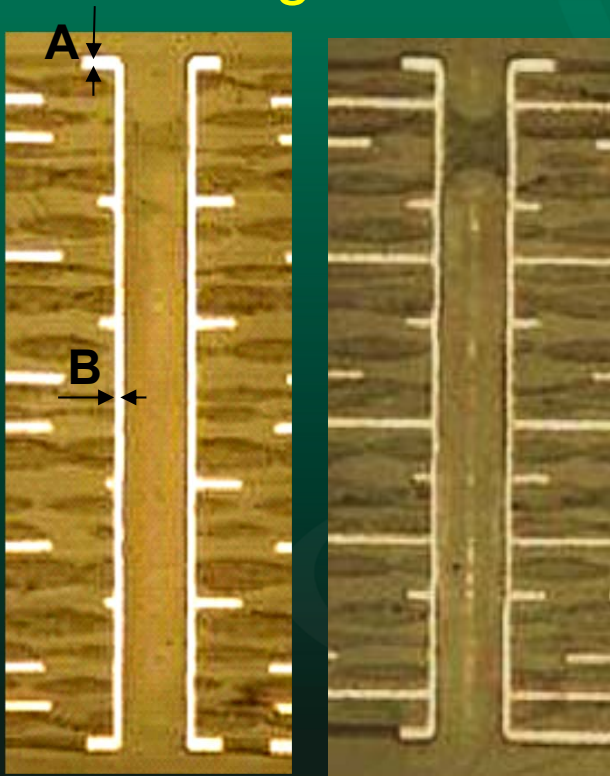


# Key tech. (2) - Electroless Cu Plating -

Concern : Plated through hole reliability

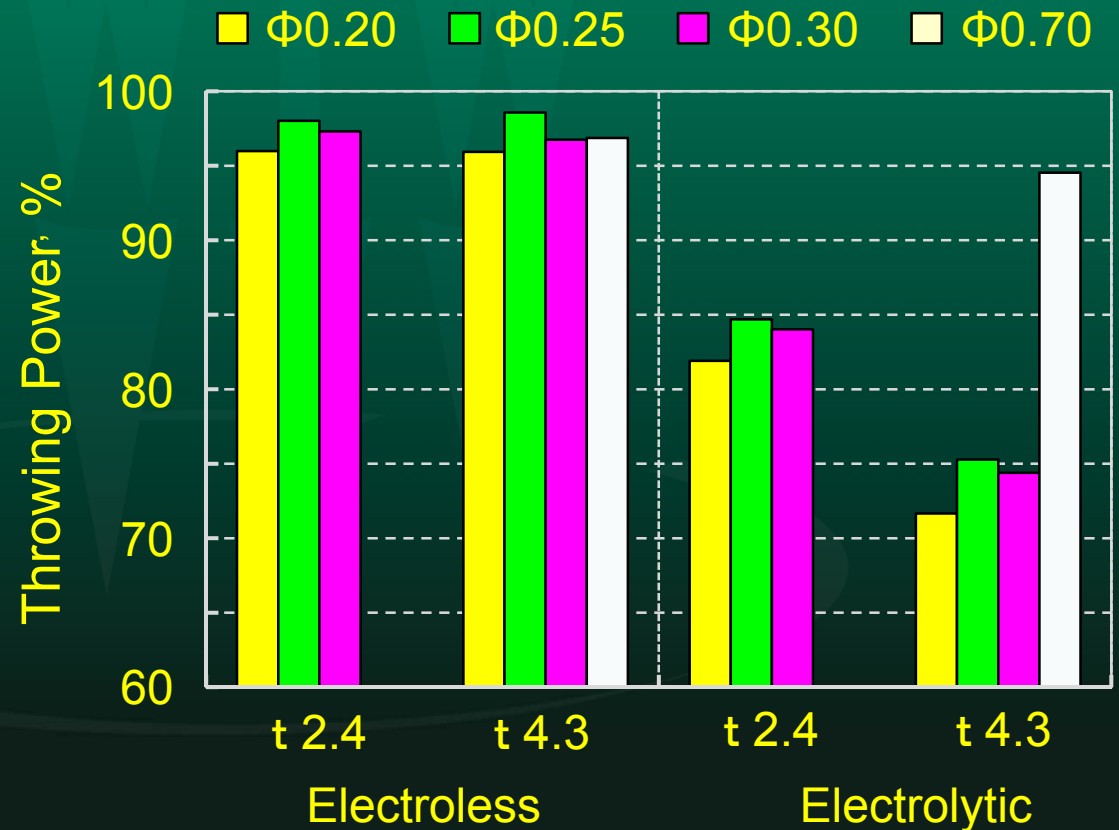
Key tech.: Uniform (High throwing power) plating by electroless Cu plating

- Throwing Power \*



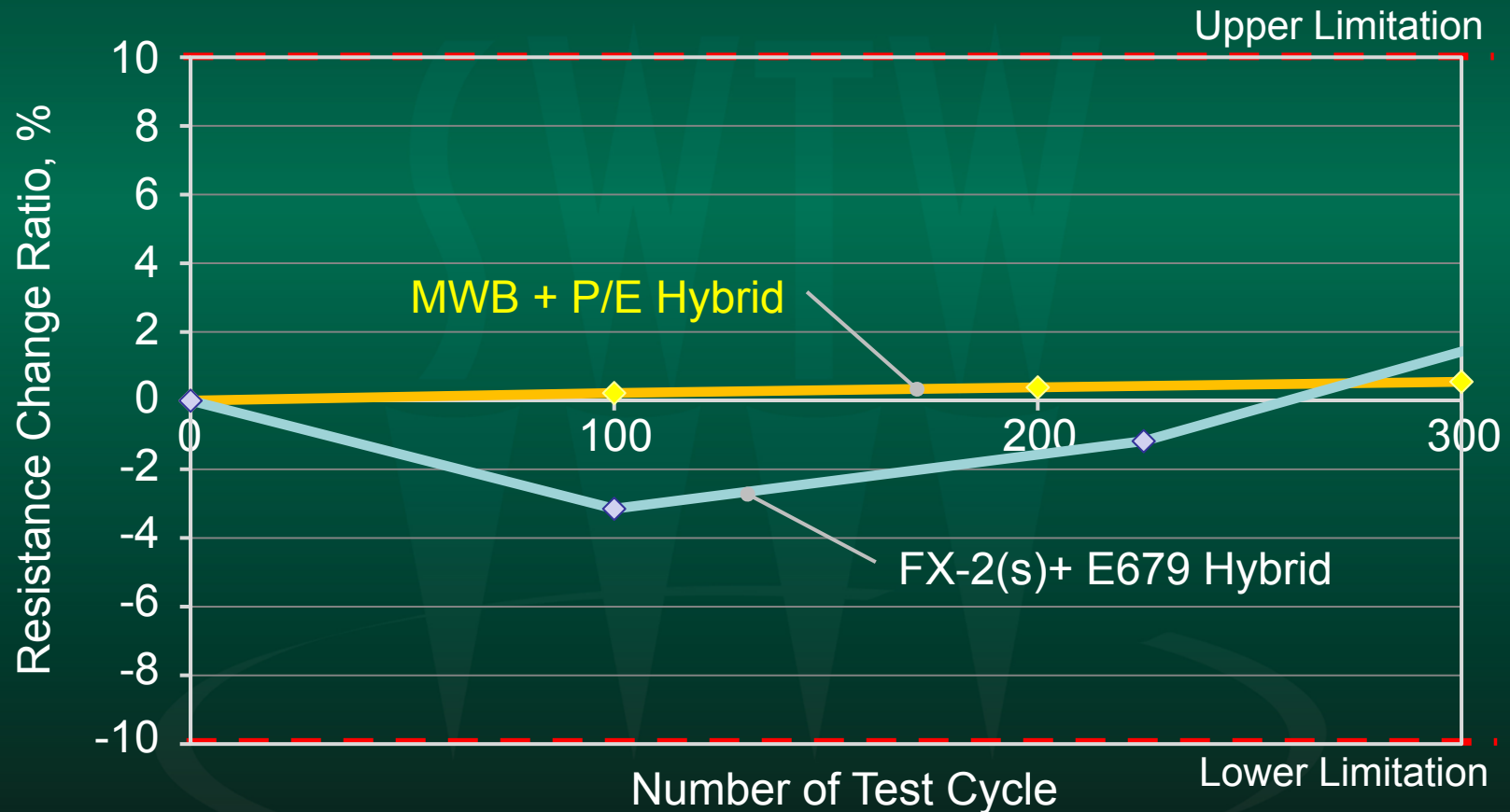
Electroless  
Φ0.30

Electrolytic  
Φ0.30



\*Throwing power = Plating thickness in the hole (B) / Plating thickness on the surface (A)

# Reliability for Hybrid PCBs



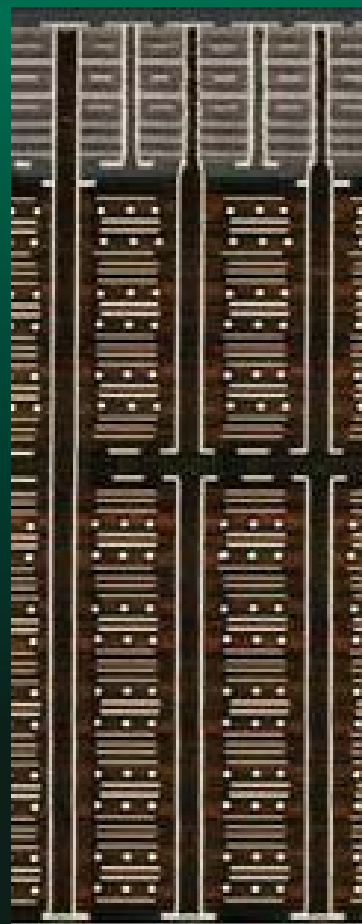
**Hybrid MWB with electroless Cu plating has good reliability.**

Test condition: 125°C/30min. ↔ -65°C/30min.

PCB thickness : t6.35mm, Drill Diameter : Φ0.30mm


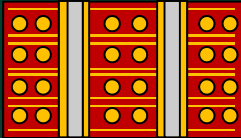
# “A-MWB” Further advanced approach

- Assembly for Narrow Pitch Comps.
- Further more High Density ( Addition of DUT count )



**Sub Unit**

## Example for Sub Unit

Item	e.g. 1	e.g. 2
PCB Type	P/E HDI or P/E (Pitch converter) 	MWB 
Feature	Assembly for Narrow Pitch Comps.	Further more High Density

# Summary

- Application of MWB provides one of the best solution for Higher density and Higher speed PCB.
- Hybrid PCB technology provides advanced characteristics.

**MWB + P/E Hybrid structure provides cost- effective solution.**

**A-MWB Hybrid structure is a further advanced approach for super high density PCB.**

# Thank You!

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29