



# IEEE SW Test Workshop

## Semiconductor Wafer Test Workshop

June 8 - 11, 2014 | San Diego, California

**probing@hot temperature**  
**a new thermal approach to probing**  
**accuracy**



**BOSCH**

Invented for life

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- Introduction
- Present Solutions
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- Measurement of Temperature and Displacement
- Concept of dynamic thermal shielding (DTS)
- Temperature Measurement w and w/o DTS
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# Introduction

- You cannot beat physics
- probing at high temperatures generates a very high amount of heat energy
- main problem is drift of X/Y/Z position
- Detailed explanation of these values and a model to explain these drifts are well known [Berger/Seitz, SWTW 2013]

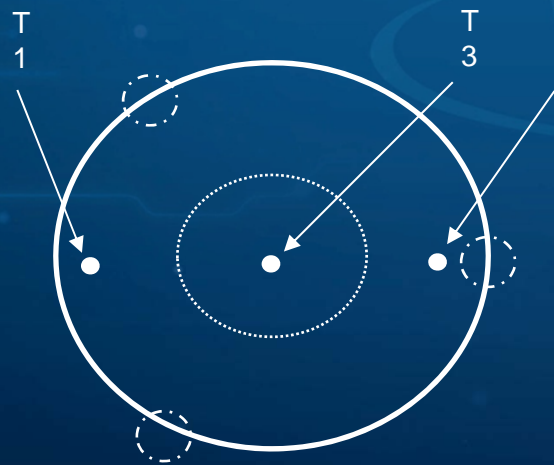
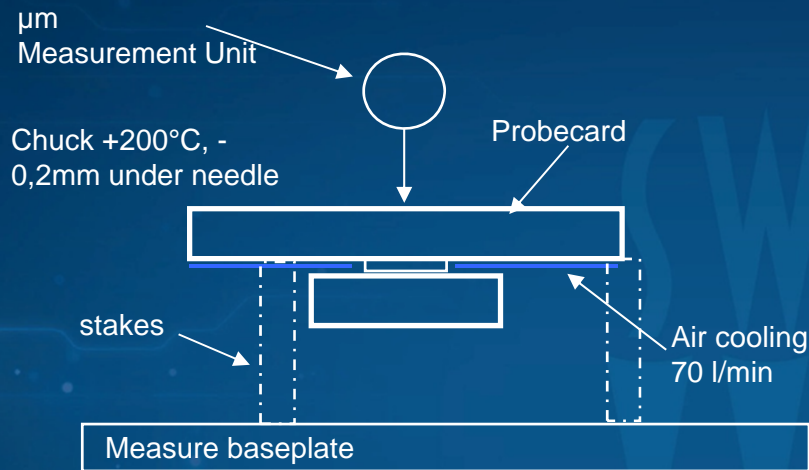
# Present Solutions

Method	Action	Advantage	Disadvantage
<b>Optical realignment</b>	correcting the drift	very accurate; no investment	time consuming; thermal disbalance while realignment; no control between realignments
<b>Pre soaking</b>	accelerates reaching a balanced situation	no investment	time consuming;
<b>Pre-heating of probecard and / or headplate</b>	accelerates reaching a balanced situation	faster than just soaking; not only probecard effected	time consuming; static, non local solution; cost of invest
<b>mathematical prediction</b>	Contol of position by temperature sensors an math. methodes	Local, no time loss	uncertainty remains (no controlling, no monitoring)

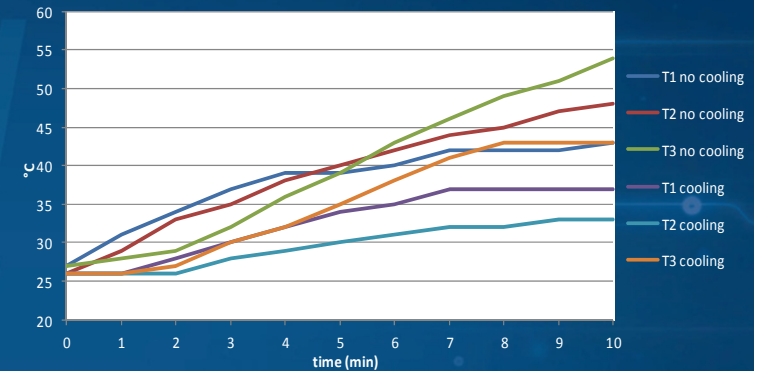
# Present Solutions

Method	Action	Advantage	Disadvantage
<b>Passive shielding</b>	prevents heat soaking for a certain time	few investment	Static, non local; retarding but not solving
<b>Cooling of probe card</b>	prevent heat soaking of probecard	Instant effect, no time lost; high invest	Static, non local
<b>“thermal design” of probecard</b>	Fit the design to high temperature use	Intrinsic solution, no other countermeasures	compromise to other PC features; expensive materials; high invest

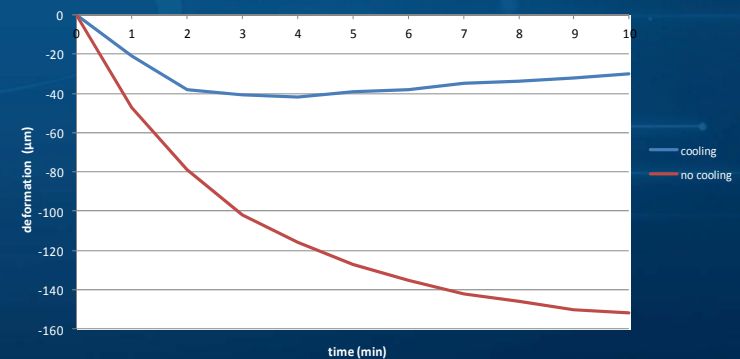
# Measurement of Temperature and Displacement



Process of Temperature

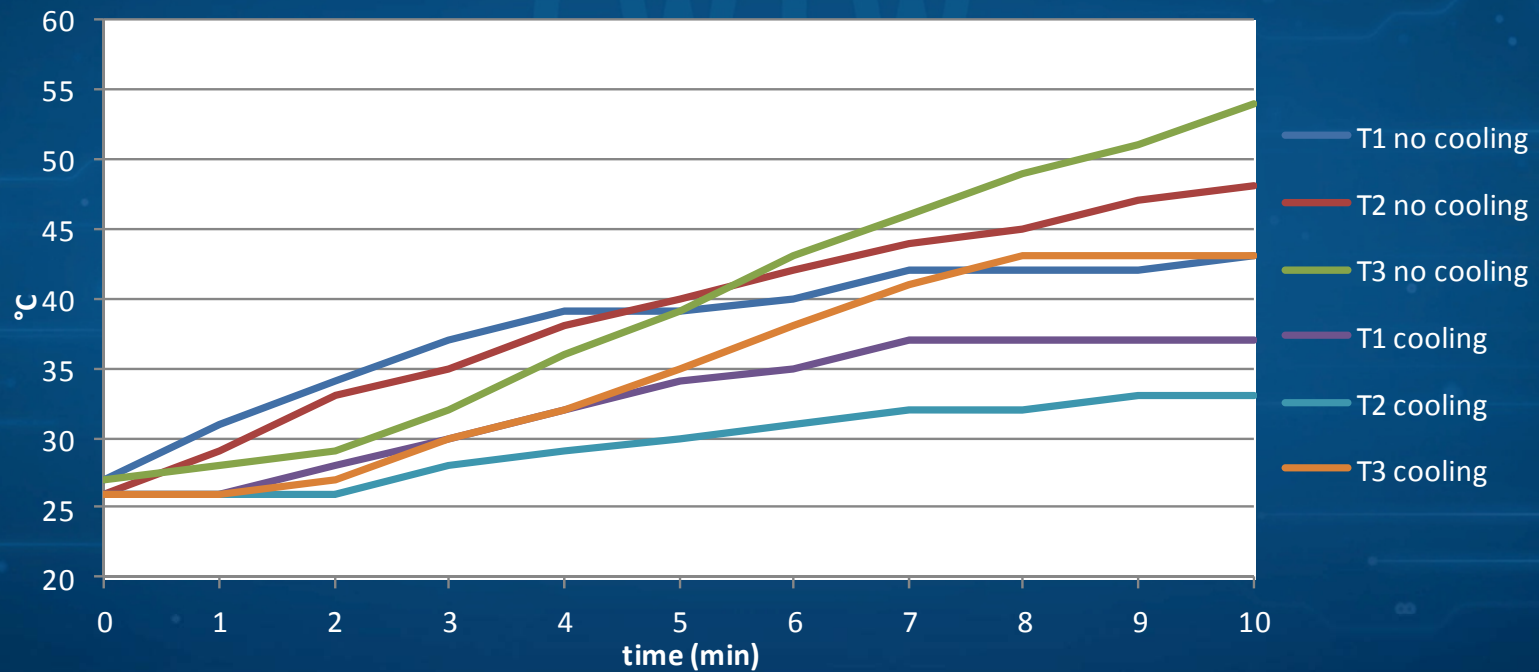


Process of Displacement



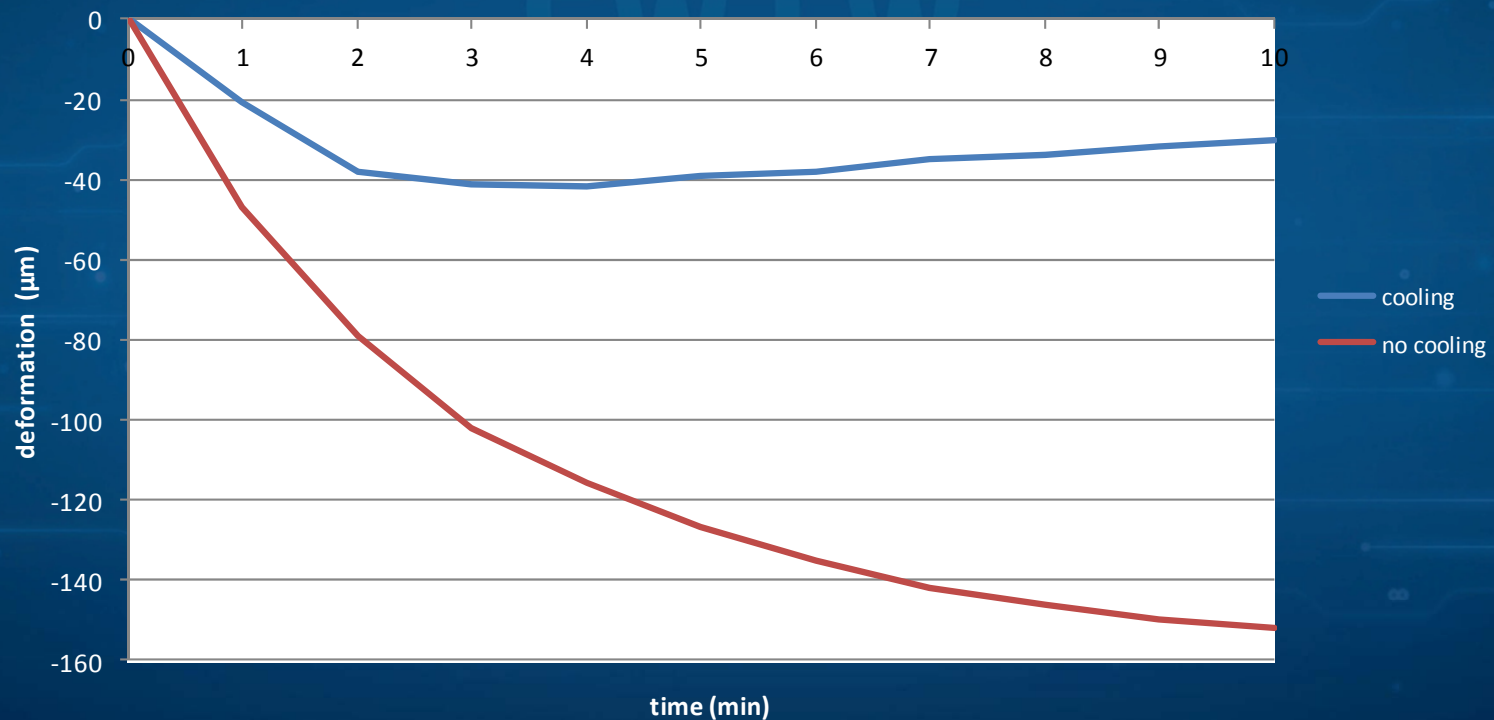
# Measurement of Temperature and Displacement

## Process of Temperature



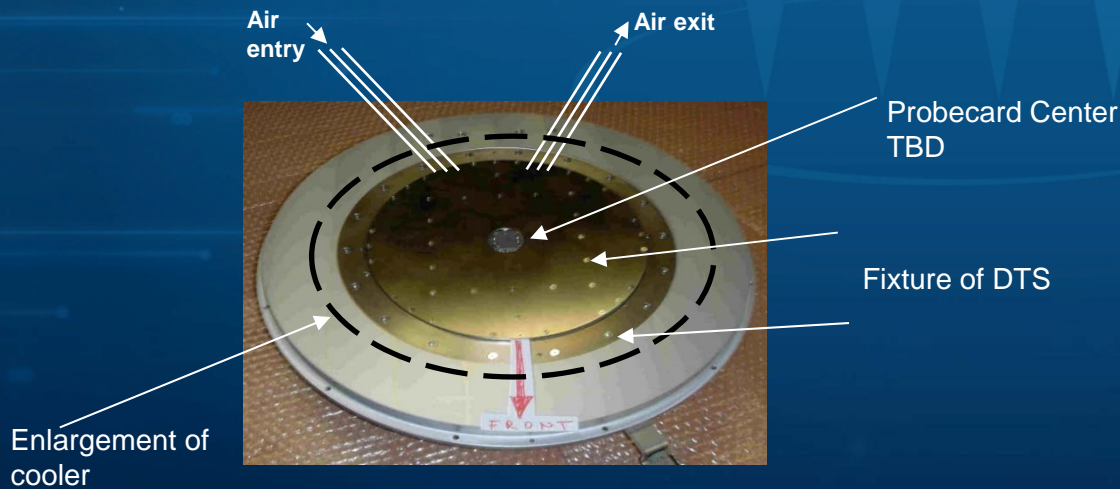
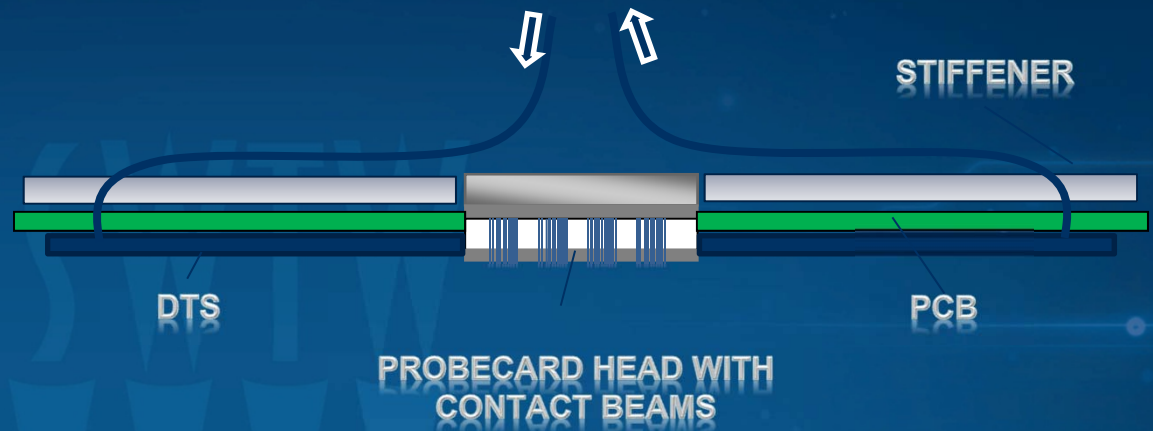
# Measurement of Temperature and Displacement

## Process of Displacement

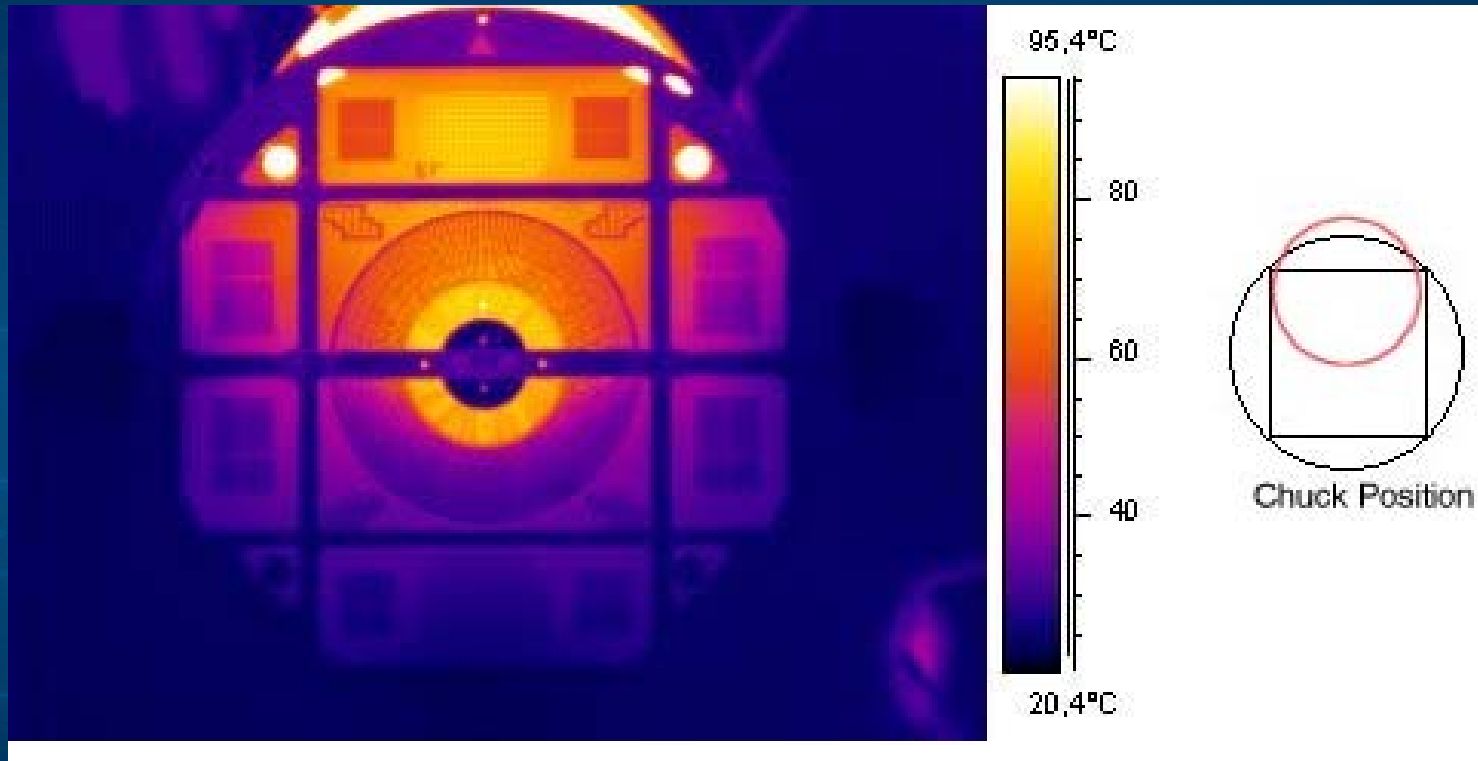




# Concept of dynamic thermal shielding (DTS)

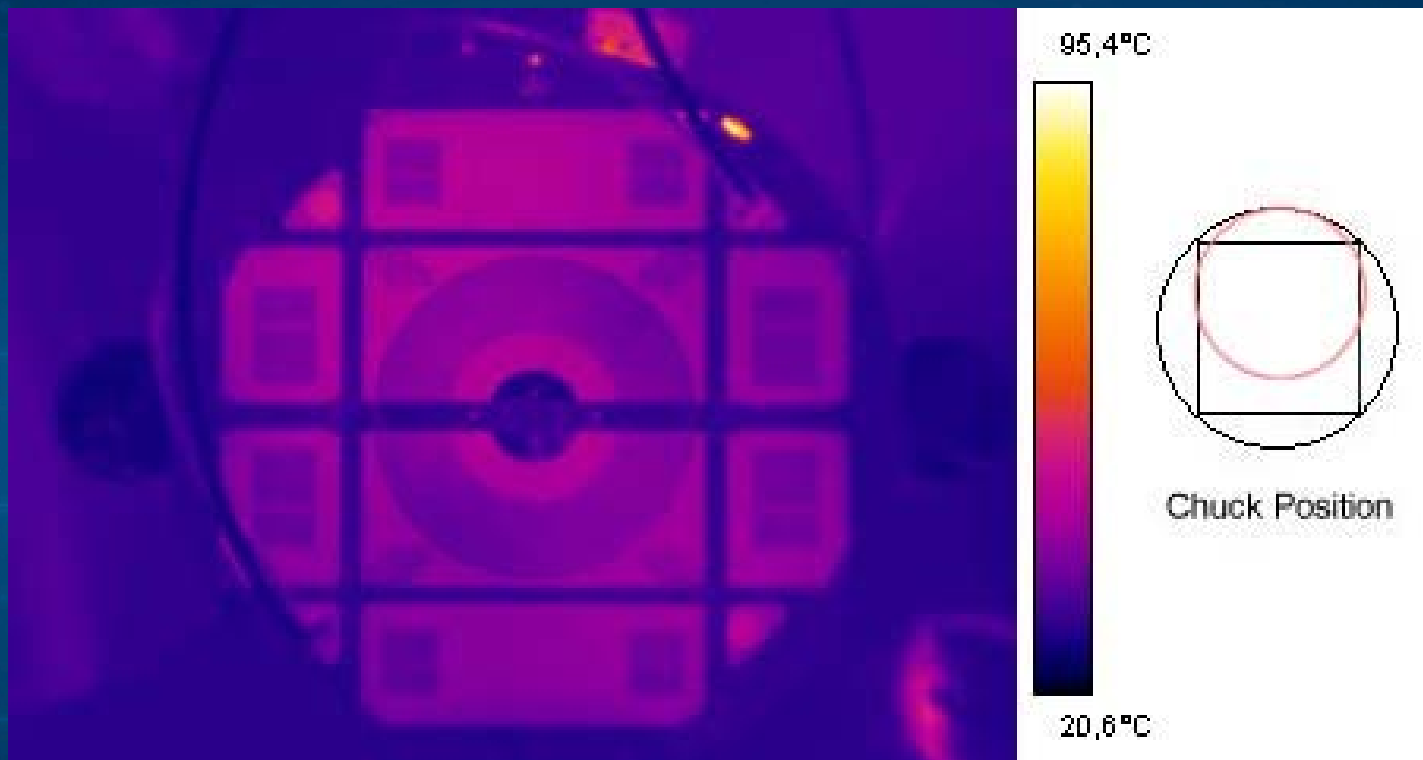


# Temperature Measurement w/o DTS



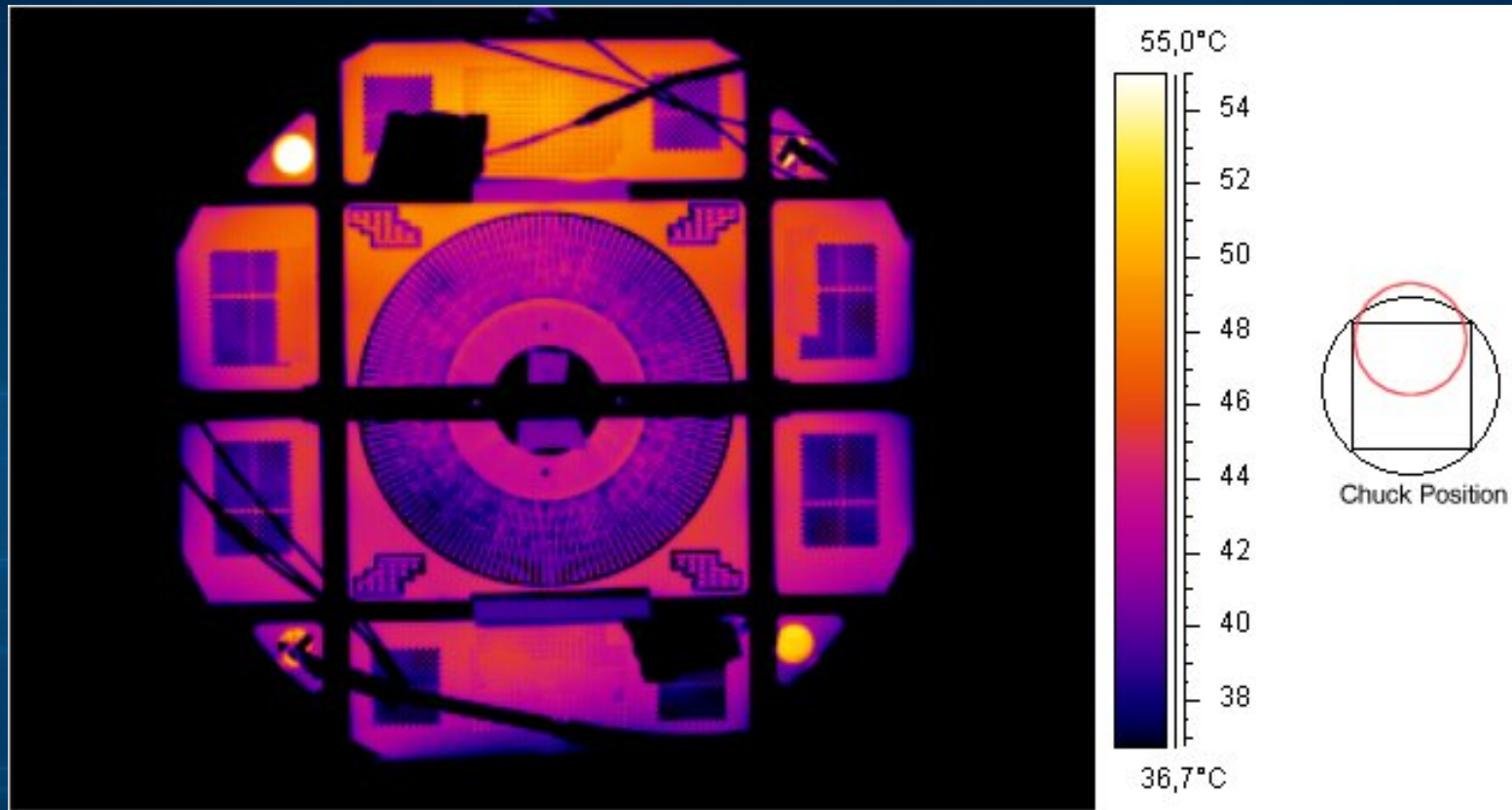
**Temperature distribution on Probecard with  
+165°C Chuck at rear position**

# Temperature Measurement w/o DTS



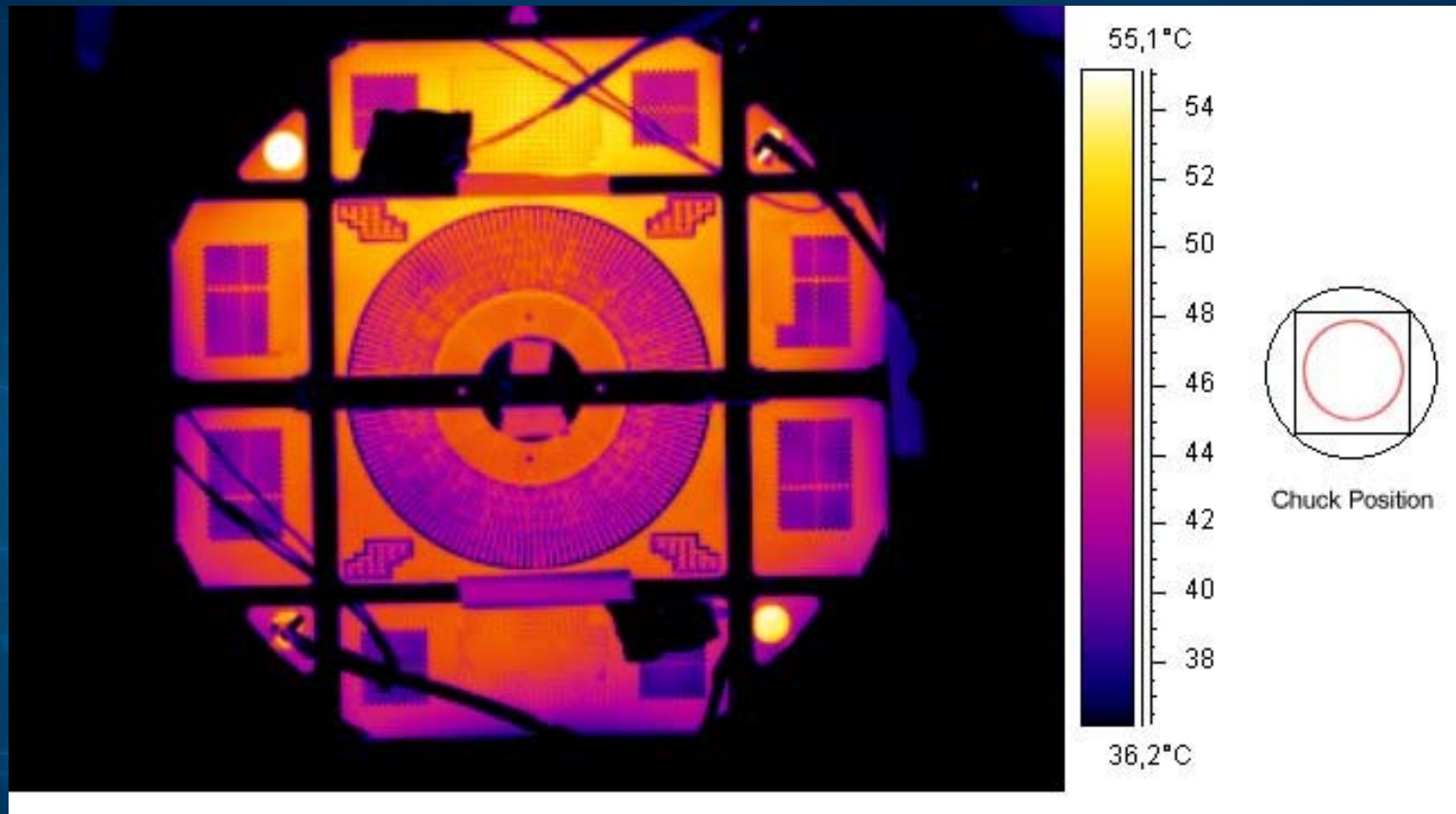
**Temperature distribution on Probecard with  
+165°C Chuck at rear position**

# Temperature Measurement w DTS



**Temperature distribution on Probecard with  
+165°C Chuck at rear position**

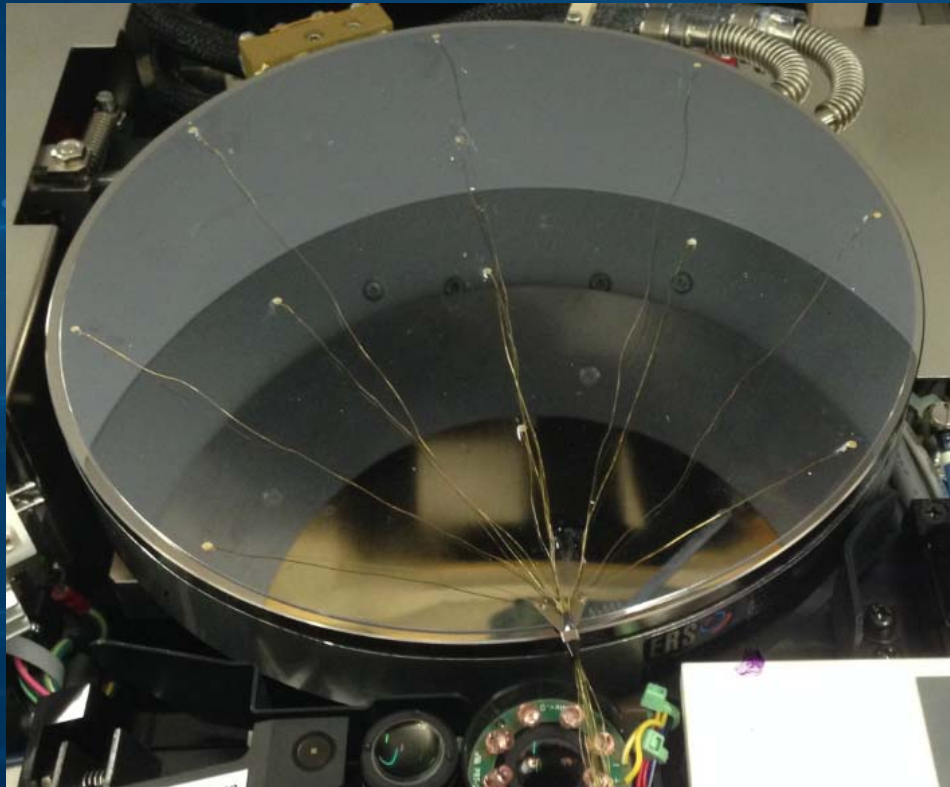
# Temperature Measurement w DTS



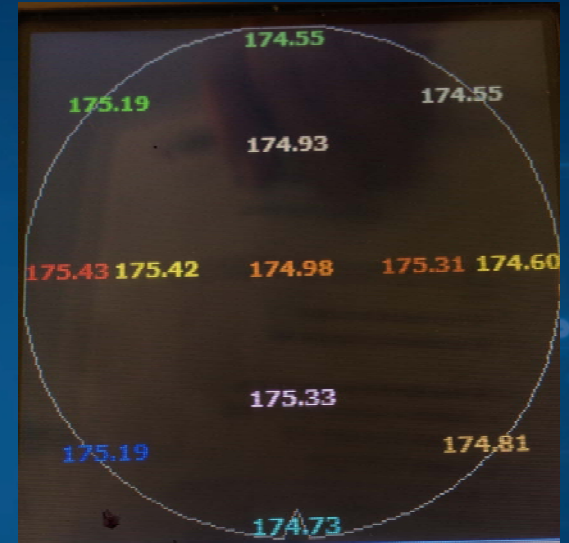
**Temperature distribution on Probecard with  
+165°C Chuck center position (no delay)**

# Feedback of dynamic thermal shielding (DTS) to wafer

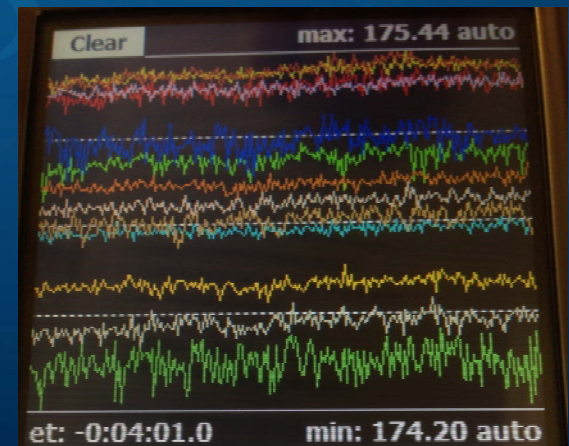
$T(\text{chuck}) = 175^\circ\text{C}$



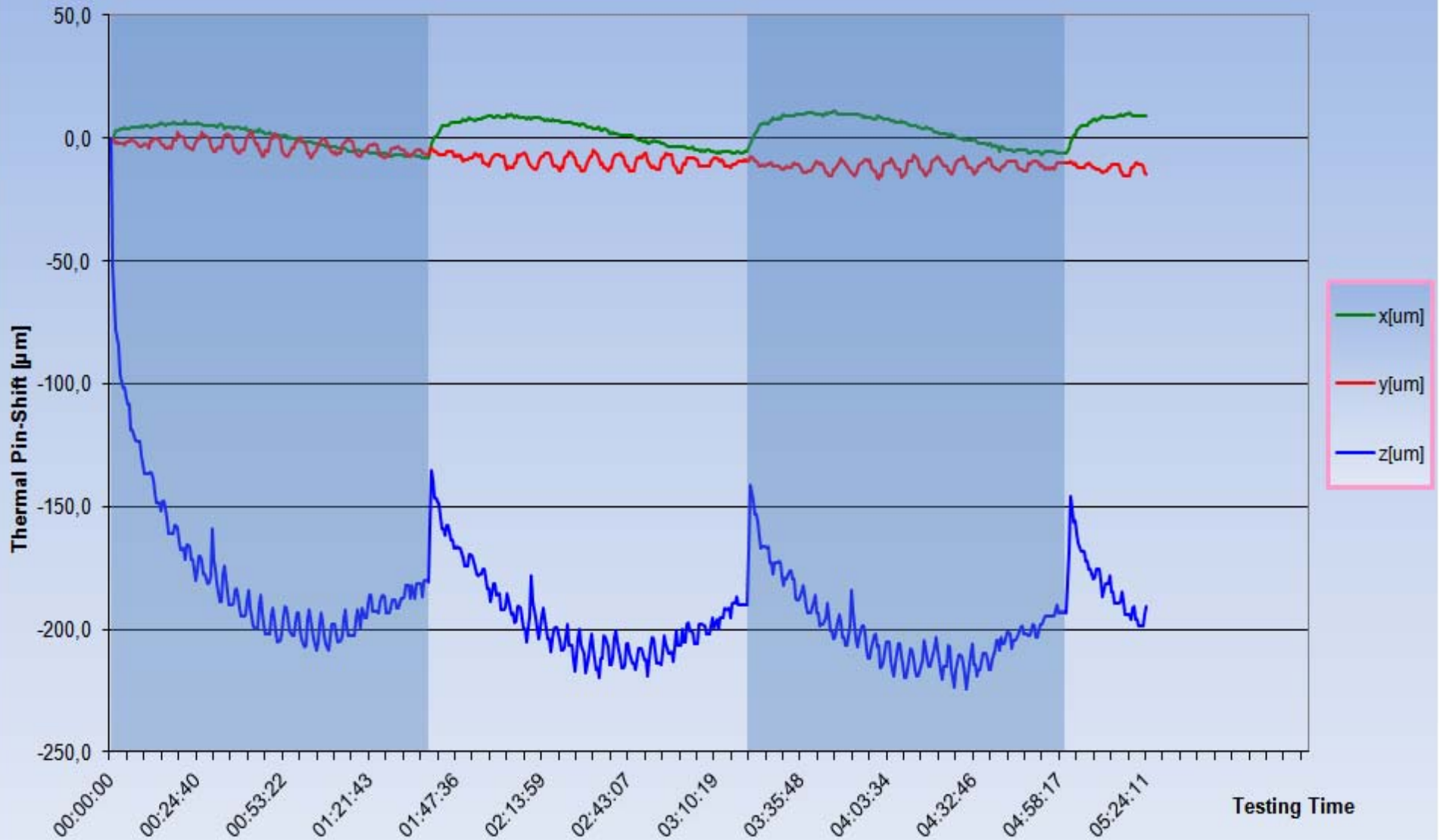
static



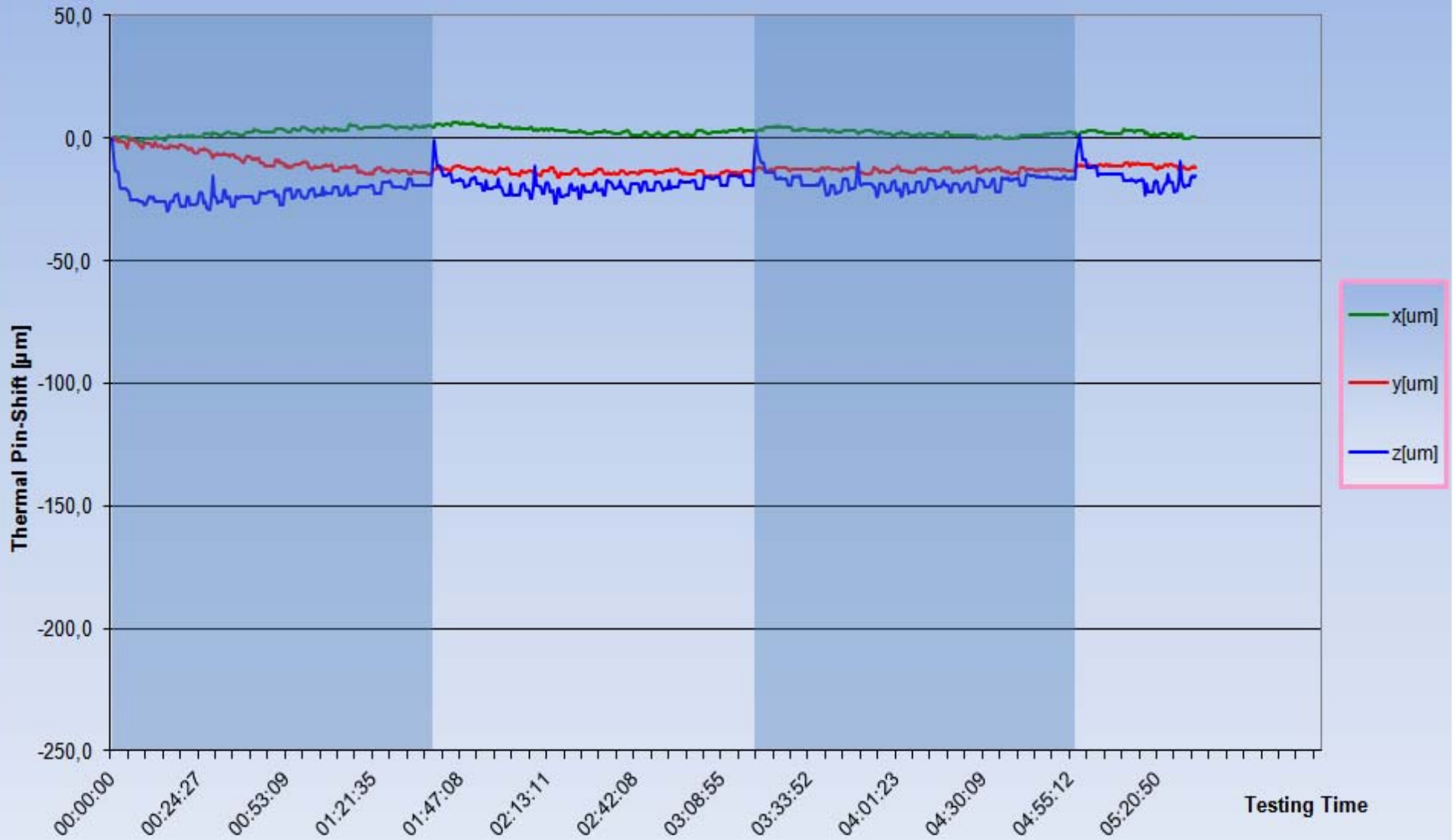
dynamic



### PC w/o DTS at 175°C Testing Temperature



### PC with DTS at 175°C Testing Temperature





# Number of realignments necessary:

Dimension	Probecard w/o cool shield	Probecard w cool shield
X	23	3
Y	14	6
Z	34	4
sum	71	13

# Outlook:

- **Thermal stabilizing of ceramic head will result in further improvement**
- **Docking of probecard has to be simplified for production**

# Summary:

- **Stable thermal equilibrium reached by cool shield**
- **Accuracy improved by factors**
- **Especially fast changes of heat source can be completely compensated**
- **Effort for realignment can be reduced dramatically**