

IEEE SW Test Workshop

Semiconductor Wafer Test Workshop

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New Pathfinding and Supplier Investigation Strategy



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Presentation Flow

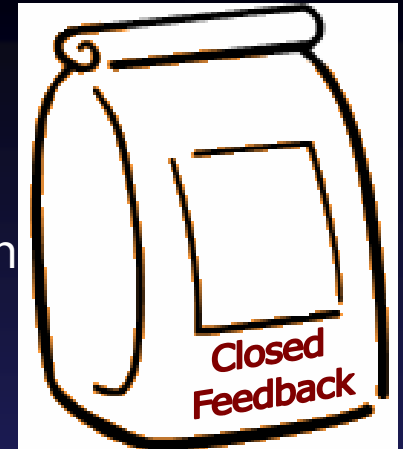
- Motivation
- Basic Methodology
- Detailed Methodology
- Comparison Past vs. Present
- Illustration of the Methodology
- Example
- Summary

Motivation

- Test costs are becoming our biggest barrier to success...
- Traditional Pathfinding wasn't yielding the necessary results
- We began the Next Generation Supplier Investigation Strategy (NGSIS) to deliver a revolutionary breakthrough



Main Ingredients



- Supplier data collected in Intel environment
- Feedback on limitations/improvements

- High quality reporting on same metrics
- Apples-Apples comparison



- Open to new suppliers
- Open to high-risk solutions



- Intel buys iterative test vehicles
- Supplier funds fundamental research

Standardized Data Collection



- **Problem Statement #1**

- Suppliers did not follow a standard methodology of testing their probing solutions or communicating their experimental results.
- An example:
 - Supplier A meets 0.5Ω Cres across 500k TDs
 - Supplier B meets 0.5Ω Cres across 500k TDs
- Impossible to compare results across the dozens of potential suppliers that Intel deals with.

- **Solution: Probe Olympics!**

- Standardized Methodology that is published to the supply base.
 - Allows new competitors quicker access to the market

Standardized Data Collection, cont...

- **Problem Statement #2**

- Success criteria was not clear for overall selection

- Assumption that excelling in one area could “compensate” for deficiencies in another
- Not all suppliers collected data on all of the requirements

- An example:

- Supplier A meets has 1.5A current carrying capability and 2g force
- Supplier B meets has 0.1nH inductance and 20m Ω probe resistance

- Impossible to judge which supplier is ahead.

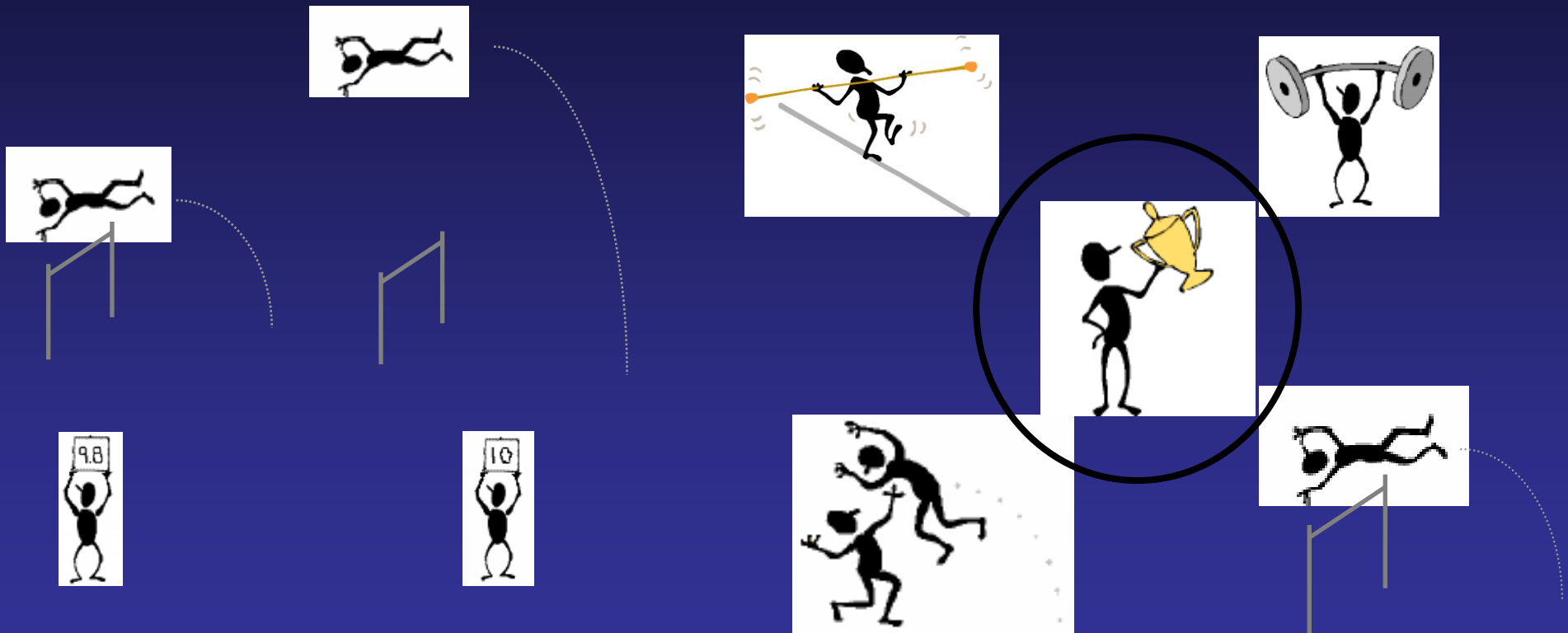
- **Solution: Probe Olympics!**

- Phase definitions that are published to the supply base.

- Fairer comparison across the whole supply base

The winner must perform well in all events...

"Probe Olympics"



Phase I

Scope: **Individual probes or test head**

Location: **Supplier** (*No Intel resources, gross reality check*)

Technical Areas:

- Probe Force
- Probe Current-carrying capacity
- Inductance (Probe & SIU)
- Probe Resistance
- Probe Lifetime

Phase II

Scope: **Integrated test head**

Location: **Intel lab**

Technical Areas:

- Alignment & Planarity
- Gram Force
- Module Integration
- Actual overtravel vs. Programmed overtravel
- Cres
- Offline Cleaning

Phase III

Scope: Full SIU
Location: Intel Mfg Floor

Technical Areas:

- Thermal Deflection
- Standing Die
- Scrub Mark Characterization
- Optimal Recipe
- Cres Stability
- Lifetime

Closed Feedback



- Running experiments on supplier's test vehicles in Intel environment
- Sharing experimental results and collaborating on technology improvements

Shared Commitment

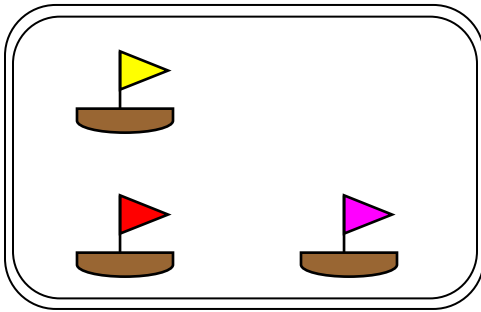


- Intel buys iterative test vehicles, not just selection SIU
- Supplier funds fundamental research, demonstrates technology envelope, validates models, etc

Distant Past vs. Present

PoR	NGSI
<ul style="list-style-type: none"> Established suppliers/technologies 	<ul style="list-style-type: none"> New suppliers with new technologies
<ul style="list-style-type: none"> Testing "off-the-shelf" solutions 	<ul style="list-style-type: none"> Creating technology with the supplier
<ul style="list-style-type: none"> Looking within established supply base 	<ul style="list-style-type: none"> Looking outside probe card industry
<ul style="list-style-type: none"> Evaluating only full solutions 	<ul style="list-style-type: none"> Enabling components (probes, ST's...)
<ul style="list-style-type: none"> Often deals with only one iteration 	<ul style="list-style-type: none"> Will require many iterations before clear results can be achieved
<ul style="list-style-type: none"> If a supplier fails, that's the end of engagement 	<ul style="list-style-type: none"> When a supplier fails, that's the beginning of engagement
<ul style="list-style-type: none"> Ordering a test vehicle and running experiments 	<ul style="list-style-type: none"> Evaluating initial models and designs, running single probe experiments, creating integrated probe card solutions
<ul style="list-style-type: none"> Clear methodology and explicit process 	<ul style="list-style-type: none"> Looser characterization, discovery paths
<ul style="list-style-type: none"> Results are easily interpreted 	<ul style="list-style-type: none"> Results often need to be "translated" to be input into a standardized analysis
<ul style="list-style-type: none"> Targeting a specific process generation 	<ul style="list-style-type: none"> Results may apply across generations

Holding Tank



- Supplier disengages from Intel to work on their own
- Intel publishes a clear "gap list" of items to close before re-engagement

Progress

Phase I

Phase II

Phase III

2009

SHOWSTOPPER
Limitations found

2008

Time

2007

- TH data collected at supplier
- Must meet Phase I req's

- TH data collected at Intel
- Must meet Phase II req's

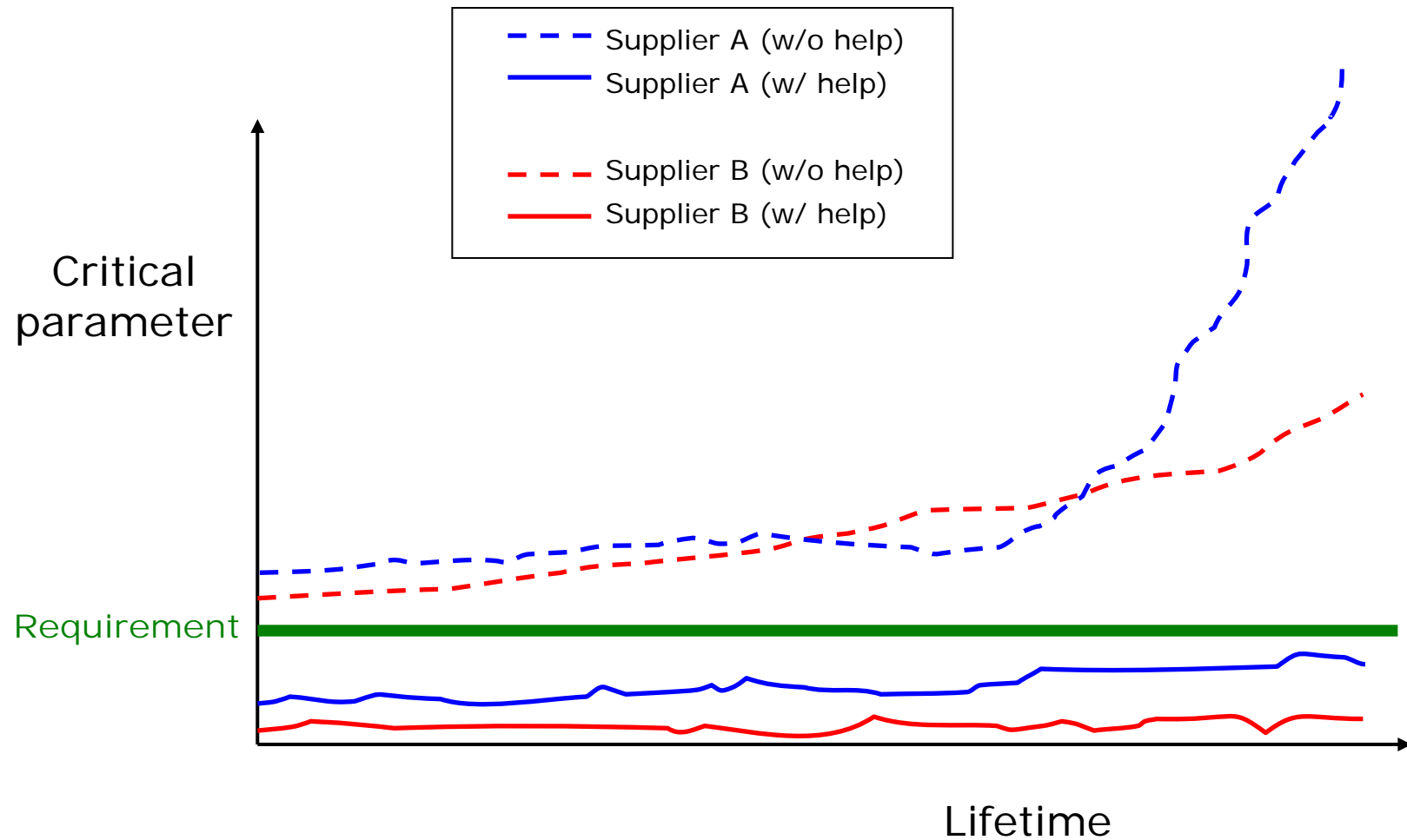
- SIU data collected at Intel
- Must meet Phase III req's

2006

2005

Limitations Fixed

The difference it can make...



Final Comments

1. Environment is different
 - Open to new suppliers
 - Open to high-risk solutions

2. Engagement is different
 - Standardized data collection
 - Closed Feedback
 - Shared commitment

- This is where we're going...
 - Are you coming with us?