IEEE SW Test Workshop Semiconductor Wafer Test Workshop

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ALTIS



A SMART PROBE-CARD DATA BASE A KEY TO SUCCESS



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Summary

- Altis company profile
- Probe card management deals with...
- "La DB Probe Cards"
 - Probe identification
 - Probe life in operation
 - Analysis
- Acknowledgements



Altis Company Profile



Altis International – Company Overview

Location: Paris area, France (HQ, facilities)

Wafer foundry BU: Capacity 35k wafers/month (8 inches)

250nm→130nm, 75% w/ Cu process

Test & Packaging BU: Largest independent test capacity in Europe

Turn Key (wafer fab + wafer test) model

Test House (test only) model





June 12 to 15, 2011

Altis Test & Packaging – BU Overview

- Altis Test & Packaging BU is offering:
 - Flexible and adaptive support, whatever your project is
 - World class cost competitive solutions
- Versatile toolset & capabilities
 - M/S, digital, analog test (Teradyne J750/E & Catalyst, Verigy 93000…)
 - Automatic inspect^o, temperature cycling, laser fusing, inking...
 - Long track of record on wafer test (advanced probing, high //, temp testing)
 - Proven experience of test dev, final test, packaging subcontracting
 - Automotive (ISO TS 16949) and security/crypto certifications



Probe Card management deals with...



My life of probe card engineer in figures

- 3000+ probe-cards and probe-heads
- x1 to x500 paralellism
- 8 different test platforms (PCM test, wafer test...)
- 15+ PC suppliers
- 2 types of PCs (removable heads or fixed ring)
- 6 PC technologies (cantilever, membrane, micro springs...)
- 3 different off line cleaning protocols
- 6 different on line cleaning materials
- Many customers (with their own requirements each)
- 100% serviceability commit given by my boss

... makes millions of reasons for me to get mad about it!



Solution for this nightmare situation

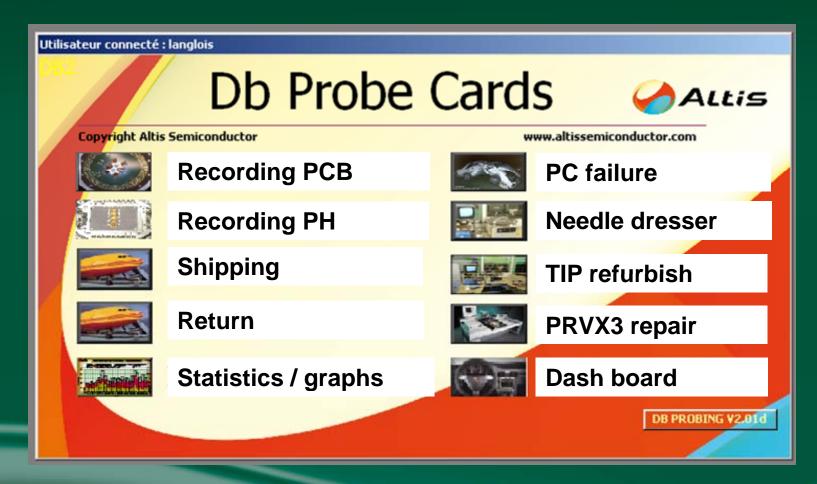
- Let's manage PCs using a dedicated IT tool and call it **DB Probe Cards**
- This DB Probe Cards is a fact, as it is today existing at Altis
- Main features are
 - It is a reliable system (24/7 availability)
 - It offers exhaustive event logging and analysis
 - It enable traceability management
 - It is secured through several levels of access (operator to engineer)
 - It is flexible for further evolutions
- Its development was done in close loop with all probe cards engineers and technicians



"La DB Probe Cards..."



DB Probe Cards entry panel





Those questions are now answered in a click!

Which information are related to and needed for this PC?

Identification

Where is this PC located?

How to know which PCs need maintenance?

How to log maintenance was done for this PC?

How to prevent from using this PC?

How to inform this PC needs maintenance?

Analysis

ife in operation

Do I have the right amount of PCs in production?

Am I sure all my PCs are equally reliable?

How many TDs for this PC?

What is the history of my PC/PH?

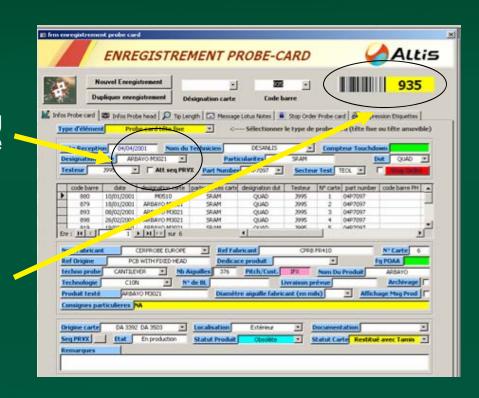
What is the status of my PC fleet?

Am I sure there's no over maintenance on my PCs?



Identification

- Each PC/PH is identified upon first delivery to Altis
- Exhaustive list of information is linked to each hardware, ranging from product name or tester type to reception date or supplier reference
- A unique id / barcode is then placed on the probe card box, PCB and probe head





Life in Operation: Where is the PC, is it useable?

- DB Probe Cards provides the operator with real time information about location of probe card:
 - in production floor
 - in probe maintenance area
 - at technician desk
 - shipped @ external repair...
- but also about the status of the probe card
 - available for production
 - waiting for test engineer validation
 - loaned to subcontractor
 - Returned to owner...
- This enable to significantly reduce time spent on search for probe cards





Life in operation: failure form recording

- Fail / Issue tracking is done by operator
- Each failing hardware is tracked using its barcode
- Simple GUI w/ predefined choices allows fast and simple form fill in
- Comment is allowed to add more info if needed
- In case a tester is stopped, higher priority is given by system







Life in operation: which PCs need maintenance?

Dashboard tool gives at a glance the status of all PCs pending analysis, by tester type and analysis type

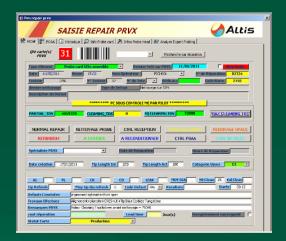




Life in operation – Repair form recording

- After doing any maintenance on probe, the technician is measuring probe-tip length/extension or bump height
 - allows for preventive maintenance
 - improves test quality
 - helps to anticipate probe repair
- Once the repair is over, technician is updating repair form
 - type of repair
 - reset of cleaning counter
 - update of measured data

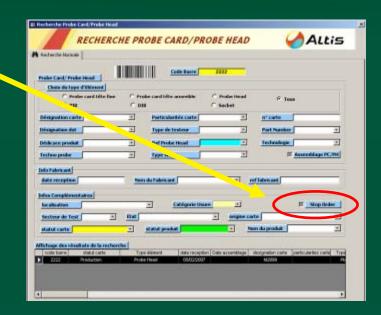






Life in operation – How to prevent from using this PC?

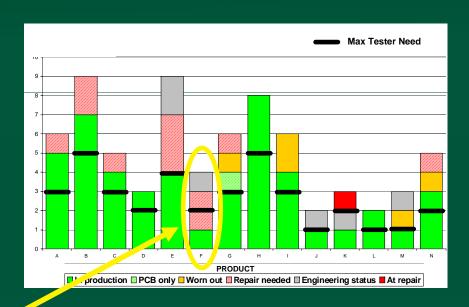
- In case a PC has a problem, it <u>must</u> not be used in production
- A specific flag set by the engineer indicates this status
- This flag is propagated to the test logistic
- Operator <u>will not</u> have the possibility to use this probe card for productive test





Analysis: Do I have the right number of PCs in production?

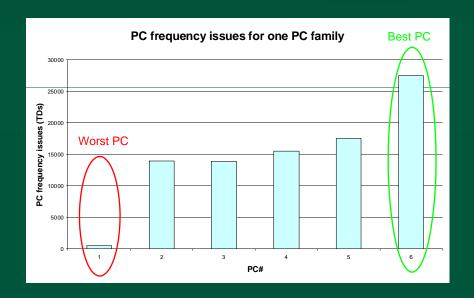
- Using volume forecast from capacity planning, it is possible to check for available PC
- Little extra analysis required for this checking
- Checking is performed against the real status of PCs (in production, ar repair...) coming from DB Probe Cards
- It enables to highlight priorities and to reduce risk of serviceability exposure due to lack of PCs





Analysis: Am I sure my Pcs are equally reliable?

- Assuming same manufacturing, all PCs should show equivalent
- Maintenance information logged in the DB Probe Cards allow to easily assess performance of each PC
- These results enable to focus maintenance on PCs w/ high failure rate
- And these results also enable to challenge PC manufacturer...





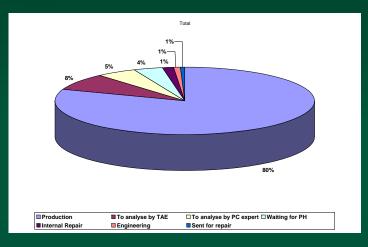
Analysis: How many TDs for this PC?

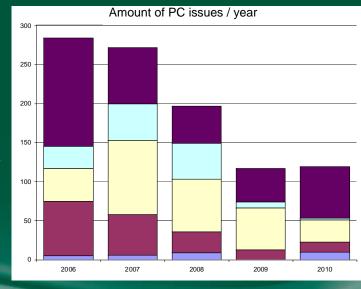
- Amount of touchdowns is a key indicator for a PC
 - Monitor of PC real life time
 - Threshold to trigger specific events (off line cleaning, reorder anticipation...)
- It is however not always easy to compute
 - All test insertions must be taken into account
 - Retest should not be forgotten
 - Also selective retest (that is partial retest of the wafer) must be accounted for
- DB Probe Cards is doing all this
 - Registration of TDs
 - Use of a threshold for each product/probe card to set the right offline cleaning frequency
 - Helps to reduce retest rate
 - Enables to anticipate end of life of PCs with respect to theoretical life time



Analysis: What is the status of my PC fleet, how can I improve the availability of PCs?

- It is possible, at any time, to get a clear view of PC fleet and status
- This view can be tailored upon specific need (by customer, by product, by tester...)
- Using information from DB Probe Cards about PC events (nature, frequency), it is possible to build a pareto of detractors of PC issues
- This pareto is then used to define improvement actions on main detractors







Analysis: What is the history of my PC/PH?

- There is a need to get traceability on probe card / probe head matching
- DB Probe Cards allows to maintain exhaustive history on probe head / probe card history
- Also, DB Probe Cards maintain full history of PC during its use in production (which tester, which – repairs...)

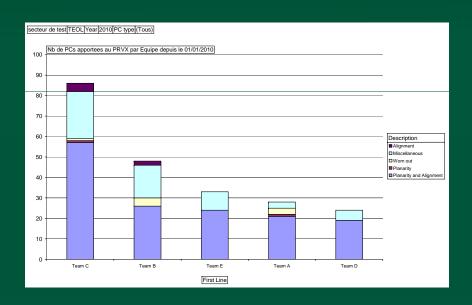


Date Interv	N° Interv	Code Barre	Nº Carte	Type de Testeur	N° de Testeur		Description du defaut	Travaux effectués	Moy Tip Dia	Résultat PRVX
30/01/2008	61197	1588	4	3750	14	NE		Nettoyage gel pack wafer + carbide		
05/02/2008	61270	1555	4	3750	17	NE		Nettoyaga gal pack wafer + carbida		
25/02/2000	61763	1555	4	3750	13	NE		Nettoyage cleaning sheet XD41HT20HG + carbide water		
01/03/2008	61902	1555	4	3750	37	NE	dx3 à 0%	Nettoyaga pinceau et alcod + contrôle visuel		
05/03/2008	61910	1588	4	3750	0	NE	carbide + gel pack à faire	Nettoyage gel pack wafer		
25/03/2008	62381	1555	4	3750	0	NE		Nettoyaga gal pack wafer		
01/04/2008	62561	1555	4	3750	31	NE		Nettoyage gel pack wafer		
10/04/2008	82757	1555	4	3750	5	NE		Nettoyage gel pack wafer		
02/05/2008	69258	1588	4	3750	37	NE	nettoyage + vérification Dut 0 (channel 90) info de K.Benhissou	Nettoyaga pincasu et alcod + contrôle visual		
05/05/2008	69259	1555	4	3750	0	NE		Nettoyage cleaning sheet XEM1HT20KG + carbide wafer		
26/05/2008	63722	1555	4	3750	13	NE		Nettoyaga gal pack wafer		
04/05/2008	64004	1588	4	3750	0	NE		Nettoyage gel pack wafer		
02/07/2008	64618	1586	4	3750	14	NE	problème DUT 3, mausais rendement	Nettoyage sur carbide tungstène en mode RFI T2 + contrôle visuel		
03/07/2008	64620	1555	4	3750	0	NE		Nettoyage gel pack wafer		
05/07/2008	64689	1555	4	3750	17	NE		Nettoyage gel pack wafer		
2407/2008	65151	1555	4	3750	30	NE	PB dut 2 (sorts 7 SB 30)	Nettoyage sur carbide tungstène en mode RFI T2 emepure cres + contrôle visuel		cpt_0004.u
25/07/2008	65186	1588	4	3750	0	PAL	Rull test PRVX	Afgrement+planeite+CRES+LK+Tip Dia+Carbide Tungstène	0.68	opt_0005.w
06,011/2008	65453	1555	4	30'50	14	PAL	probe retiré prod => à verifier puis à donner à Karlim pour essai	Alignement + planeite + CRES+LK+Tip Dia + Carbide Tungstène	0.71	cpt_0039.ui
13/01/2000	65578	1555	4	3750	32	NE	aceval	Nettoyage gal pack wafer		
16/08/2008	65628	1555	4	3750	32	NE	nettoyage preventif avant test	Nettoyage sur carbide tungstène en mode FFT T2 + contrôle visuel + gel pack sur needle dresser		
26,016/2008	65762	1588	4	30%0	32	NE	Nattoyage de I pc pour pb perte de rendement	Nettoyage gel pack wafer		
59005/2008	93978	1565	4	3/50	24	rec		remiyage gel pack wafer		
05/09/2008	66043	1588	4	3750	32	NE		Nettoyage gel pack wafer		
11/09/2008	66051	1588	4	3750	32	NE	perte de rendement sur dut 0 et 2	NEANT MOSE ALIPRIAN DE LA PODEMANDE DE LANGOS		
11/09/2008	66151	1555	4	3750	0	NE	VOIR NOTE DE LANGLOIS	Nettoyage sur carbide tungståne en mode FFI T2 + Gall pack + mesure cres + contrôle visual		opt_0044.u
12)05/2008	66177	1588	4	3750	7	NE	aceval	Nettoyage gel pack wafer		



Analysis: Am I sure there is no over maintenance on my PCs?

- Assuming normal activities, one should expect no difference in PCs wear out between production shifts
- Again, DB Probe Cards enables to output statistics such as return rate by shift or by operator
- It is then possible to check and analyse specific behaviour
- Eventually, corrective actions are done to level jam rate by shift/operator





Summary

- The DB Probe Cards is an all-in-one system
 - It fits operator needs (GUI for all activities, used to let him know which probe card to use...)
 - It fits technician needs (friendly user interface to know which PCs to work on, possibility to input all maintenance info for each single PC...)
 - It fits engineer needs (improvement and fine tuning of PC efficiency is easily manageable, better cost of ownership is then made possible for the end customer)

DB Probe Cards is the Swiss knife of the probing team!



Many Thanks to...

•	Julien. Harrault (PC coordinator)	Altis
•	Patrick Buffel (DB administrator/ PC technician)	Altis
•	Jean-Marc Desanlis (PC administrator)	Altis
•	Frederic Fontaine/P. Drugeon (PC technician)	Altis

And do not hesitate to contact us:

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End of the presentation

Thanks for your attention

