

# IEEE SW Test Workshop

Semiconductor Wafer Test Workshop

June 12 to 15, 2011  
San Diego, CA



## Kevin Fredriksen / MSO – Multi Site Optimizer For IEEE SW Test Workshop



Kevin Fredriksen, SPA GmbH  
Germany

Customer support / Sales

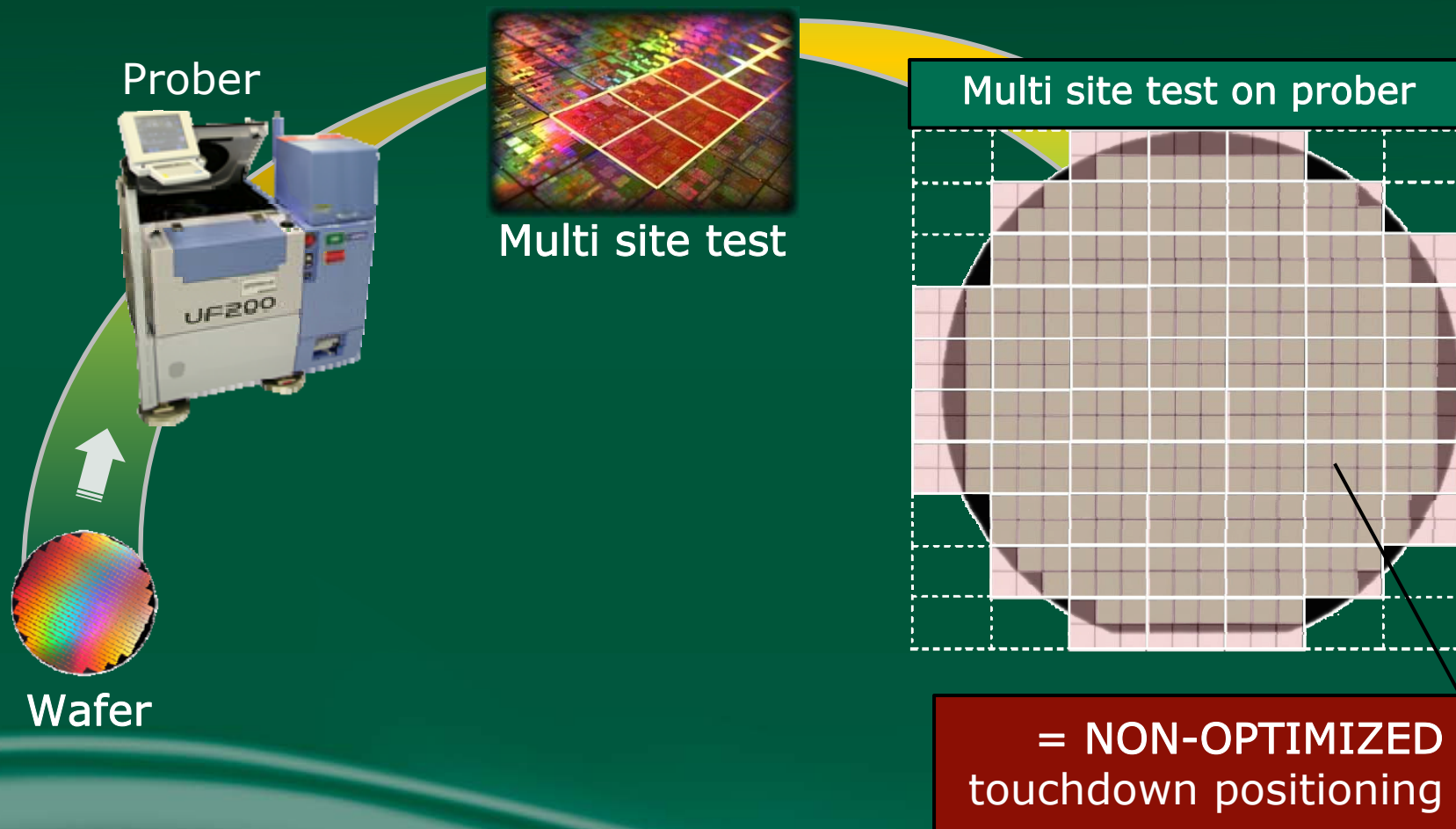
# OVERVIEW

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- MSO HISTORY
- MSO basics and facts
- Optimization example
- Multi site layouts & touchdown options
- Stepping path algorithms
- MEMS wafer test – a multi site quest!
- Summary



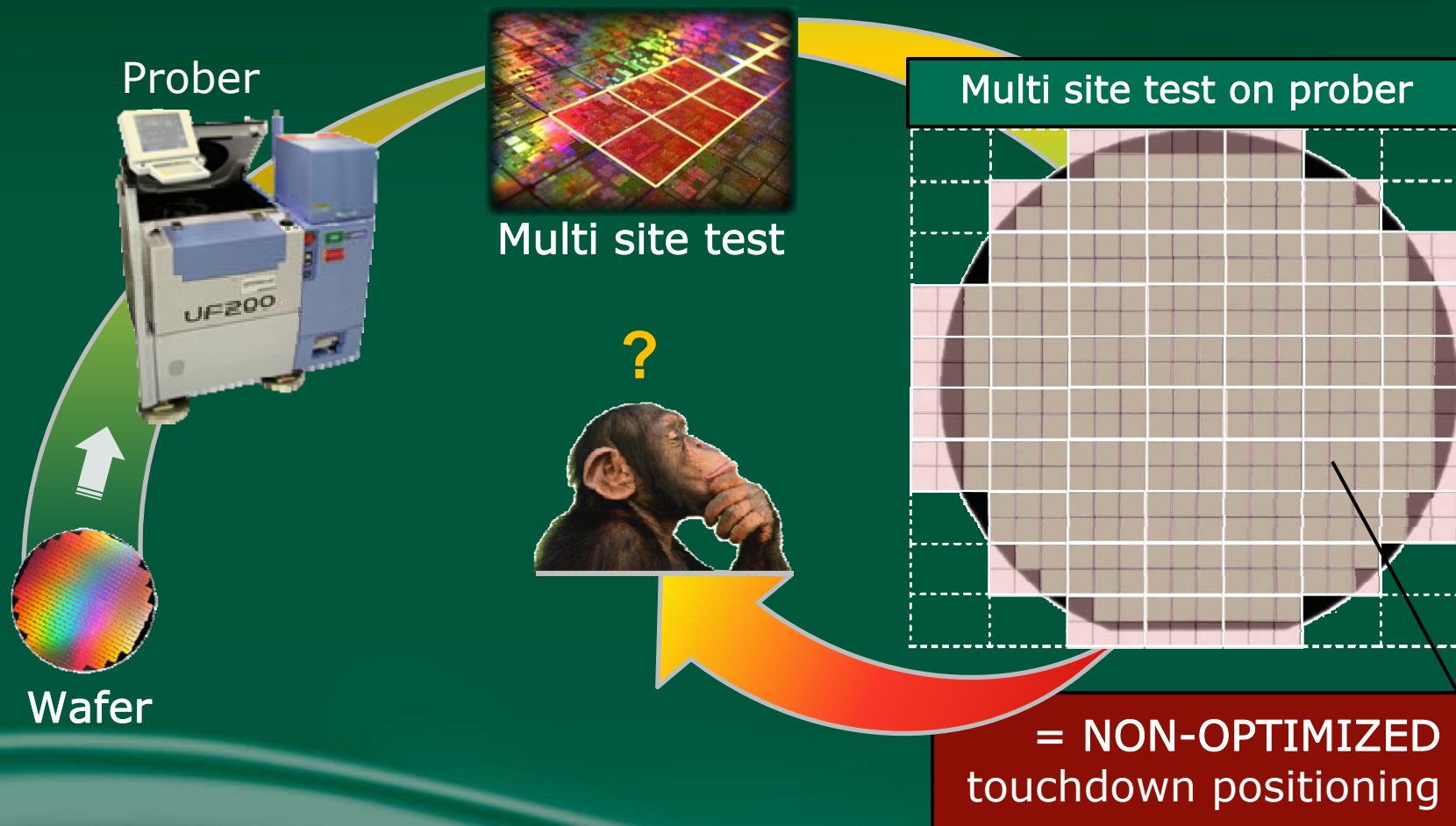
# MSO HISTORY



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# MSO HISTORY



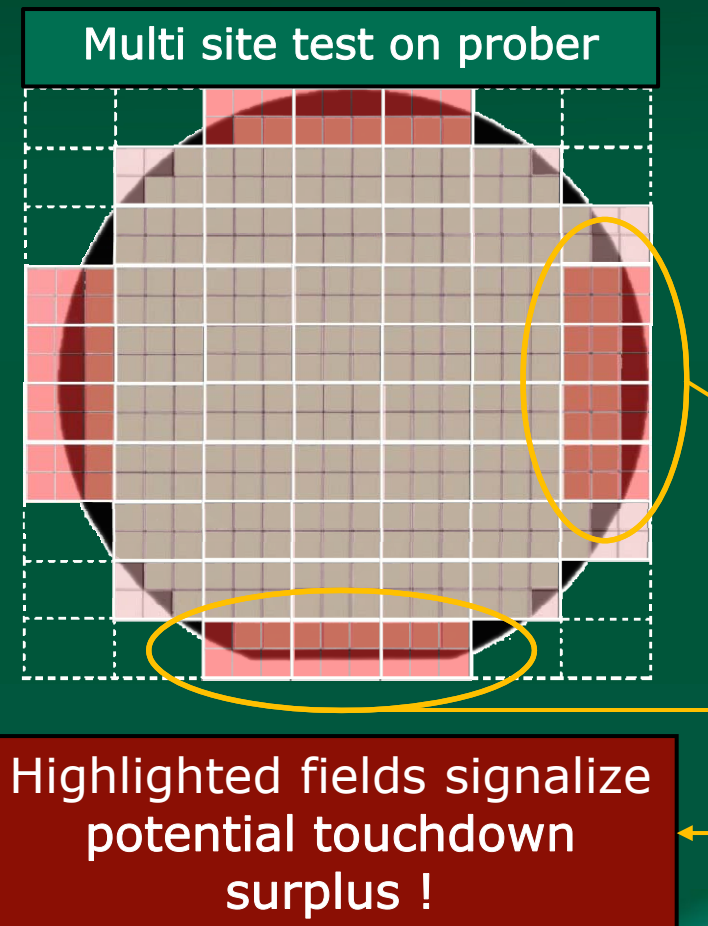
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# MSO HISTORY

## MULTI SITE TEST SITUATION

- Most probers do not supply any intelligent stepping algorithms
- Unnecessary high amount of touchdowns to process
- As a result: long process times





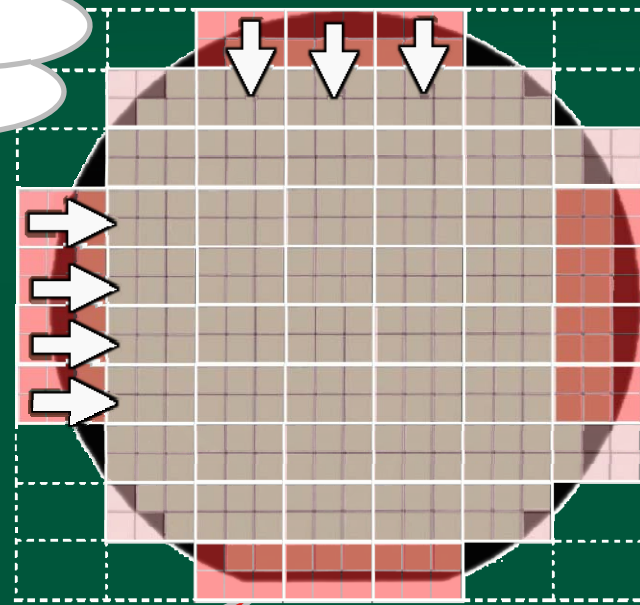
# MSO HISTORY

*Create a program that minimizes touchdowns by automatic shifting of touchdown coordinates.*

**MSO**  
Multi Site Layout Optimizer

**SPA**  
SOFTWARE FOR  
PRODUCTION  
AUTOMATION

**IS-TEST**  
GmbH



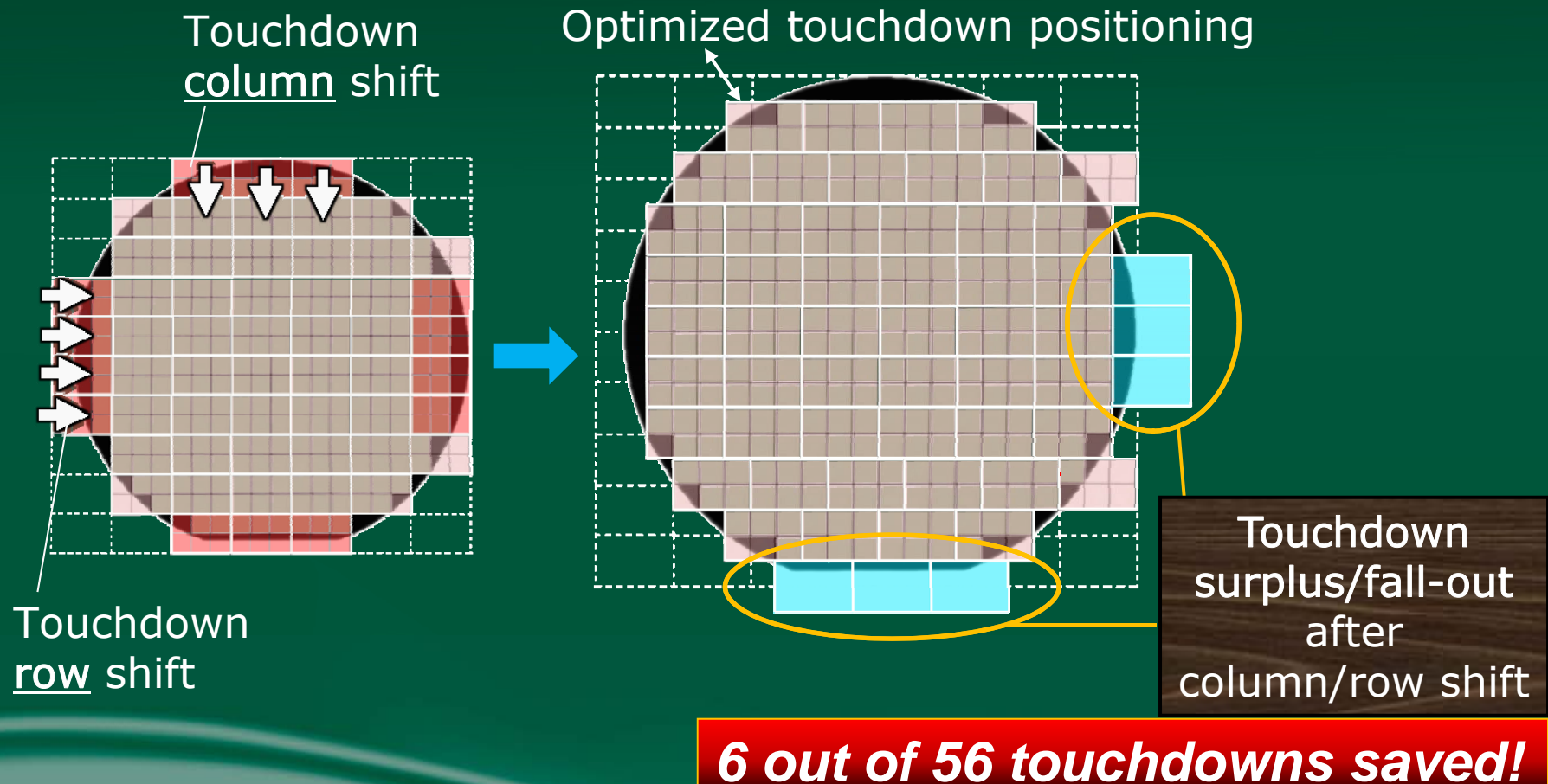
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6

# MSO HISTORY

PRIMARY GOAL: minimize total amount of touchdowns



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# MSO

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## *Basics and facts*

The benefits and usability of MSO



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8



# INTRODUCTION

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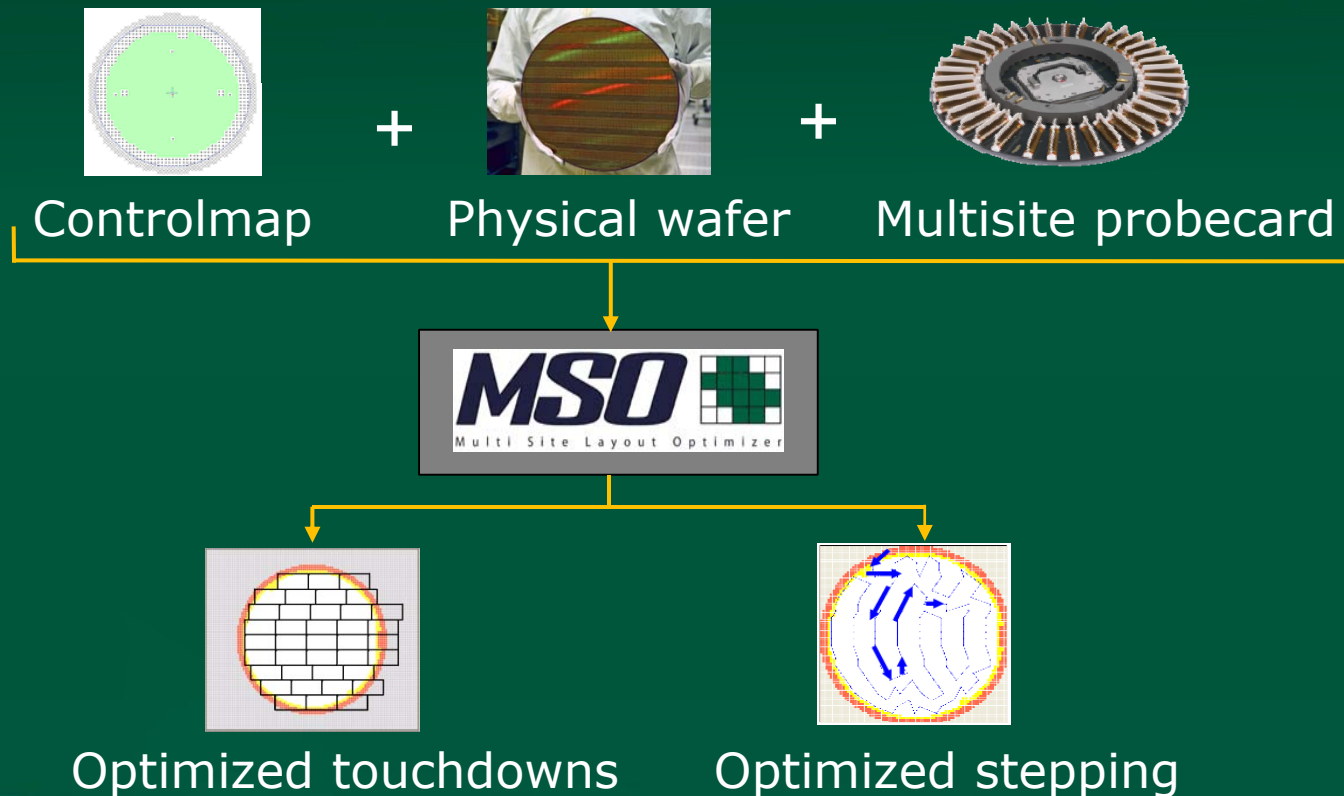
The Multi Site Layout Optimizer (MSO) is an easy to use software for optimizing multisite layouts.

***The benefits:***

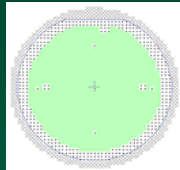
- ➔ ***Less touchdowns***
- ➔ ***Optimized probing path***
- ➔ ***Optimized hot probing***
- ➔ ***Find ideal probecard layout***



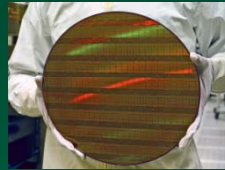
# MSO workflow A



# MSO workflow B



+



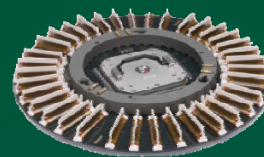
+



Controlmap

Physical wafer

Multisite params



Ideal multi site  
probecard layout



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11

# MSO

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Example

## *Complete Optimization*

Touchdowns and stepping path

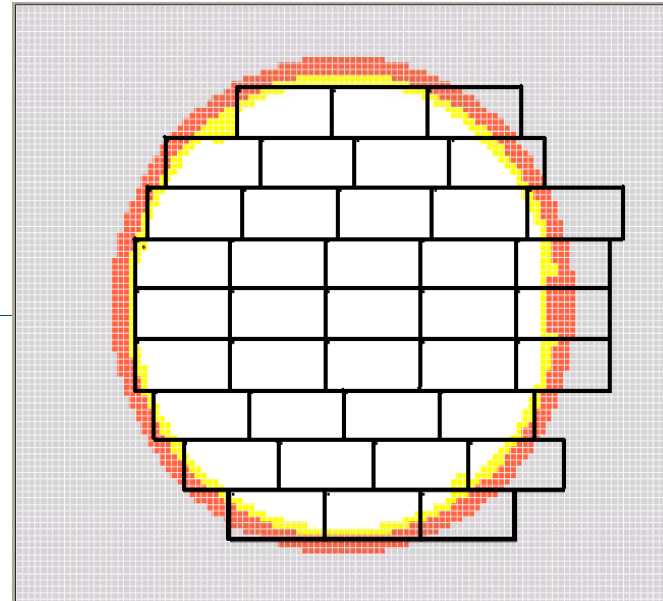
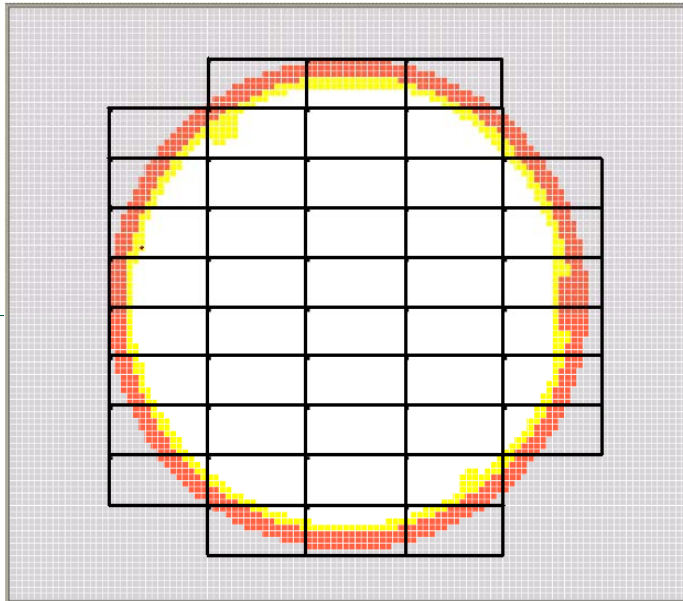


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12

# MINIMIZE TOUCHDOWNS



Prober: 44 Touchdowns → MSO: 38 Touchdowns

Technology: automatic column / row shift

*= 14,6 % TD reduction!*

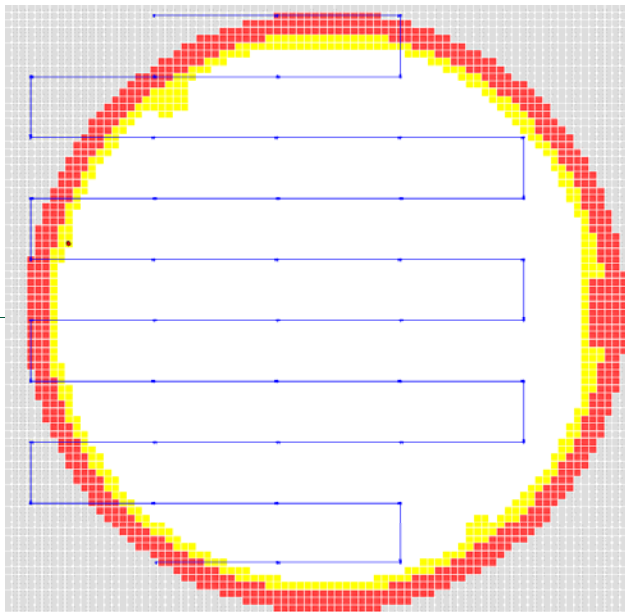


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13

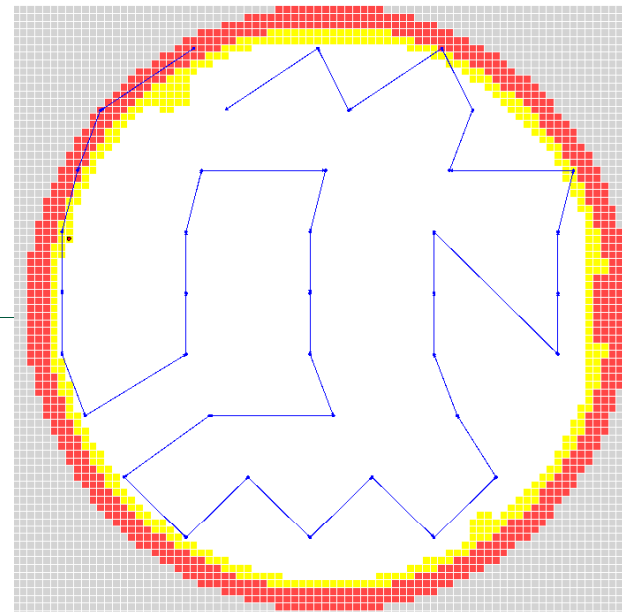
# OPTIMIZE PROBING PATH



Prober stepping: **1680mm**



MSO stepping: **1056mm**



Technology: shortest way

**= ~37 % Stepping reduction!**



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# TEST-COST REDUCTION

## *Wafertype/Prober parameters*

Dies to test / Wafer = 3.823

Test Time / TD = 5 sec.

Number of DUT = 128

X/Y Stepping / sec = 100mm

	Non-optimized	MSO
Number of TD / Wafer	44 TD	38 TD
Test time / Wafer	220 sec.	190 sec.
X/Y Stepping / Wafer	1680 mm	1056 mm
Stepping Time / Wafer	17 sec.	11 sec.
<b>Total Test Time / Wafer</b>	<b>237 sec.</b>	<b>201 sec.</b>
<b>Process time reduction /wafer = 36 sec.</b>		<b>= ~15%</b>



## TEST COST REDUCTION!



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15

# *MSO*

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## *Feature Overview*

Multi site layouts and touchdown options



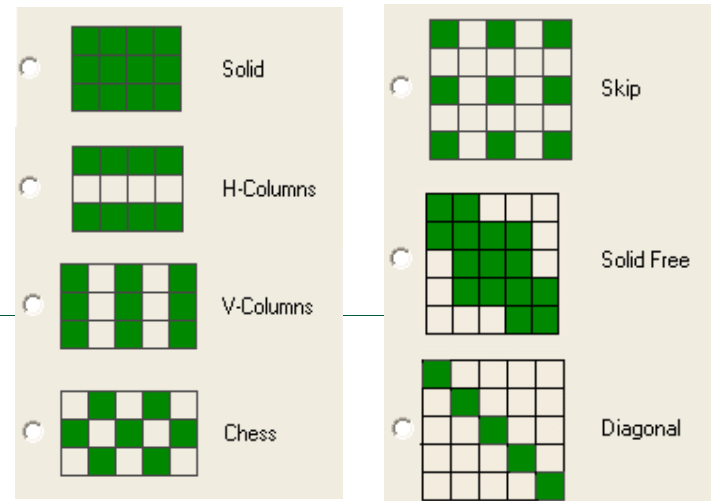
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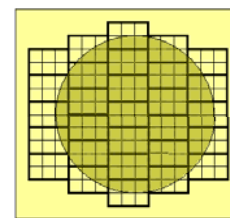
16

# MSO TOUCHDOWN OPTIONS

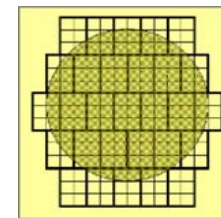
- Multi site shape options:



- Touchdown shifting algorithms:



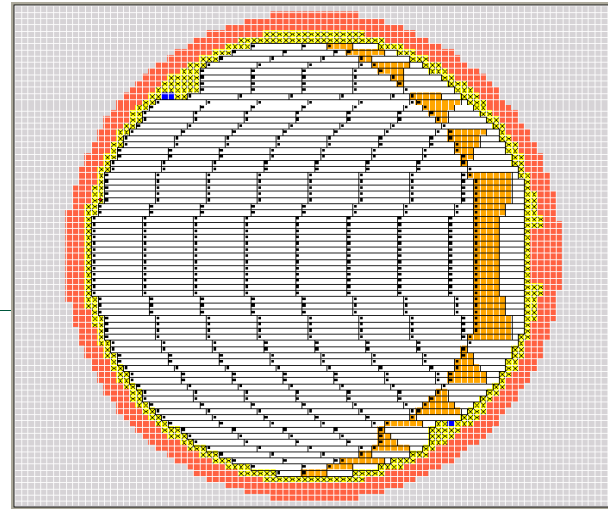
COL shift



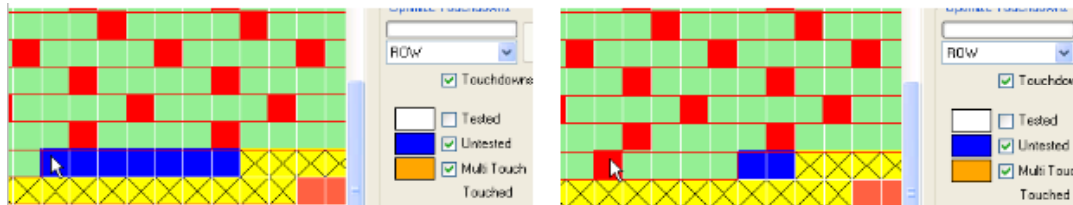
ROW shift

# MSO TOUCHDOWN OPTIONS

- Untouchable die zones  
(=> overlappings/untested)



- Manual touchdown edit



# MSO RESULT DATA

## Touchdown data

Optimize Touchdowns

Analyze

ROW\_ADV ▼

☒ Touchdowns 38  
max. 40

☐ Tested 3.823

☐ Untested 0

☐ Multi Touch 0

Touched 4.864

Multi Site Factor 100,61  
79%

## Stepping path data

Optimize Path

Analyze

Step/Time ▼

☒ ShowPath

☐ Show TD Numbers

Step Route Total (mm) 1098  
X/Y 612 / 796

Step Time Total (sec.) 10



# *MSO*

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## *Feature Overview*

Stepping path algorithms



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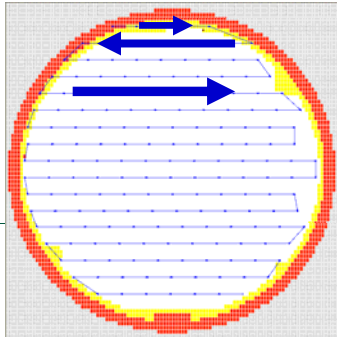
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20

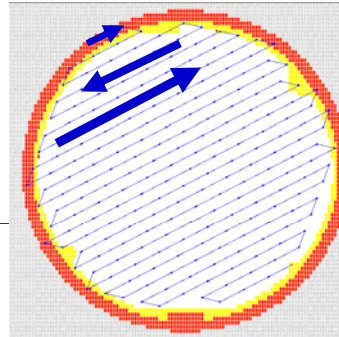


# MSO STEPPING OPTIONS

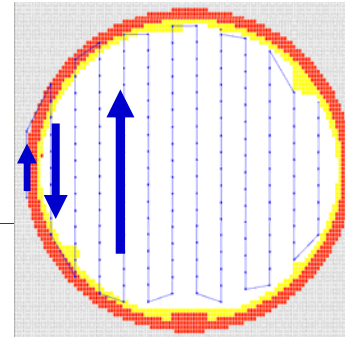
- Meander stepping options



Meander

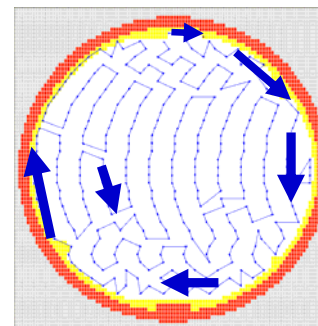


Meander\_shift  
(North-East shift)



Meander\_shift  
(Column shift)

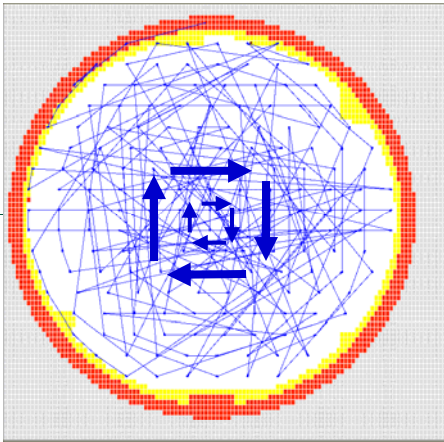
- Time optimized stepping



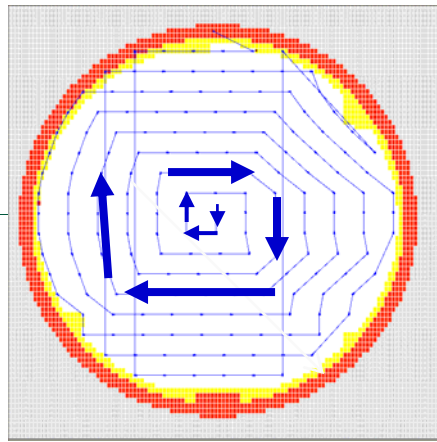
Shortest way

# MSO STEPPING OPTIONS

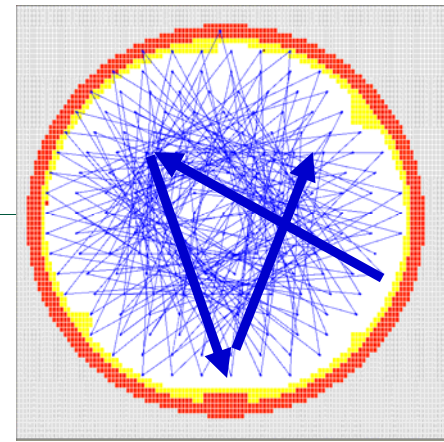
## ■ Hot probing optimizations \*



Radial



Spiral



Longest way

Optimized stepping for more uniform heat expansion of probe card, tester interface and prober head plate.

\* BLEYL, et al, SWTW 2011



# MEMS wafer test

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:: a multi site quest ::



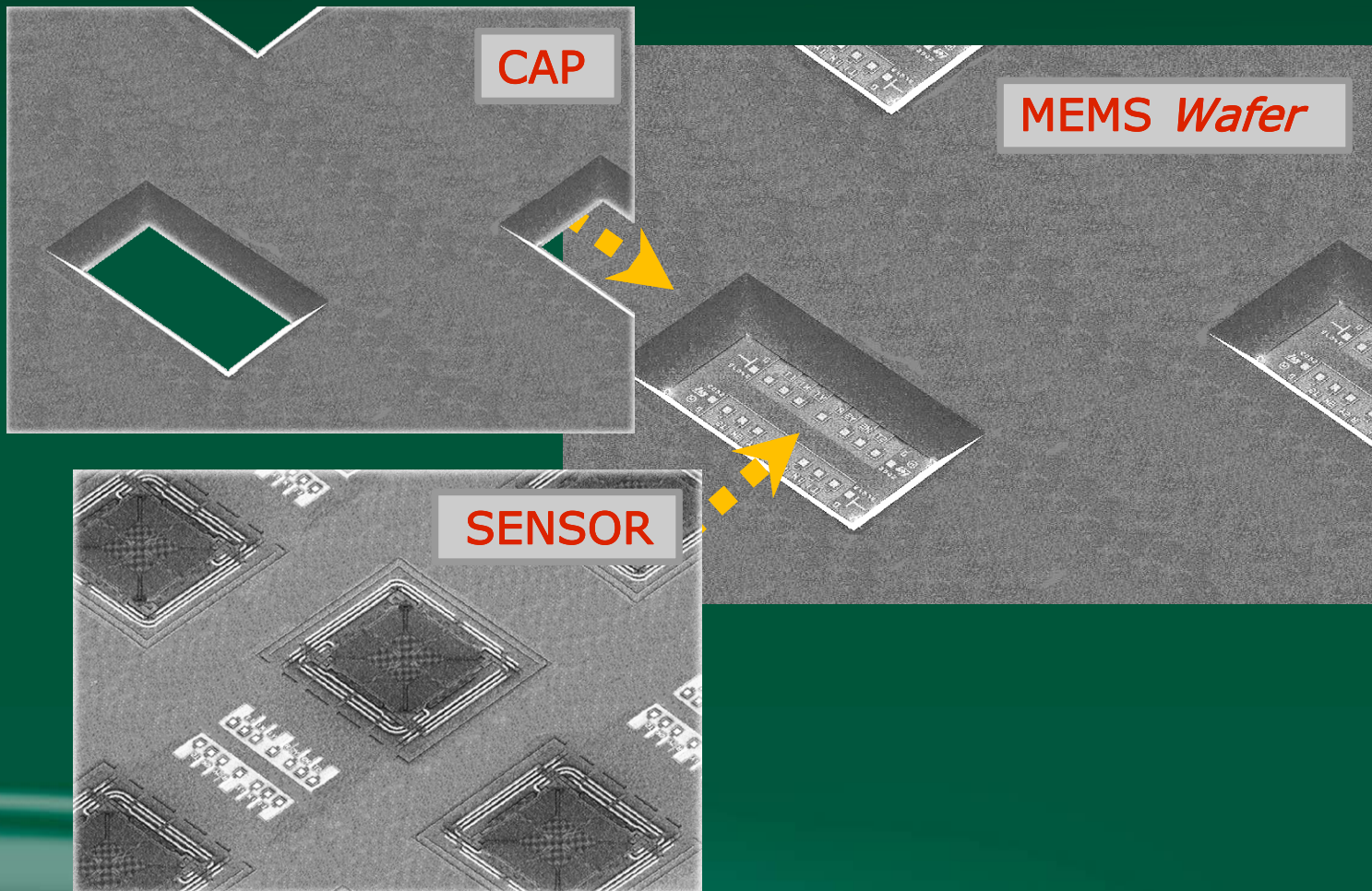
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23

# MEMS Waferprocess

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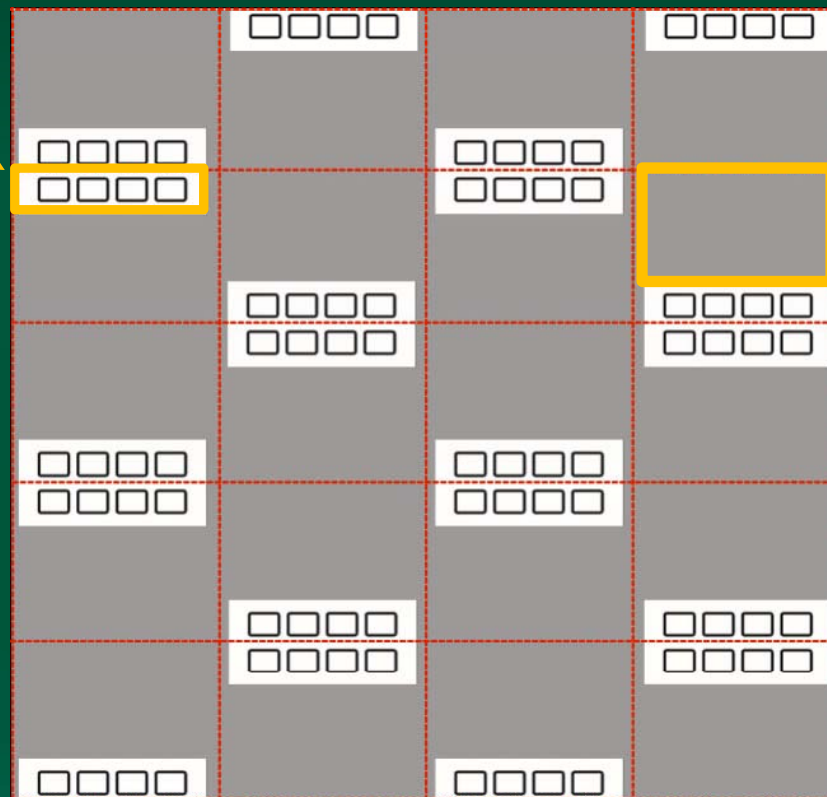
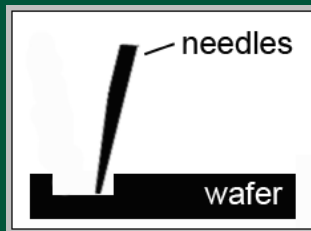
24



# MEMS chip structure

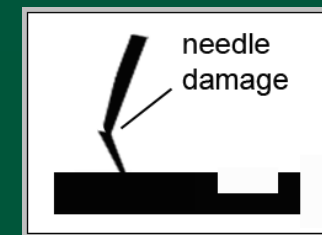
## SENSOR test pads

- electrical testing
- probecard needles contact area

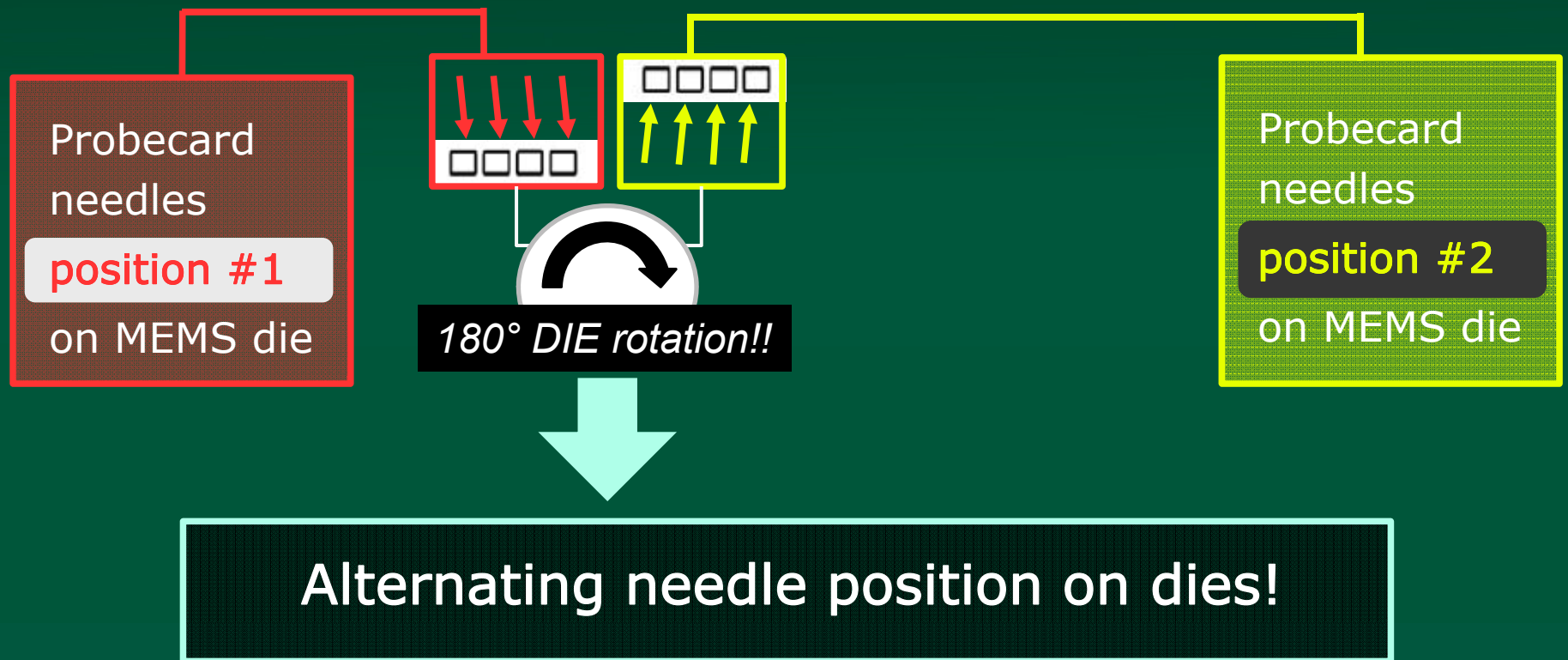


## CAP wafer surface

- NO-contact area for probecard needles



# MEMS test pads





# MEMS wafer test

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## Single Site probing

*Example*



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27

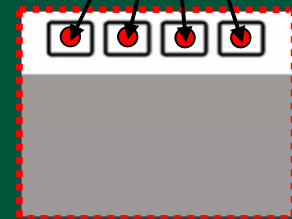
# MEMS single site test

Example

Needles contacting  
upper sensor pads

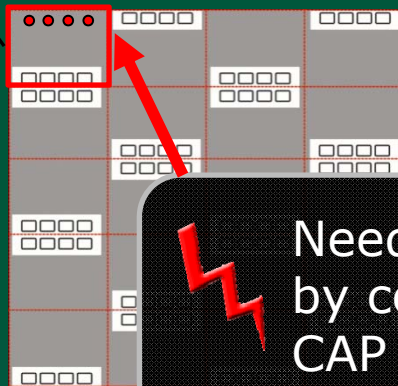


Probecard layout



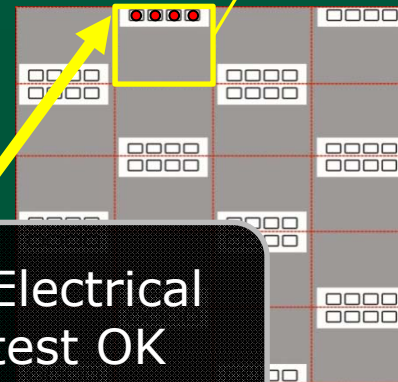
MEMS die

Chip type a



Needle damage  
by collision with  
CAP surface

Chip type b



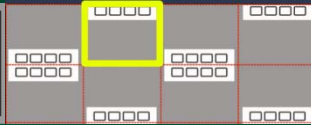
Electrical  
test OK



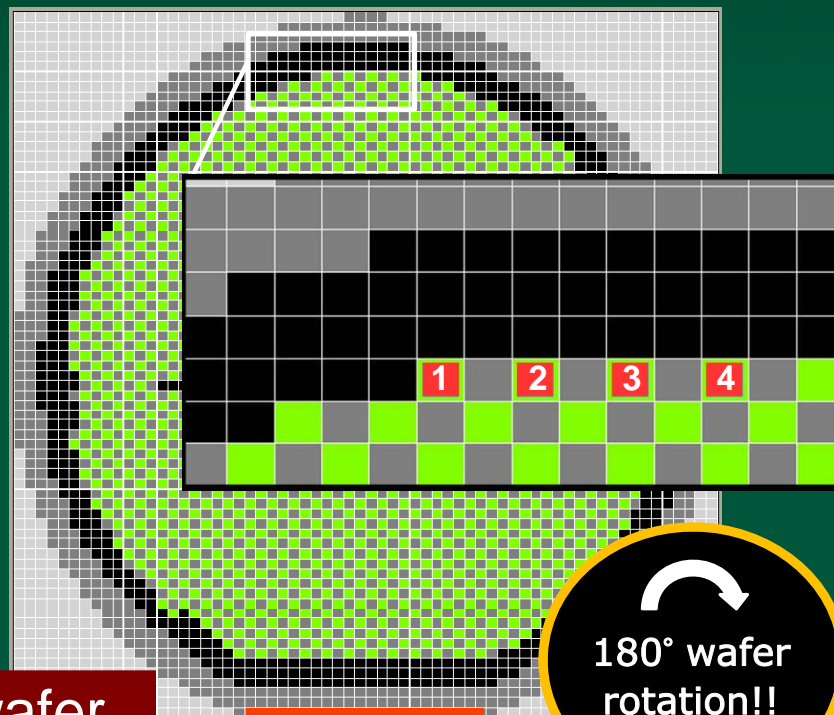
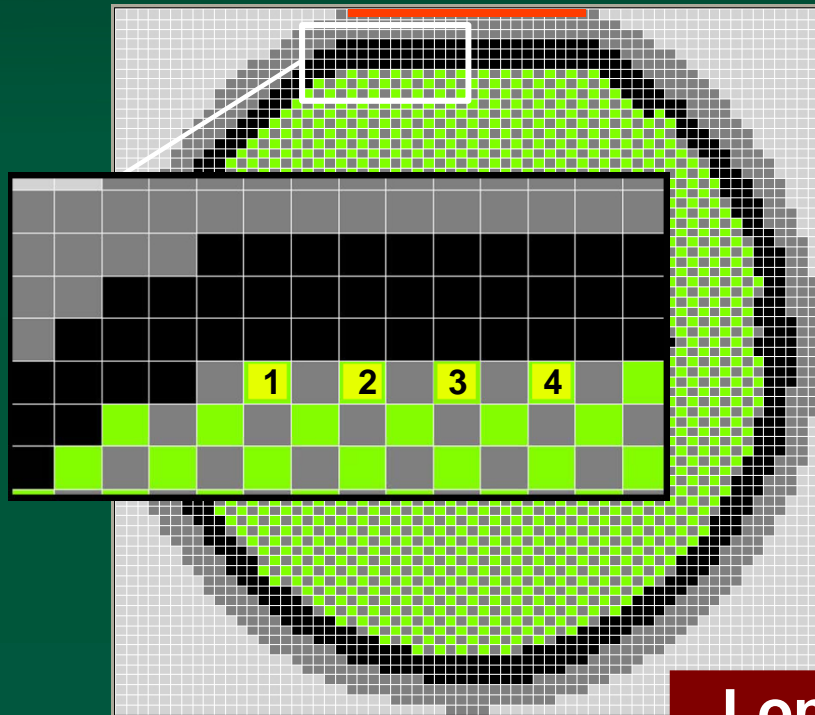
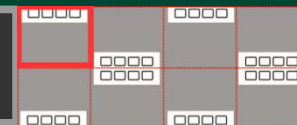
# MEMS single site test



1. Test



2. Test



**Long wafer  
process time !**

180° wafer  
rotation!!



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29

# MEMS Wafertest

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**Why not process  
MEMS wafer within  
ONE  
test cycle ?**



# MEMS Wafertest

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## Multi Site Probing !!!



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31

# MEMS multi site test

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What are the potential **blocking  
points**  
for multi site testing  
MEMS devices?





# MEMS multi site test

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## Blocking points?

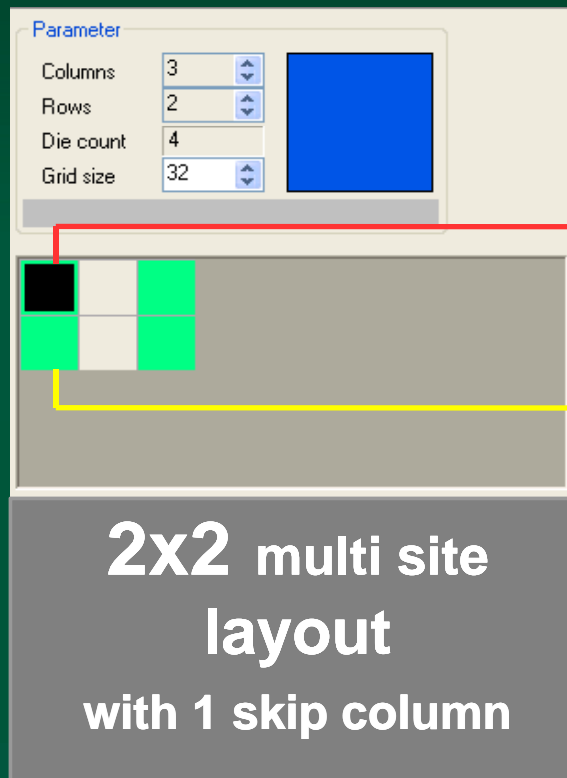
✱ **Probecard site needles touching the CAP Wafer surface (causing damage)**

- ➔ Needles touching CAP area of die
- ➔ Needles touching wafer EDGE area

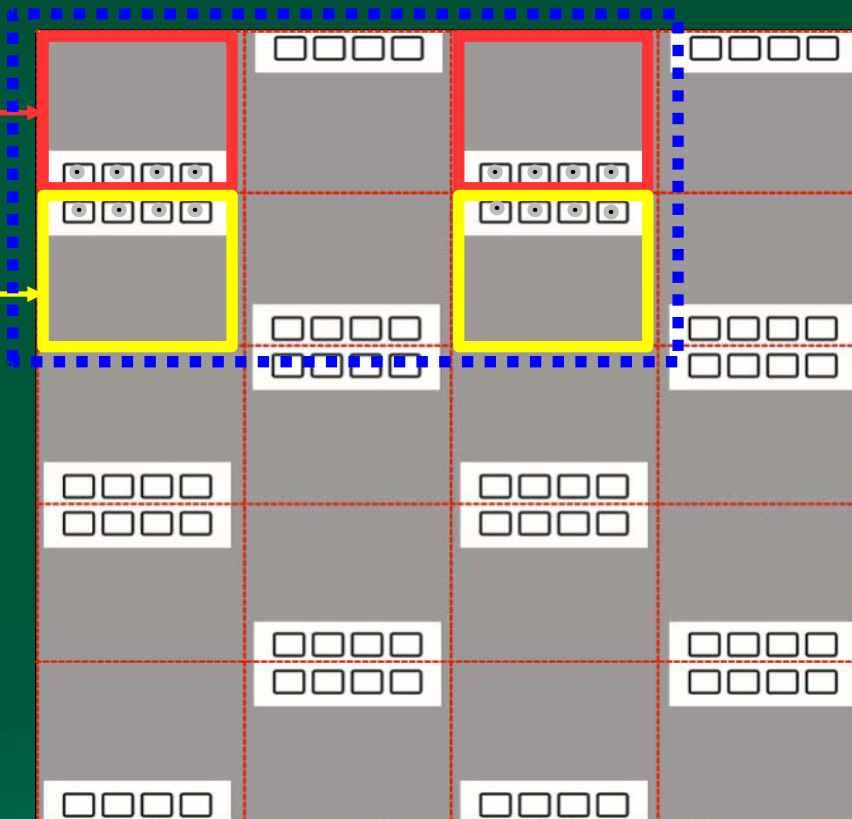


# MEMS multi site test

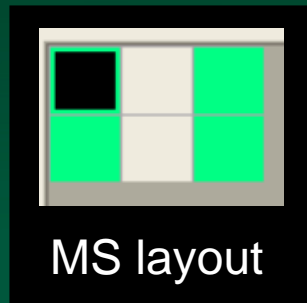
## Example



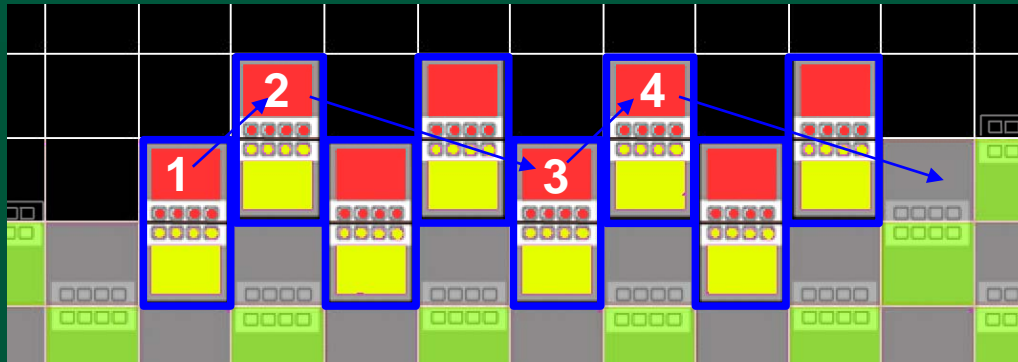
### One touchdown



# MEMS multi site test



First touchdown



Special stepping  
algorithm for MEMS  
to avoid CAP contact

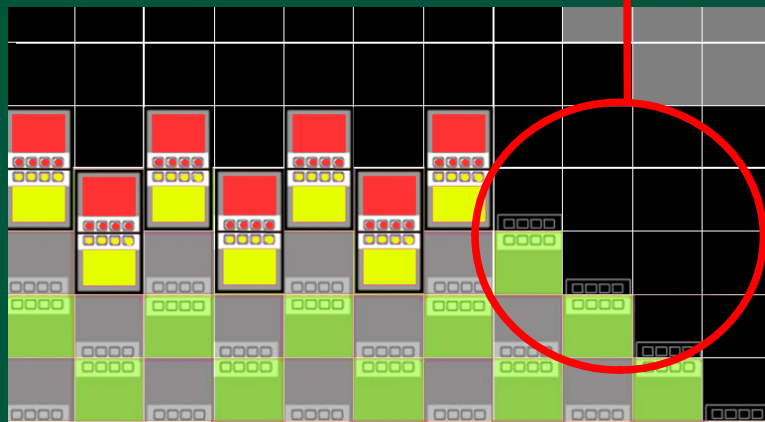


MEMS device can be tested in ONE probing cycle!



# MEMS multi site test

**WAFER EDGE ZONE !!!**



**Needle damage!**



***SOLUTION*** → see next page



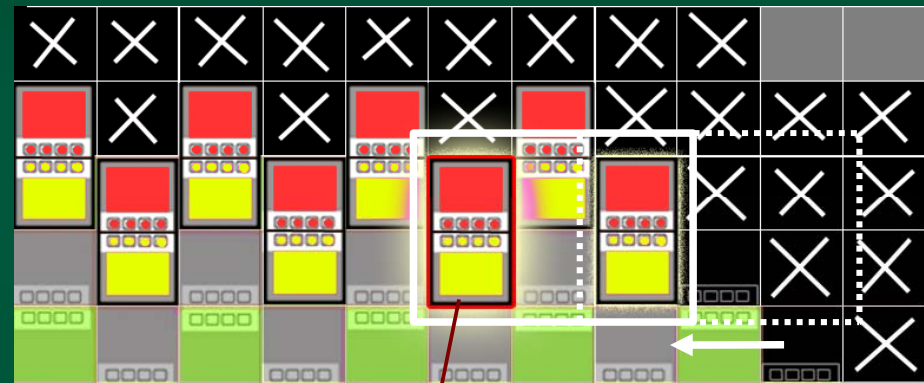
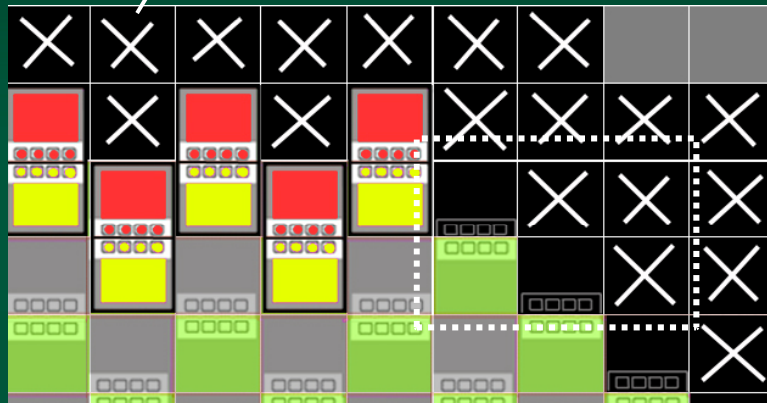
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# MEMS multi site test

UNTOUCHABLE DIES



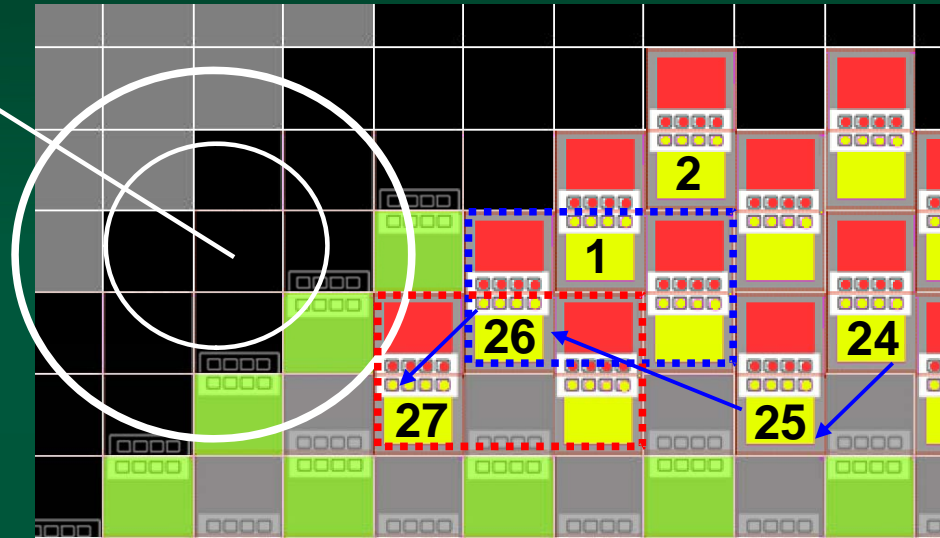
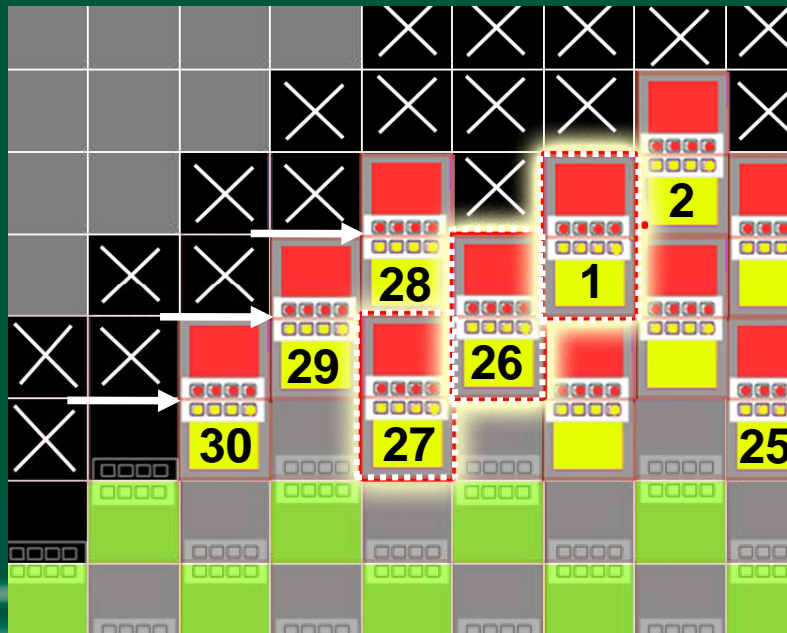
Automatic touchdown repositioning  
→ creating 2 double touched dies!

=> Safe needle contacting via untouchable die zones !



# MEMS multi site test

WAFER EDGE ZONE !!!



Automatic touchdown  
repositioning → creating 6  
double touched dies here !



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# MEMS multi site test

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## Blocking points?

- ✳ Contact of probecard and  
CAP/EDGE wafer can  
be avoided by intelligent  
touchdown positioning!





# SUMMARY

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- MSO optimized touchdown positioning increases prober test efficiency and saves process time!
- MSO is able to provide special stepping path algorithms, which are useful to:
  - compensate probing-related problems such as needle expansions during HOT PROBING (already used by customer NXP)
  - handle wafer-specific features and optimize the test process (e.g. MEMS / future prospect)



# ACKNOWLEDGEMENTS

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- Thomas Schäfer (SPA GmbH)
- Roland Fredriksen (TRF GmbH)



**THANK YOU  
VERY MUCH FOR YOUR  
ATTENTION!**

**Questions?**



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42

# CONTACT...



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43