

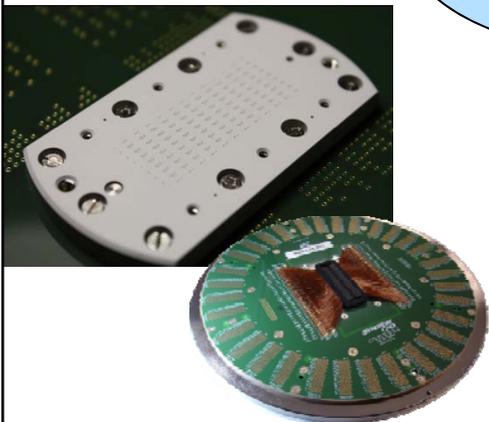
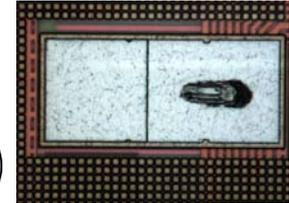
In partnership with ST Microelectronics Rousset (EWS Probe card Group)
 Synergie Cad Probe developed a concept to improve probe mark quality
 and reduce cost of wafer level test



High
 parallelis
 m
 High pin
 count

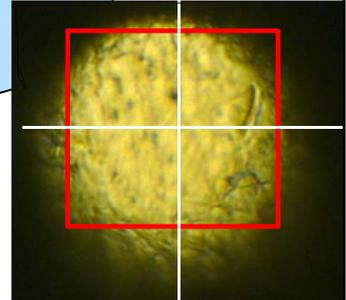
For all
 technologies
 Vertical
 Cantilever
 Mems

Very fine
 pitch/pad size
 on new product



ST Microelectronics
 Our Need is
 certification of X/Y
 alignment in
 production

DIAMETER



Quick test
 time

Data
 collection

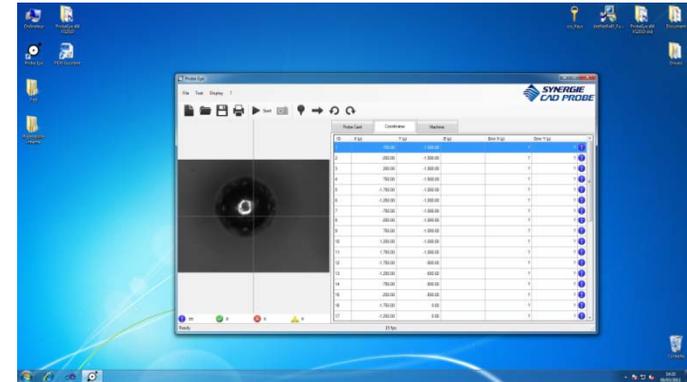
TEST REPORT

Ref.: JEPQZL 130% Probe Card Number: Synergie CAD PROBE
 Customer: Date: 29/03/2011 19:31:00
 Method: 1

Pass: 433 Failed: 15 Not Found: 0 Warning (10s): 0 Total: 448

ID	Date	Pad Size (x)	X (µ)	Y (µ)	X error (µ)	Y error (µ)	Q (µ)	Q (µ)	Q (µ)
1	1	80x80	-1552.00	1547.00	63.91	-11.21	8.50	1	1
2	0	80x80	-4468.00	13871.00	-0.30	-8.84	25.00	1	1
3	0	80x80	-4468.00	13988.00	6.31	-8.53	24.17	1	1
4	0	80x80	-4871.00	15121.00	-2.80	-8.22	25.80	1	1
5	0	80x80	-4871.00	15212.00	-5.84	-4.34	24.17	1	1
6	0	80x80	-4871.00	15823.00	0.17	-8.18	24.17	1	1
7	0	80x80	-3588.00	15488.00	14.48	-7.17	19.17	1	1
8	1	80x80	-135.00	13887.00	8.30	-4.81	25.00	1	1
9	1	80x80	-1247.00	13871.00	8.34	-4.34	25.83	1	1
10	1	80x80	-1247.00	13988.00	6.76	-7.98	23.33	1	1
11	1	80x80	-1218.00	14874.00	6.30	-4.88	25.00	1	1
12	1	80x80	-1048.00	15212.00	8.44	-1.48	22.00	1	1
13	1	80x80	-1048.00	15823.00	8.04	-4.42	23.33	1	1
14	1	80x80	-187.00	15488.00	13.28	-6.88	21.67	1	1
15	0	80x80	1248.00	13887.00	-10.80	-7.80	23.33	1	1
16	0	80x80	135.00	13871.00	-13.77	-4.42	23.33	1	1
17	0	80x80	135.00	13988.00	-18.38	-5.32	25.00	1	1

SAMX.9®

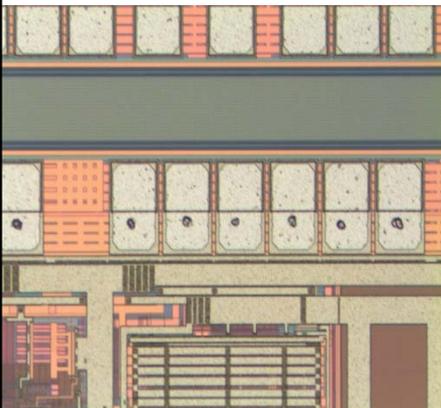
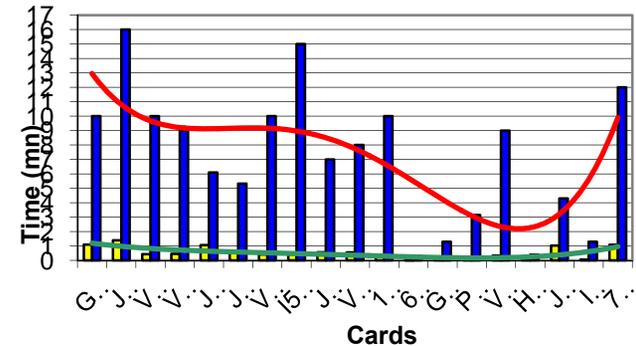


Accepted Solution

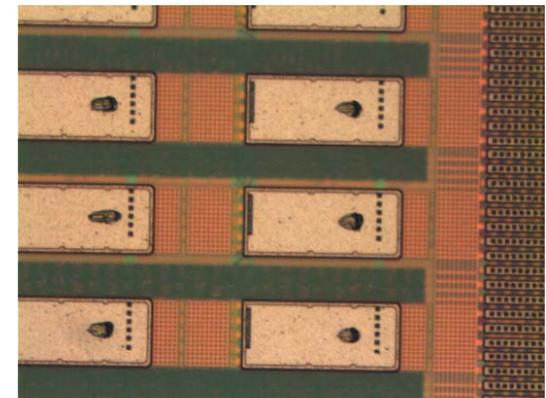
- ❖ Test of Probe Alignment (X/Y)
- ❖ Test of Probe Diameter
- ❖ Precise and Repetitive Measurements
- ❖ Speed of Setup (≈ 1 minute)
- ❖ Short Test Time (1000 probes ≈ 3 minutes)
- ❖ Easy to Handle
- ❖ Operator Friendly (Touch-Screen with Windows 7®)
- ❖ Test of all Probe Card Technologies
- ❖ Tested and Approved by Major Semiconductor Fabs
- ❖ Competitive cost, One Year Return on Investment

CAPITAL GAIN IN PRODUCTION

- Cell test Up time improvement
 - Setup optimization/start efficiency
 - Setup time reduction
- Low MMTR probe card
 - Diagnostic less than 5'



- Quality improvement
 - Check all probe cards before use :
 - Probe mark position and integrity



Your Contacts



Didier Baumann: didier.baumann@st.com

Robert Diperi: robert.diperi@st.com



info@synergie-cad-probe.fr



sales@jpkummer.fr